OMG Systems Modeling Language (OMG SysML™), V1.0

OMG Available Specification

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Preface

About the Object Management Group

OMG

Founded in 1989, the Object Management Group, Inc. (OMG) is an open membership, not-for-profit computer industry standards consortium that produces and maintains computer industry specifications for interoperable, portable and reusable enterprise applications in distributed, heterogeneous environments. Membership includes Information Technology vendors, end users, government agencies and academia.

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- UML
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- Specialized CORBA specifications
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OMG SysML™ Roadmap

Requirements for SysML were originally specified by:

ad/2003-03-41 (UML for Systems Engineering RFP)

The source documents for this specification include:

Alpha: ad/2006-03-01 (submission)
       ad/2006-04-07 (errata)
       ad/2006-03-04 (glossary)

Associated Schema files: ad/2006-03-02 (XMI)

The Finalization Task Force (FTF) process generated the following documents:

Beta 1: ptc/2006-05-04 (a.k.a. final adopted specification)

Beta 2: ptc/2007-03-03, ptc/2007-03-04 (a.k.a. convenience document, with and without change bars)
       ptc/2007-03-05 (XMI)
       ptc/2007-03-09 (Annex E - Requirements Traceability)
       ptc/2007-03-19 (FTF Report - full record of FTF votes and issue resolutions)

Associated Schema files for this specification, at http://www.omg.org/spec/SysML/20070901/, include the following files:

SysML-profile.xmi XMI 2.1 serialization of the SysML Profile
<table>
<thead>
<tr>
<th>File Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Activities-model.xmi</td>
<td>XMI 2.1 serialization of the Activities model library</td>
</tr>
<tr>
<td>Blocks-model.xmi</td>
<td>XMI 2.1 serialization of the Blocks model library</td>
</tr>
<tr>
<td>UML4SysML-metamodel.xmi</td>
<td>XMI 2.1 serialization of the merged UML4SysML subset of UML 2 (used to define the SysML Profile)</td>
</tr>
</tbody>
</table>
Part I - Introduction

This specification defines a general-purpose modeling language for systems engineering applications, called the OMG Systems Modeling Language (OMG SysML™). Throughout the rest of the specification, the language will be referred to as SysML.

SysML supports the specification, analysis, design, and verification and validation of a broad range of complex systems. These systems may include hardware, software, information, processes, personnel, and facilities.

The origins of the SysML initiative can be traced to a strategic decision by the International Council on Systems Engineering’s (INCOSE) Model Driven Systems Design workgroup in January 2001 to customize the Unified Modeling Language (UML) for systems engineering applications. This resulted in a collaborative effort between INCOSE and the Object Management Group (OMG), which maintains the UML specification, to jointly charter the OMG Systems Engineering Domain Special Interest Group (SE DSIG) in July 2001. The SE DSIG, with support from INCOSE and the ISO AP 233 workgroup, developed the requirements for the modeling language, which were subsequently issued by the OMG as part of the UML for Systems Engineering Request for Proposal (UML for SE RFP; OMG document ad/2003-03-41) in March 2003.

Currently it is common practice for systems engineers to use a wide range of modeling languages, tools and techniques on large systems projects. In a manner similar to how UML unified the modeling languages used in the software industry, SysML is intended to unify the diverse modeling languages currently used by systems engineers.

SysML reuses a subset of UML 2 and provides additional extensions needed to address the requirements in the UML for SE RFP. SysML uses the UML 2 extension mechanisms as further elaborated in Chapter 17, “Profiles & Model Libraries” of this specification as the primary mechanism to specify the extensions to UML 2.

Since SysML uses UML 2 as its foundation, systems engineers modeling with SysML and software engineers modeling with UML 2 will be able to collaborate on models of software-intensive systems. This will improve communication among the various stakeholders who participate in the systems development process and promote interoperability among modeling tools. It is anticipated that SysML will be customized to model domain-specific applications, such as automotive, aerospace, communications, and information systems.
1 Scope

The purpose of this document is to specify the Systems Modeling Language (SysML), a new general-purpose modeling language for systems engineering that satisfies the requirements of the UML for Systems Engineering RFP. Its intent is to specify the language so that systems engineering modelers may learn to apply and use SysML, modeling tool vendors may implement and support SysML, and both can provide feedback to improve future versions.

SysML reuses a subset of UML 2 and provides additional extensions to satisfy the requirements of the language. This specification documents the language architecture in terms of the parts of UML 2 that are reused and the extensions to UML 2. The specification includes the concrete syntax (notation) for the complete language and specifies the extensions to UML 2. The reusable portion of the UML 2 specification is not included directly in the specification but is included by reference. The specification also provides examples of how the language can be used to solve common systems engineering problems.

SysML is designed to provide simple but powerful constructs for modeling a wide range of systems engineering problems. It is particularly effective in specifying requirements, structure, behavior, and allocations and constraints on system properties to support engineering analysis. The language is intended to support multiple processes and methods such as structured, object-oriented, and others, but each methodology may impose additional constraints on how a construct or diagram kind may be used. The initial version of the language supports most, but not all of the requirements of the UML for SE RFP, as shown in the Requirements Traceability Matrix referenced by Annex E. These gaps are intended to be addressed in future versions of SysML as indicated in the matrix.

SysML is intended to be supported by two evolving interoperability standards: the OMG XMI 2.1 model interchange standard for UML 2 modeling tools and the ISO 10303-233 data interchange standard for systems engineering tools. While the details of this alignment are beyond the scope of this specification, overviews of the alignment approach and relevant references are furnished in Annex D.

The following sections provide background information about this specification. Instructions to either systems engineers and vendors who read this specification are provided in Section 3.2, “How to Read this Specification.” The main body of this document (Parts II-IV) describes the normative technical content of the specification. The annexes include additional information to aid in understanding and implementation of this specification.

NOTE: Refer to Chapter 5 for detailed Compliance information.

2 Normative References

The following normative documents contain provisions which, through reference in this text, constitute provisions of this specification. Refer to the OMG site for subsequent amendments to, or revisions of any of these publications.

3 Additional Information

3.1 Relationships to Other Standards

SysML is defined as an extension of the OMG UML 2 Superstructure specification. See Chapter 2, “Normative References,” for the current version of the UML 2 Superstructure specification.

SysML is intended to be supported by two evolving interoperability standards including the OMG XMI 2.1 model interchange standard for UML 2 modeling tools and the ISO 10303 STEP AP233 data interchange standard for systems engineering tools. Overviews of the approach to model interchange and relevant references are included in Annex D.

SysML supports the OMG’s Model Driven Architecture (MDA) initiative by its reuse of the UML and related standards.

3.2 How to Read this Specification

This specification is intended to be read by systems engineers so that they may learn and apply SysML, and by modeling tool vendors so that they may implement and support SysML. As background, all readers are encouraged to first read Part I, “Introduction.”

After reading the introduction, readers should be prepared to explore the user-level constructs defined in the next three parts: Part II, “Structural Constructs,” Part III, “Behavioral Constructs,” and Part IV, “Crosscutting Constructs.” Systems engineers should read the Overview, Diagram Elements, and Usage Examples sections in each chapter, and explore the UML Extensions as they see fit. Modeling tool vendors should read all sections. In addition, systems engineers and vendors should read Annex B, “Sample Problem” to understand how the language is applied to an example, and Annex E, “Requirements Traceability” to understand how the requirements in the UML for SE RFP are satisfied by this specification.

Although the chapters are organized into logical groupings that can be read sequentially, this specification can be used for reference and may be read in a non-sequential manner.

3.3 Acknowledgments

The following companies and organizations submitted or supported parts of this specification:

Industry

- American Systems Corporation
- BAE SYSTEMS
- Boeing
- Deere & Company
- EADS Astrium
- Eurostep
- Israel Aircraft Industries
- Lockheed Martin Corporation
- Motorola
- Northrop Grumman
- oose Innovative Informatik GmbH
- PivotPoint Technology

OMG SysML™, Version 1.0
• Raytheon
• THALES

US Government
• NASA/Jet Propulsion Laboratory
• National Institute of Standards and Technology (NIST)
• DoD/Office of the Secretary of Defense (OSD)

Vendors
• ARTiSAN Software Tools
• Ceira Technologies
• EmbeddedPlus Engineering
• Gentleware
• IBM
• I-Logix
• Mentor Graphics
• Telelogic
• Structured Software Systems Limited
• Sparx Systems
• Vitech

Academia
• Georgia Institute of Technology

Liaisons
• Consultative Committee for Space Data Systems (CCSDS)
• Embedded Architecture and Software Technologies (EAST)
• International Council on Systems Engineering (INCOSE)
• ISO STEP AP233
• Systems Level Design Language (SLDL) and Rosetta

The following persons were members of the team that designed and wrote this specification: Vincent Arnould, Laurent Balmelli, Ian Bailey, James Baker, Cory Bialowas, Conrad Bock, Carolyn Boettcher, Roger Burkhart, Murray Cantor, Bruce Douglass, Harald Eisenmann, Anders Ek, Brenda Ellis, Marilyn Escue, Sanford Friedenthal, Eran Gery, Hal Hamilton, Dwayne Hardy, James Hummel, Cris Kobryn, Michael Latta, John Low, Robert Long, Kumar Marimuthu, Alan Moore, Veronique Normand, Salah Obeid, Eldad Palachi, David Price, Bran Selic, Chris Sibbald, Joseph Skipper, Rick Steiner, Robert Thompson, Jim U’Ren, Tim Weilkiens, Thomas Weigert, and Brian Willard.

In addition, the following persons contributed valuable ideas and feedback that significantly improved the content and the quality of this specification: Perry Alexander, Michael Chonoles, Mike Dickerson, Orazio Gurrieri, Julian Johnson, Jim Long, Henrik Lönn, Stephen Mellor, Dave Oliver, Jim Schier, Matthias Weber, Peter Shames, and the Georgia Institute of Technology research team including Manas Bajaj, Injoong Kim, Chris Paredis, Russell Peak, and Diego Tamburini. The SysML team also wants to acknowledge Pavel Hruby and his contribution by providing the Visio stencil for UML 2 that was adapted for most of the figures throughout this specification.
4 Language Architecture

SysML reuses a subset of UML 2 and provides additional extensions needed to address requirements in the UML for Systems Engineering RFP. This specification documents the language architecture in terms of the parts of UML 2 that are reused and the extensions to UML 2. This chapter explains design principles and how they are applied to define the SysML language architecture.

In order to visualize the relationship between the UML and SysML languages, consider the Venn diagram shown in Figure 4.1, where the sets of language constructs that comprise the UML and SysML languages are shown as the circles marked “UML” and “SysML,” respectively. The intersection of the two circles, shown by the region marked “UML reused by SysML,” indicates the UML modeling constructs that SysML reuses. The compliance matrix in Table 4.1 below specifies the UML packages that a SysML tool must reuse in order to implement SysML.

The region marked “SysML extensions to UML” in Figure 4.1 indicates the new modeling constructs defined for SysML which have no counterparts in UML, or which replace UML constructs. Note that there is also a part of UML 2 that is not required to implement SysML, which is shown by the region marked “UML not required by SysML.”

![Figure 4.1 - Overview of SysML/UML interrelationship](image)

4.1 Design Principles

The fundamental design principles for SysML are:

- Requirements-driven. SysML is intended to satisfy the requirements of the UML for SE RFP.
• UML reuse. SysML reuses UML wherever practical to satisfy the requirements of the RFP, and when modifications are required, they are done in a manner that strives to minimize changes to the underlying language. Consequently, SysML is intended to be relatively easy to implement for vendors who support UML 2.

• UML extensions. SysML extends UML as needed to satisfy the requirements of the RFP. The primary extension mechanism is the UML 2 profile mechanism as further refined in Chapter 17, “Profiles & Model Libraries” of this specification.

• Partitioning. The package is the basic unit of partitioning in this specification. The packages partition the model elements into logical groupings that minimize circular dependencies among them.

• Layering. SysML packages are specified as an extension layer to the UML metamodel.

• Interoperability. SysML inherits the XMI interchange capability from UML. SysML is also intended to be supported by the ISO 10303-233 data interchange standard to support interoperability among other engineering tools.

4.2 Architecture

The SysML language reuses and extends many of the packages from UML. As shown in Figure 4.2, the set of UML metaclasses to be reused are merged into a single metamodel package, UML4SysML. The detailed list of packages that are merged is shown in Table 4.1. Some UML packages are not being reused, since they are not considered essential for systems engineering applications to meet the requirements of the UML for SE RFP.

Figure 4.2 - SysML Extension of UML

The SysML profile specifies the extensions to UML. It references the UML4SysML package, thus importing all the metaclasses into SysML that are either reused as-is from UML or extended in SysML. The semantics of UML profiles ensure that when a user model “strictly” applies the SysML profile, only the UML metaclasses referenced by SysML are available to the user of that model. If the profile is not “strictly” applied, then additional UML metaclasses which were not explicitly referenced may also be available. The SysML profile also imports the Standard Profile L1 from UML to make use of its stereotypes.
<table>
<thead>
<tr>
<th>UML Language Unit</th>
<th>UML Package</th>
<th>Metaclasses</th>
</tr>
</thead>
<tbody>
<tr>
<td>Actions</td>
<td>Actions::BasicActions</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Actions::StructuredActions</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Actions::IntermediateActions</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Actions::CompleteActions</td>
<td>All</td>
</tr>
<tr>
<td>Activities</td>
<td>Activities::FundamentalActivities</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Activities::BasicActivities</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Activities::IntermediateActivities</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Activities::StructuredActivities</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Activities::CompleteActivities</td>
<td>All</td>
</tr>
<tr>
<td>Classes</td>
<td>Classes::Kernel</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Classes::Dependencies</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Classes::Interfaces</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Classes::PowerTypes</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Classes::AssociationClasses</td>
<td>All</td>
</tr>
<tr>
<td>General Behavior</td>
<td>CommonBehaviors::BasicBehaviors</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>CommonBehaviors::SimpleTime</td>
<td>All</td>
</tr>
<tr>
<td>Information Flows</td>
<td>AuxiliaryConstructs::InformationFlows</td>
<td>All</td>
</tr>
<tr>
<td>Interactions</td>
<td>Interactions::BasicInteractions</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>Interactions::Fragments</td>
<td>All</td>
</tr>
<tr>
<td>Models</td>
<td>AuxiliaryConstructs::Models</td>
<td>All</td>
</tr>
<tr>
<td>Profiles</td>
<td>AuxiliaryConstructs::Profiles</td>
<td>All</td>
</tr>
<tr>
<td>State Machines</td>
<td>StateMachines::BehaviorStateMachines</td>
<td>All</td>
</tr>
<tr>
<td>Structures</td>
<td>CompositeStructures::InternalStructures</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>CompositeStructures::StructuredClasses</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>CompositeStructures::InvocationActions</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>CompositeStructures::Ports</td>
<td>All</td>
</tr>
<tr>
<td></td>
<td>CompositeStructures::StructuredActivities</td>
<td>All</td>
</tr>
<tr>
<td>Use Cases</td>
<td>UseCases</td>
<td>All</td>
</tr>
</tbody>
</table>
As previously stated, the design approach for SysML is to reuse a subset of UML and create extensions to support the specific concepts needed to satisfy the requirements in the UML for SE RFP. The SysML package structure shown in Figure 4.3 contains a set of packages that correspond to concept areas in SysML that have been extended. The reusable portion of UML that has not been extended is included by reference to the merged package (UML4SysML), and includes Interactions, State Machines, Use Cases, and Profiles.

The SysML packages extend UML as follows:

- SysML::Model Elements refactors and extends the UML kernel portion of UML classes
- SysML::Blocks reuses structured classes from composite structures
- SysML::ConstraintBlocks extends Blocks to support parametric modeling
- SysML::Ports and Flows extends UML::Ports, UML::InformationFlows and SysML::Blocks
- SysML::Activities extends UML activities
- SysML::Allocations extends UML dependencies
- SysML::Requirements extends UML classes and dependencies

### 4.3 Extension Mechanisms

This specification uses the following mechanisms to define the SysML extensions:

- UML stereotypes
- UML diagram extensions
- Model libraries
SysML stereotypes define new modeling constructs by extending existing UML 2 constructs with new properties and constraints. SysML diagram extensions define new diagram notations that supplement diagram notations reused from UML 2. SysML model libraries describe specialized model elements that are available for reuse. Additional non-normative extensions are included in Annex C: Non-normative Extensions.

The SysML user model is created by instantiating the metaclasses and applying the stereotypes specified in the SysML profile and subclassing the model elements in the SysML model library. Chapter 17, “Profiles & Model Libraries” describes how profiles and model libraries are applied and how they can be used to further extend SysML.

### 4.4 SysML Diagrams

The SysML diagram taxonomy is shown in Figure 4.4. The concrete syntax (notation) for the diagrams along with the corresponding specification of the UML extensions is described in Parts II - IV of this specification. The Diagrams Annex (Annex A) describes generalized features of diagrams, such as their frames and headings.

**Figure 4.4 - SysML Diagram Taxonomy**
5 Compliance

Compliance with SysML requires that the subset of UML required for SysML is implemented, and the extensions to the UML subset required for SysML are implemented. In order to fully comply with SysML, a tool must implement both the concrete syntax (notation) and abstract syntax (metamodel) for the required UML subset and the SysML extensions. The following sections elaborate the definition of compliance for SysML.

5.1 Compliance with UML Subset (UML4SysML)

The subset of UML required for SysML is specified by the UML4SysML package as described in Chapter 4, “Language Architecture.” UML has three compliance levels (L1, L2, L3) that SysML applies to the subset in the UML4SysML package. The levels are:

- **Level 1 (L1)**. This level provides the core UML concepts from the UML kernel and adds language units for use cases, interactions, structures, actions, and activities.
- **Level 2 (L2)**. This level extends the language units already provided in Level 1 and adds language units for state machine modeling and profiles.
- **Level 3 (L3)**. This level represents the complete UML. It extends the language units provided by Level 2 and adds new language units for modeling information flows and for model packaging.

These compliance levels are constructed in the same fashion as for UML and readers are referred to the UML Superstructure specification ([http://www.omg.org/cgi-bin/doc?formal/2007-02-03](http://www.omg.org/cgi-bin/doc?formal/2007-02-03)) for further information.

5.1.1 Compliance Level Contents

The following tables identify the metamodel packages whose contents contribute to the individual compliance levels. The metaclasses in each level are included in addition to those that are defined in lower levels (Level (N) includes all the packages supported by Level (N-1)).

**Table 5.1 - Metamodel packages added in Level 1**

<table>
<thead>
<tr>
<th>Language Unit</th>
<th>Metamodel Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>Actions</td>
<td>Actions::BasicActions</td>
</tr>
<tr>
<td>Activities</td>
<td>Activities::FundamentalActivities</td>
</tr>
<tr>
<td></td>
<td>Activities::BasicActivities</td>
</tr>
<tr>
<td>Classes</td>
<td>Classes::Kernel</td>
</tr>
<tr>
<td></td>
<td>Classes::Dependencies</td>
</tr>
<tr>
<td></td>
<td>Classes::Interfaces</td>
</tr>
<tr>
<td>General Behavior</td>
<td>CommonBehaviors::BasicBehaviors</td>
</tr>
<tr>
<td>Structures</td>
<td>CompositeStructure::InternalStructures</td>
</tr>
<tr>
<td>Interactions</td>
<td>Interactions::BasicInteractions</td>
</tr>
<tr>
<td>UseCases</td>
<td>UseCases</td>
</tr>
</tbody>
</table>
5.2 Compliance with SysML Extensions

In addition to UML, further units of compliance for SysML are the subpackages of the SysML profile. The list of these packages is provided in Chapter 4, Language Architecture.

For an implementation of SysML to comply with a particular SysML package, it must also comply with any packages on which the particular package depends. For SysML, this includes not only other SysML packages, but the UML4SysML compliance level that introduces all the metaclasses extended by stereotypes in that package. The following table identifies the level of UML4SysML on which each SysML package depends. Note that some of the SysML packages, such as ModelElements, have two compliance points. This occurs when different stereotypes within the package extend metaclasses that are at more than one UML compliance level.
5.3 Meaning of Compliance

An implementation of SysML must comply with both the subset of UML4SysML and the SysML extensions as described above. The meaning of compliance in SysML is based on the UML definition of compliance, excluding diagram interchange. (Note that diagram interchange is different from model interchange which is included in SysML—refer to XMI in Annex D, “Model Interchange.”)

Compliance can be defined in terms of the following:

- **Abstract syntax compliance.** For a given compliance level, this entails:
  - compliance with the metaclasses, stereotypes, and model libraries; their structural relationships; and any constraints defined as part of the abstract syntax for that compliance level; and the ability to output models and to read in models based on the XMI schema corresponding to that compliance level.
- **Concrete syntax compliance.** For a given compliance level, this entails:
  - Compliance to the notation defined in the “Diagram Elements” tables and diagrams extension sections in each chapter of this specification for those metamodel elements that are defined as part of the merged metamodel or profile subset for that compliance level and, by implication, the diagram types in which those elements may appear.

Compliance for a given level can be expressed as:

- abstract syntax compliance
- concrete syntax compliance
- abstract syntax with concrete syntax compliance

The fullest compliance response is “YES,” which indicates full realization of all language units/stereotypes that are defined for that compliance level. This also implies full realization of all language units/stereotypes in all the levels below that level. “Full realization” for a language unit at a given level means supporting the complete set of modeling concepts defined for that language unit at that level. A compliance response of “PARTIAL” indicates partial realization and requires a feature support statement detailing which concepts are supported. These statements should reference either the language unit and metaclass, or profile package and stereotype for abstract syntax, or a diagram element for concrete syntax (the diagram elements in SysML are given unique names to allow unambiguous references). Finally, a response of “NO” indicates that none of the language units/stereotypes in this compliance point is realized.

<table>
<thead>
<tr>
<th>SysML Package</th>
<th>UML4SysML Compliance Level</th>
</tr>
</thead>
<tbody>
<tr>
<td>Activities (without Probability)</td>
<td>Level 2</td>
</tr>
<tr>
<td>Activities (with Probability)</td>
<td>Level 3</td>
</tr>
<tr>
<td>Allocations</td>
<td>Level 2</td>
</tr>
<tr>
<td>Blocks</td>
<td>Level 2</td>
</tr>
<tr>
<td>ConstraintBlocks</td>
<td>Level 2</td>
</tr>
<tr>
<td>ModelElements</td>
<td>Level 1</td>
</tr>
<tr>
<td>Ports&amp;Flows (without ItemFlow)</td>
<td>Level 2</td>
</tr>
<tr>
<td>Ports&amp;Flows (with ItemFlow)</td>
<td>Level 3</td>
</tr>
<tr>
<td>Requirements</td>
<td>Level 1</td>
</tr>
</tbody>
</table>
Thus, it is not meaningful to claim compliance to, say, Level 2 without also being compliant with Level 1. A tool that is compliant at a given level must be able to import models from tools that are compliant to lower levels without loss of information.

**Table 5.5 - Example Compliance Statement**

<table>
<thead>
<tr>
<th>Compliance Summary</th>
<th>Abstract Syntax</th>
<th>Concrete Syntax</th>
</tr>
</thead>
<tbody>
<tr>
<td>UML4SysML Level 1</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>UML4SysML Level 2</td>
<td>PARTIAL</td>
<td>YES</td>
</tr>
<tr>
<td>UML4SysML Level 3</td>
<td>NO</td>
<td>NO</td>
</tr>
<tr>
<td>Activities (without Probability)</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>Activities (with Probability)</td>
<td>NO</td>
<td>NO</td>
</tr>
<tr>
<td>Allocations</td>
<td>PARTIAL</td>
<td>PARTIAL</td>
</tr>
<tr>
<td>Blocks</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>ConstraintBlocks</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>ModelElements</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>Ports&amp;Flows (without ItemFlow)</td>
<td>YES</td>
<td>YES</td>
</tr>
<tr>
<td>Ports&amp;Flows (with ItemFlow)</td>
<td>NO</td>
<td>NO</td>
</tr>
<tr>
<td>Requirements</td>
<td>YES</td>
<td>YES</td>
</tr>
</tbody>
</table>

In the case of “PARTIAL” support for a compliance point, in addition to a formal statement of compliance, implementors, and profile designers must also provide feature support statements. These statements clarify which language features are not satisfied in terms of language units and/or individual packages, as well as for less precisely defined dimensions such as semantic variation points.

An example feature support statement is shown in Table 5.6 for an implementation whose compliance statement is given in Table 5.5.

**Table 5.6 - Example feature support statement**

<table>
<thead>
<tr>
<th>Feature Support Statement</th>
<th>Abstract Syntax</th>
<th>Concrete Syntax</th>
<th>Semantics</th>
</tr>
</thead>
<tbody>
<tr>
<td>UML4SysML::Level 2 StateMachines::BehaviorStateMachines</td>
<td>Note (1)</td>
<td>Note(1)</td>
<td>Note (2)</td>
</tr>
<tr>
<td>SysML::Blocks Block</td>
<td>YES</td>
<td>Note (3)</td>
<td></td>
</tr>
</tbody>
</table>

**NOTE(s):**

1. States and state machines are limited to a single region.
   Shallow history pseudostates not supported.

2. FIFO queueing in event pool.

3. Don’t show Blocks::StructuredCompartment notation.
6 Language Formalism

The SysML specification is defined by using UML 2 specification techniques. These techniques are used to achieve the following goals in the specification.

- Correctness
- Precision
- Conciseness
- Consistency
- Understandability

The specification technique used in this specification describes SysML as a UML extension that is defined using stereotypes and model libraries.

6.1 Levels of Formalism

SysML is specified using a combination of UML modeling techniques and precise natural language to balance rigor and understandability. Use of more formal constraints and semantics may be applied in future versions to further increase the precision of the language.

6.2 Chapter Specification Structure

The chapters in Parts II - IV are organized according to the SysML packages as described in the language architecture and selected reusable portions of UML 2 packages. This section provides information about how each chapter is organized.

6.2.1 Overview

This section provides an overview of the SysML modeling constructs defined in the subject package, which are usually associated with one or more SysML diagram types.

6.2.2 Diagram Elements

This section provides tables that summarize the concrete syntax (notation) and abstract syntax references for the graphic nodes and paths associated with the relevant diagram types. The diagram elements tables are intended to include all of the diagrammatic constructs used in SysML. However, they do not represent all the different combinations in which they can be used. The reader should refer to the usage examples in the chapters and the sample problem annex (Annex B) for typical usages of the concrete syntax. General diagram information on the use of diagram frames and headings can be found in the Diagram Annex A.

The diagram elements tables and the additional usage examples fill an important role in defining the scope of SysML. As described in Chapter 4, Language Architecture, SysML imports many entire packages from the UML metamodel, which it then reuses and extends. Only a subset of the entire UML metamodel, however, is required to support the notations included in SysML.
Unless a type of diagram element is shown in some form in one of the SysML diagram elements tables, or in a usage example in one of the normative SysML chapters, it is not considered to be part of the subset of UML included within SysML, even if the UML metamodel packages support additional constructs. For example, SysML imports the entire Dependencies package from UML, but it includes diagram elements for only a subset of the dependency types defined in this package.

6.2.3 UML Extensions

This section specifies the SysML extensions to UML in terms of diagram extensions and semantic extensions. Diagram extensions are included when the concrete syntax uses notation other than the standard stereotype notation as defined in the Profiles and Model Libraries chapter. Semantic extensions consist of stereotype and model library extensions. Stereotype extensions always include the abstract syntax that identifies which metaclasses a stereotype extends. Each stereotype includes a general description with a definition and semantics, along with stereotype properties (attributes), and constraints. The model libraries are defined as subclasses of existing metaclasses.

6.2.4 Usage Examples

This section shows how the SysML modeling constructs can be applied to solve systems engineering problems and is intended to reuse and/or elaborate the sample problem in Annex B.

6.3 Conventions and Typography

In the description of SysML, the following conventions have been used:

- While referring to stereotypes, metaclasses, metaassociations, metaattributes, etc. in the text, the exact names as they appear in the model are used.
- No visibilities are presented in the diagrams, since all elements are public.
- If a section is not applicable, it is not included, except for the top-level sections outlined in Section 6.2, “Chapter Specification Structure” above
- Stereotype, metaclass, and metassociation names: initial embedded capitals are used (e.g., “ModelElement,” “ElementReference”).
- Boolean metaattribute names: always start with “is” (e.g., “isComposite”).
- Enumeration types: always end with “Kind” (e.g., “DependencyKind”).
Part II - Structural Constructs

This Part defines the static and structural constructs used in SysML structure diagrams, including the package diagram, block definition diagram, internal block diagram, and parametric diagram. The structural constructs are defined in the Model Elements, Blocks, Ports and Flows, and Constraint Blocks chapters.

Chapter 7: The Model Elements chapter refactors the kernel package from UML 2 and includes some extensions to provide some foundation capabilities for model management.

Chapter 8: The Blocks chapter reuses and extends structured classes from UML 2 composite structures to provide the fundamental capability for describing system decomposition and interconnection, and to define different types of system properties including value properties with optional units of measure.

Chapter 9: The Ports and Flows chapter provides the semantics for defining how blocks and parts interact through ports and how items flow across connectors.

Chapter 10: The Constraint Blocks chapter defines how blocks are extended to be used on parametric diagrams. Parametric diagrams model a network of constraints on system properties to support engineering analysis, such as performance, reliability, and mass properties analysis.
7 Model Elements

7.1 Overview

The ModelElements package of SysML defines general-purpose constructs that may be shown on multiple SysML diagram types. These include package, model, various types of dependencies (e.g., import, access, refine, realization), constraints, and comments. The package diagram defined in this chapter is used to organize the model by partitioning model elements into packageable elements and establishing dependencies between the packages and/or model elements within the package. The package defines a namespace for the packageable elements. Model elements from one package can be imported and/or accessed by another package. This organizational principle is intended to help establish unique naming of the model elements and avoid overloading a particular model element name. Packages can also be shown on other diagrams such as the block definition diagram, requirement diagram, and behavior diagrams.

Constraints are used to capture simple constraints associated with one or more model elements and can be represented on several SysML diagrams. The constraint can represent a logical constraint such as an XOR, a condition on a decision branch, or a mathematical expression. The constraint has been significantly enhanced in SysML as specified in Chapter 10, “Constraint Blocks” to enable it to be reused and parameterized to support engineering analysis.

Comments can be associated with any model element and are quite useful as an informal means of documenting the model. The comment is not included in the model repository. SysML has introduced an extension to a comment called rationale to facilitate the system modeler in capturing decisions. The rationale may be attached to any entity, such as a system element (block), or to any relationship, such as the satisfy relationship between a design element and a requirement. In the latter case, it may be used to capture the basis for the design decision and may reference an analysis report or trade study for further elaboration of the decision. In addition, SysML includes an extension of a comment to reflect a problem or issue that can be attached to any other model element.

SysML has extended the concept of view and viewpoint from UML to be consistent with the IEEE 1471 standard. In particular, a viewpoint is a specification of rules for constructing a view to address a set of stakeholder concerns, and the view is intended to represent the system from this viewpoint. This enables stakeholders to specify aspects of the system model that are important to them from their viewpoint, and then represent those aspects of the system in a specific view. Typical examples may include an operational, manufacturing, or security view/viewpoint.

7.2 Diagram Elements

Many of the diagram elements defined in this chapter, specifically comments, constraints, problem, rationale, and dependencies, including the dependency subtypes Conform, Realization, and Refine, may be shown on all SysML diagram types, in addition to the diagram elements that are specific to each diagram type.
<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Comment</td>
<td><img src="image" alt="Comment Diagram" /></td>
<td>UML4SysML::Comment</td>
</tr>
<tr>
<td>ConstraintNote</td>
<td><img src="image" alt="Constraint Note Diagram" /></td>
<td>UML4SysML::Constraint</td>
</tr>
<tr>
<td>ConstraintTextualNote</td>
<td><img src="image" alt="Constraint Textual Note Diagram" /></td>
<td>UML4SysML::Constraint</td>
</tr>
<tr>
<td>Model</td>
<td><img src="image" alt="Model Diagram" /></td>
<td>UML4SysML::Model</td>
</tr>
<tr>
<td>PackageDiagram</td>
<td><img src="image" alt="Package Diagram" /></td>
<td>UML4SysML::Package</td>
</tr>
<tr>
<td>PackageWith NameInTab</td>
<td><img src="image" alt="Package With Name In Tab Diagram" /></td>
<td>UML4SysML::Package</td>
</tr>
<tr>
<td>Element Name</td>
<td>Concrete Syntax Example</td>
<td>Abstract Syntax Reference</td>
</tr>
<tr>
<td>------------------------------</td>
<td>-------------------------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>PackageWith NameInside</td>
<td></td>
<td>UML4SysML::Package</td>
</tr>
<tr>
<td>Problem</td>
<td></td>
<td>SysML::ModelElements::Problem</td>
</tr>
<tr>
<td>Rationale</td>
<td></td>
<td>SysML::ModelElements::Rationale</td>
</tr>
<tr>
<td>ViewWith NameInside</td>
<td></td>
<td>SysML::ModelElements::View</td>
</tr>
<tr>
<td>ViewWith NameInTab</td>
<td></td>
<td>SysML::ModelElements::View</td>
</tr>
<tr>
<td>Viewpoint</td>
<td></td>
<td>SysML::ModelElements::Viewpoint</td>
</tr>
</tbody>
</table>

Table 7.1 - Graphical nodes defined by ModelElements package

Element Name       Concrete Syntax Example                                      Abstract Syntax Reference
PackageWith NameInside

Problem

Rationale

ViewWith NameInside

ViewWith NameInTab

Viewpoint

stakeholders="..." purpose="..." concerns="..." languages="..." methods="..."
<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Conform</td>
<td>«conform»</td>
<td>SysML::ModelElements::Conform</td>
</tr>
<tr>
<td>Dependency</td>
<td>«stereotype1» dependency1</td>
<td>UML4SysML::Dependency</td>
</tr>
<tr>
<td>PublicPackageImport</td>
<td>«import»</td>
<td>UML4SysML::PackageImport with visibility = public</td>
</tr>
<tr>
<td>PrivatePackageImport</td>
<td>«access»</td>
<td>UML4SysML::PackageImport with visibility = private</td>
</tr>
<tr>
<td>PackageContainment</td>
<td></td>
<td>UML4SysML::Package::ownedElement</td>
</tr>
<tr>
<td>Realization</td>
<td></td>
<td>UML4SysML::Realization</td>
</tr>
<tr>
<td>Refine</td>
<td>«refine»</td>
<td>UML4SysML::Refine</td>
</tr>
</tbody>
</table>
7.3 UML Extensions

7.3.1 Diagram Extensions

7.3.1.1 Stereotype Keywords or Icons Inside a Comment Note Box

Description
A comment note box may contain stereotype keywords or icons even though Comment is not a named element. UML specifies placement of a stereotype keyword relative to the name of the element. SysML makes explicit that they may appear inside a comment box as well. The stereotype keywords, if present, should appear prior to the comment text. The stereotype properties, if present, should appear after the comment text. The typical placement of stereotype icons is in the upper-right-hand corner of the containing graphical node.

![Figure 7.1 - Notation for the Rationale Stereotype of Comment](image)

7.3.1.2 UML Diagram Elements not Included in SysML

The notation for a “merge” dependency between packages, using a «merge» keyword on a dashed-line arrow, is not included in SysML. UML uses package merge in the definition of its own metamodel, which SysML builds on, but SysML does not support this capability for user-level models. Combining packages that have the same named elements, resulting in merged definitions of the same names, could cause confusion in user models and adds no inherent modeling capability, and so has been left out of SysML.
7.3.2 Stereotypes

Package ModelElements

**7.3.2.1 Conform**

**Description**

A Conform relationship is a dependency between a view and a viewpoint. The view conforms to the specified rules and conventions detailed in the viewpoint. Conform is a specialization of the UML dependency, and as with other dependencies the arrow direction points from the (client/source) to the (supplier/target).

**Constraints**

[1] The supplier/target must be an element stereotyped by «viewpoint».

[2] The client/source must be an element that is stereotyped by «view».

**7.3.2.2 Problem**

**Description**

A Problem documents a deficiency, limitation, or failure of one or more model elements to satisfy a requirement or need, or other undesired outcome. It may be used to capture problems identified during analysis, design, verification, or manufacture and associate the problem with the relevant model elements. Problem is a stereotype of comment and may be attached to any other model element in the same manner as a comment.

**7.3.2.3 Rationale**

**Description**

A Rationale documents the justification for decisions and the requirements, design, and other decisions. A Rationale can be attached to any model element including relationships. It allows the user, for example, to specify a rationale that may reference more detailed documentation such as a trade study or analysis report. Rationale is a stereotype of comment and may be attached to any other model element in the same manner as a comment.
7.3.2.4 View

Description
A View is a representation of a whole system or subsystem from the perspective of a single viewpoint. Views are allowed to import other elements including other packages and other views that conform to the viewpoint.

Attributes
- /viewpoint: Viewpoint

The viewpoint for this View, derived from the supplier of the «conform» dependency whose client is this View.

Constraints
[1] A view can only own element import, package import, comment, and constraint elements.
[2] The view is constructed in accordance with the methods and languages that are specified as part of the viewpoint. SysML does not define the specific methods. The precise semantics of this constraint is a semantic variation point.

7.3.2.5 Viewpoint

Description
A Viewpoint is a specification of the conventions and rules for constructing and using a view for the purpose of addressing a set of stakeholder concerns. The languages and methods for specifying a view may reference languages and methods in another viewpoint. They specify the elements expected to be represented in the view, and may be formally or informally defined. For example, the security viewpoint may require the security requirements, security functional and physical architecture, and security test cases.

Attributes
- stakeholders: String [*] Set of stakeholders.
- purpose: String The purpose addresses the stakeholder concerns.
- concerns: String [*] The interest of the stakeholders.
- languages: String [*] The languages used to construct the viewpoint.
- methods: String [*] The methods used to construct the views for this viewpoint.

Constraints
[1] A viewpoint cannot be the classifier of an instance specification.
[3] The property ownedAttributes must be empty.
[4] The property isAbstract must be set to true.
7.4 Usage Examples

Figure 7.3 - View/Viewpoint example

NOTE: moe refers to Measure of Effectiveness (see Annex C.3.2)
Figure 7.4 - Rationale and Problem examples

The master cylinder in the previous version leaked. The best-practice solution consists in assigning one reservoir per brakeline. See "automotive_d32_hdb.doc"
8 Blocks

8.1 Overview

Blocks are modular units of system description. Each block defines a collection of features to describe a system or other element of interest. These may include both structural and behavioral features, such as properties and operations, to represent the state of the system and behavior that the system may exhibit.

Blocks provide a general-purpose capability to model systems as trees of modular components. The specific kinds of components, the kinds of connections between them, and the way these elements combine to define the total system can all be selected according to the goals of a particular system model. SysML blocks can be used throughout all phases of system specification and design, and can be applied to many different kinds of systems. These include modeling either the logical or physical decomposition of a system, and the specification of software, hardware, or human elements. Parts in these systems may interact by many different means, such as software operations, discrete state transitions, flows of inputs and outputs, or continuous interactions.

The Block Definition Diagram in SysML defines features of blocks and relationships between blocks such as associations, generalizations, and dependencies. It captures the definition of blocks in terms of properties and operations, and relationships such as a system hierarchy or a system classification tree. The Internal Block Diagram in SysML captures the internal structure of a block in terms of properties and connectors between properties. A block can include properties to specify its values, parts, and references to other blocks. Ports are a special class of property used to specify allowable types of interactions between blocks, and are described in Chapter 9, “Ports and Flows.” Constraint Properties are a special class of property used to constrain other properties of blocks, and are described in Chapter 10, “Constraint Blocks.” Various notations for properties are available to distinguish these specialized kinds of properties on an internal block diagram.

A property can represent a role or usage in the context of its enclosing block. A property has a type that supplies its definition. A part belonging to a block, for example, may be typed by another block. The part defines a local usage of its defining block within the specific context to which the part belongs. For example, a block that represents the definition of a wheel can be used in different ways. The front wheel and rear wheel can represent different usages of the same wheel definition. SysML also allows each usage to define context-specific values and constraints associated with the individual usage, such as 25 psi for the front tires and 30 psi for the rear tires.

Blocks may also specify operations or other features that describe the behavior of a system. Except for operations, this chapter deals strictly with the definition of properties to describe the state of a system at any given point in time, including relations between elements that define its structure. Chapter 9, “Ports and Flows” specifies specific forms of interactions between blocks, and the Behavioral Constructs in Section III including activities, interactions, and state machines can be applied to blocks to specify their behavior. Chapter 15, “Allocations” in Part IV describes ways to allocate behavior to parts and blocks.

SysML blocks are based on UML classes as extended by UML composite structures. Some capabilities available for UML classes, such as more specialized forms of associations, have been excluded from SysML blocks to simplify the language. SysML blocks always include an ability to define internal connectors, regardless of whether this capability is needed for a particular block. SysML Blocks also extend the capabilities of UML classes and connectors with reusable forms of constraints, multi-level nesting of connector ends, participant properties for composite association classes, and connector properties. SysML blocks include several notational extensions as specified in this chapter.
### 8.2 Diagram Elements

#### 8.2.1 Block Definition Diagram

Table 8.1 - Graphical nodes defined in Block Definition diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>BlockDefinition Diagram</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Blocks::Block UML4SysML::Package</td>
</tr>
<tr>
<td>Block</td>
<td><img src="image" alt="Block Diagram" /></td>
<td>SysML::Blocks::Block</td>
</tr>
<tr>
<td>Actor</td>
<td><img src="image" alt="Actor Diagram" /></td>
<td>UML4SysML::Actor</td>
</tr>
<tr>
<td>DataType</td>
<td><img src="image" alt="DataType Diagram" /></td>
<td>UML4SysML::DataType</td>
</tr>
<tr>
<td>Element Name</td>
<td>Concrete Syntax Example</td>
<td>Abstract syntax Reference</td>
</tr>
<tr>
<td>----------------------</td>
<td>----------------------------------------------------------------------------------------</td>
<td>-----------------------------------</td>
</tr>
</tbody>
</table>
| ValueType            | «valueType»
|                      | ValueType1
|                      | operations
|                      | operation1(p1: Type1): Type2
|                      | properties
|                      | property1: Type3
|                      | «valueType»
|                      | unit = UnitName
|                      | SysML::Blocks::ValueStringType                                                       |
| Enumeration          | «enumeration»
|                      | Enumeration1
|                      | literalName1
|                      | literalName2
|                      | UML4SysML::Enumeration                                                                |
| AbstractDefinition   | Name
|                      | {abstract} Name
|                      | Name
|                      | {abstract}
|                      | Name
|                      | UML4SysML::Classifier with isAbstract equal true                                       |
| StereotypeProperty   | «stereotype1»
|                      | Block1
|                      | «stereotype1»
|                      | property1 = value
|                      | UML4SysML::Stereotype                                                                 |
| Namespace            | Block1
|                      | namespace
|                      | Block2
|                      | part1
|                      | 1
|                      | 0..*
|                      | Block3
|                      | SysML::Blocks::Block                                                                   |
| Structure            | Block1
|                      | structure
|                      | Block2
|                      | c1
|                      | 1
|                      | e1
|                      | Block3
|                      | SysML::Blocks::Block                                                                   |
### Table 8.2 Graphical paths defined by in Block Definition diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Unit</strong></td>
<td></td>
<td>SysML::Blocks::Unit</td>
</tr>
<tr>
<td></td>
<td>«unit»</td>
<td></td>
</tr>
<tr>
<td></td>
<td>{dimension = Dimension1}</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Unit1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Dimension</strong></td>
<td></td>
<td>SysML::Blocks::Dimension</td>
</tr>
<tr>
<td></td>
<td>«dimension»</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Dimension1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Dependency</strong></td>
<td></td>
<td>UML4SysML::Dependency</td>
</tr>
<tr>
<td></td>
<td>«stereotype1»</td>
<td></td>
</tr>
<tr>
<td></td>
<td>dependency1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0..1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>property1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1..*{ordered}</td>
<td></td>
</tr>
<tr>
<td></td>
<td>property2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>association1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>property1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>1 (ordered)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>property3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>association1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>property1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0..*</td>
<td></td>
</tr>
<tr>
<td><strong>ReferenceAssociation</strong></td>
<td></td>
<td>UML4SysML::Association and UML4SysML::Property with aggregationKind = none</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>PartAssociation</strong></td>
<td></td>
<td>UML4SysML::Association and UML4SysML::Property with aggregationKind = composite</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>SharedAssociation</strong></td>
<td></td>
<td>UML4SysML::Association and UML4SysML::Property with aggregationKind = shared</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>MultibranchPartAssociation</strong></td>
<td></td>
<td>UML4SysML::Association and UML::Kernel::Property with aggregationKind = composite</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Table 8.2 Graphical paths defined by in Block Definition diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>MultibranchShared Association</td>
<td><img src="image" alt="Diagram of MultibranchShared Association" /></td>
<td>UML4SysML::Association and UML::Kernel::Property with aggregationKind = shared</td>
</tr>
<tr>
<td>Generalization</td>
<td><img src="image" alt="Diagram of Generalization" /></td>
<td>UML4SysML::Generalization</td>
</tr>
<tr>
<td>Multibranch Generalization</td>
<td><img src="image" alt="Diagram of Multibranch Generalization" /></td>
<td>UML4SysML::Generalization</td>
</tr>
<tr>
<td>GeneralizationSet</td>
<td><img src="image" alt="Diagram of GeneralizationSet" /></td>
<td>UML4SysML::GeneralizationSet</td>
</tr>
<tr>
<td>BlockNamespace Containment</td>
<td><img src="image" alt="Diagram of BlockNamespace Containment" /></td>
<td>UML4SysML::Class::nestedClassifier</td>
</tr>
</tbody>
</table>
### Table 8.2 Graphical paths defined by in Block Definition diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Abstract syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>ParticipantProperty</strong></td>
<td><img src="image1.png" alt="Diagram" /></td>
<td>UML4SysML:: Property, UML4SysML:: AssociationClass</td>
</tr>
<tr>
<td><strong>ConnectorProperty</strong></td>
<td><img src="image2.png" alt="Diagram" /></td>
<td>UML4SysML:: Property, UML4SysML:: Connector</td>
</tr>
</tbody>
</table>
### 8.2.2 Internal Block Diagram

<table>
<thead>
<tr>
<th>Table 8.3 - Graphical nodes defined in Internal Block diagrams</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Element Name</strong></td>
</tr>
<tr>
<td>------------------</td>
</tr>
<tr>
<td>InternalBlockDiagram</td>
</tr>
<tr>
<td>Property</td>
</tr>
<tr>
<td>ActorPart</td>
</tr>
<tr>
<td>PropertySpecificType</td>
</tr>
</tbody>
</table>
8.3 UML Extensions

8.3.1 Diagram Extensions

8.3.1.1 Block Definition Diagram

A block definition diagram is based on the UML class diagram, with restrictions and extensions as defined by SysML.

Block and ValueType Definitions

A SysML Block defines a collection of features to describe a system or other element of interest. A SysML ValueType defines values that may be used within a model. SysML blocks are based on UML classes, as extended by UML composite structures. SysML value types are based on UML data types. Diagram extensions for SysML blocks and value types are described by other subheadings of this section.

Default «block» stereotype on unlabeled box

If no stereotype keyword appears within a definition box on a block definition diagram (including any stereotype property compartments), then the definition is assumed to be a SysML block, exactly as if the «block» keyword had appeared before the name in the top compartment of the definition.

Labeled compartments

SysML allows blocks to have multiple compartments, each optionally identified with its own compartment name. The compartments may partition the features shown according to various criteria. Some standard compartments are defined by SysML itself, and others can be defined by the user using tool-specific facilities. Compartments may appear in any order.
SysML defines two additional compartments, namespace and structure compartments, which may contain graphical nodes rather than textual constraint or feature definitions. See separate subsections of this section for a description of these compartments.

**Constraints compartment**

SysML defines a special form of compartment, with the label “constraints,” which may contain one or more constraints owned by the block. A constraint owned by the block may be shown in this compartment using the standard text-based notation for a constraint, consisting of a string enclosed in brace characters. The use of a compartment to show constraints is optional. The note-based notation, with a constraint shown in a note box outside the block and linked to it by a dashed line, may also be used to show a constraint owned by a block.

A constraints compartment may also contain declarations of constraint properties owned by the block. A constraint property is a property of the block that is typed by a ConstraintBlock, as defined in Chapter 10, “Constraint Blocks.” Only the declaration of the constraint property may be shown within the compartment, not the details of its parameters or binding connectors that link them to other properties.

**Namespace compartment**

A compartment with the label “namespace” may appear as part of a block definition to show blocks that are defined in the namespace of a containing block. This compartment may contain any of the graphical elements of a block definition diagram. All blocks or other named elements defined in this compartment belong to the namespace of the containing block.

Because this compartment contains graphical elements, a wider compartment than typically used for feature definitions may be useful. Since the same block can appear more than once in the same diagram, it may be useful to show this compartment as part of a separate definition box than a box that shows only feature compartments. Both namespace and structure compartments, which may both need a wide compartment to hold graphical elements, could also be shown within a common definition box.

**Structure compartment**

A compartment with the label “structure” may appear as part of a block definition to show connectors and other internal structure elements for the block being defined. This compartment may contain any of the graphical elements of an internal block diagram.

Because this compartment contains graphical elements, a wider compartment than typically used for feature definitions may be useful. Since the same block can appear more than once in the same diagram, it may be useful to show this compartment as part of a separate definition box than a box that shows only feature compartments. Both namespace and structure compartments, which may both need a wide compartment to hold graphical elements, could also be shown within a common definition box.

**Unit and Dimension declarations**

The declarations of value types have been extended to support the declaration of a unit of measure or a dimension. These declarations must refer by name to an instance of a Unit or Dimension stereotype defined separately. A sample set of predefined dimensions and units is given in Annex C, Section C.4.

**Default multiplicities**

SysML defines defaults for multiplicities on the ends of specific types of associations. A part or shared association has a default multiplicity of \([0..1]\) on the black or white diamond end. A unidirectional association has a default multiplicity of
I on its target end. These multiplicities may be assumed if not shown on a diagram. To avoid confusion, any multiplicity other than the default should always be shown on a diagram.

**Property-specific type**

Enclosing the type name of a property in square brackets specifies that the type is a local specialization of the referenced type, which may be overridden to specify additional values or other customizations that are unique to the property. Redefined or added features of the newly defined type may be shown in compartments for the property on an internal block diagram. If no type name appears between the square brackets, the property-specific type is defined provided by its own declarations, without specializing any existing type.

### 8.3.1.2 Internal Block Diagram

An internal block diagram is based on the UML composite structure diagram, with restrictions and extensions as defined by SysML.

**Property types**

Three general categories of properties are recognized in SysML: parts, references, and value properties (see Section 8.3.2.1, “Binding Connector”). A part or value property is always shown on an internal block diagram with a solid-outline box. A reference property is shown by a dashed-outline box, consistent with UML.

**Block reference in diagram frame**

The diagram heading name for an internal block diagram (the string contained in the tab in the upper-left-hand corner of the diagram frame) must identify the name of a SysML block as its modelElementName. (See Annex A for the definition of a diagram heading name including the modelElementName component. This component is optional for many SysML diagram types, but not for an internal block diagram.) All the properties and connectors that appear inside the internal block diagram belong to the block that is named in the diagram heading name.

**Compartments on internal properties**

SysML permits any property shown on an internal block diagram to also show compartments within the property box. These compartments may be given standard or user-customized labels just as on block definitions. All features shown within these compartments must match those of the block or value type that types the property. For a property-specific type, these compartments may be used to specify redefined or additional features of the locally defined type. An unlabeled compartment on an internal property box is by default a structure compartment.

**Compartments on a diagram frame**

SysML permits compartments to be shown across the entire width of the diagram frame on an internal block diagram. These compartments must always follow an initial compartment which always shows the internal structure of a referenced block. These compartments may have all the same contents as could be shown on a block definition diagram for the block defined at the top level of the diagram frame.

**Property path name**

A property name shown inside or outside the property box may take the form of a multi-level name. This form of name references a nested property accessible through a sequence of intermediate properties from a referencing context. The name of the referenced property is built by a string of names separated by “.”, resulting in a form of path name that
identifies the property in its local context. A colon and the type name for the property may optionally be shown following the dotted name string. If any of the properties named in the path name string identifies a reference property, the property box is shown with a dashed-outline box, just as for any reference property on an internal block diagram.

This notation is purely a notational shorthand for a property that could otherwise be shown within a structure of nested property boxes, with the names in the dotted string taken from the name that would appear at each level of nesting. In other words, the internal property shown with a path name in the left-hand side of Figure 8.1 is equivalent to the innermost nested box shown at the right:

![Figure 8.1 - Nested property reference](image)

**Nested connector end**

Connectors may be drawn that cross the boundaries of nested properties to connect to properties within them. The connector is owned by the most immediate block that owns both ends of the connector. A NestedConnectorEnd stereotype of a UML ConnectorEnd is automatically applied to any connector end that is nested more than one level deep within a containing context.

Use of nested connector ends does not follow strict principles of encapsulation of the parts or other properties that a connector line may cross. The need for nested connector ends can be avoided if additional properties can be added to the block at each containing level. Nested connector ends are available for cases where the introduction of these intermediate properties is not feasible or appropriate.

The ability to connect to nested properties within a containing block requires that multiple levels of decomposition be shown on the same diagram.

**Property-specific type**

Enclosing the type name of an internal property in square brackets specifies that the type is a local specialization of the referenced type, which may be overridden to specify additional values or other customizations that are unique to the property. Redefined or added features of the newly defined type may be shown in compartments for the property. If the property name appears on its own, with no colon or type name, or if no type name appears between the square brackets, the property-specific type is entirely provided by its own declarations, without specializing any existing type.

**Default value compartment**

A compartment with a label of “defaultValue” may be used to show the default value for a property as an alternative to an “=“suffix string on its declaration within its containing block. It may be used for a property whose type has substructure and a default value with many subvalues. A default value compartment on a property may be used instead of a property-specific type when all that is required are property-specific values. If a default value is specified for a property nested any level deeper than the top level of an internal block diagram frame, then its containing property must still have a property-specific type, so that the default value specification can be included within that type.
Default multiplicities
SysML defines default multiplicities of 1 on each end of a connector. These multiplicities may be assumed if not shown on a diagram. To avoid confusion, any multiplicity other than the default should always be shown on a diagram.

8.3.1.3 UML Diagram Elements not Included in SysML Block Definition Diagrams

The supported variety of notations for associations and association annotations has been reduced to simplify the burden of teaching, learning, and interpreting SysML diagrams for the systems engineering user. Notational and metamodel support for n-ary associations and qualified associations has been excluded from SysML. N-ary associations, shown in UML by a large open diamond with multiple branches, can be modeled by an intermediate block with no loss in expressive power. Qualified associations, shown in SysML by an open box at the end of an association path with a property name inside, are a specialized feature of UML that specifies how a property value can represent an identifier of an associated target. This capability, while useful for data modeling, does not seem essential to accomplish any of the SysML requirements for support of systems engineering. The use of navigation arrowheads on an association has been simplified by excluding the case of arrowheads on both ends, and requiring that such an association always be shown without arrowheads on either end. An “X” on a single end of an association to indicate that an end is “not navigable” has similarly been dropped, as has the use of a small filled dot at the end of an association to indicate an owned end of an association. SysML still supports use of an arrowhead on one end of a unidirectional association.

The use of a «primitive» keyword on a value type definition (which in UML specifies the PrimitiveType specialization of UML DataType) is not supported. Whether or not a value type definition has internal structure can be determined from the value type itself.

8.3.1.4 UML Diagram Elements not Included in SysML Internal Block Diagrams

The UML Composite Structure diagram has many notations not included in the subset defined in this chapter. Other SysML chapters add some of these notations into the supported contents of an internal block diagram.

8.3.2 Stereotypes

Package Blocks

Figure 8.2 - Abstract syntax expressions for SysML blocks
Figure 8.3 - Abstract syntax extensions for SysML properties

Figure 8.4 - Abstract syntax extensions for SysML value types

Figure 8.5 - Abstract syntax extensions for SysML connector ends
8.3.2.1 Binding Connector

Description
A Binding Connector is a connector which specifies that the properties at both ends of the connector have equal values. If the properties at the ends of a binding connector are typed by a DataType or ValueType, the connector specifies that the instances of the properties must hold equal values, recursively through any nested properties within the connected properties. If the properties at the ends of a binding connector are typed by a Block, the connector specifies that the instances of the properties must refer to the same block instance.

Constraints
[1] The two ends of a binding connector must have either the same type or types that are compatible so that equality of their values can be defined.

8.3.2.2 Block

Description
A Block is a modular unit that describes the structure of a system or element. It may include both structural and behavioral features, such as properties and operations, that represent the state of the system and behavior that the system may exhibit. Some of these properties may hold parts of a system, which can also be described by blocks. A block may include a structure of connectors between its properties to indicate how its parts or other properties relate to one another.

SysML blocks provide a general-purpose capability to describe the architecture of a system. They provide the ability to represent a system hierarchy, in which a system at one level is composed of systems at a more basic level. They can describe not only the connectivity relationships between the systems at any level, but also quantitative values or other information about a system.

SysML does not restrict the kind of system or system element that may be described by a block. Any reusable form of description that may be applied to a system or a set of system characteristics may be described by a block. Such reusable descriptions, for example, may be applied to purely conceptual aspects of a system design, such as relationships that hold between parts or properties of a system.

Connectors owned by SysML blocks may be used to define relationships between parts or other properties of the same containing block. The type of a connector or its connected ends may specify the semantic interpretation of a specific connector. A Binding Connector is a connector that is not typed by an association. If the two ends of a binding connector have the same type, the connector specifies that the properties at the end of the connector must have the same values, recursively through any nested properties within the connected properties.
SysML excludes variations of associations in UML in which navigable ends can be owned directly by the association. In SysML, navigation is equivalent to a named property owned directly by a block. The only form of an association end that SysML allows an association to own directly is an unnamed end used to carry an inverse multiplicity of a reference property. This unnamed end provides a metamodel element to record an inverse multiplicity, to cover the specific case of a unidirectional reference that defines no named property for navigation in the inverse direction. SysML enforces its equivalence of navigation and ownership by means of constraints that the block stereotype enforces on the existing UML metamodel.

SysML establishes three standard classifications of properties belonging to a SysML Block. A property typed by a SysML Block that has composite aggregation is classified as a part property. A property typed by a Block that does not have composite aggregation is classified as a reference property. A property typed by a UML DataType or SysML ValueType is classified as a value property. Part, reference, and value properties may be shown in block definition compartments with the labels “parts,” “references,” and “values” respectively. Properties of any type may be shown in a “properties” compartment.

On a block definition diagram, a part property is shown by a black diamond symbol on an association. As in UML, an instance of a block may be included in at most one part property at a time. A part property holds instances that belong to a larger whole. Typically, a part-whole relationship means that certain operations that apply to the whole also apply to each of the parts. For example, if a whole represents a physical object, a change in position of the whole could also change the position of each of the parts. A property of the whole such as its mass could also be implied by its parts. Operations and relationships that apply to parts typically apply transitively across all parts of these parts, through any number of levels. A particular application domain may establish its own interpretation of part-whole relationships across the blocks defined in a particular model, including the definition of operations that apply to the parts along with the whole. For software objects, a typical interpretation is that delete, copy, and move operations apply across all parts of a composite object.

SysML also supports properties with shared aggregation, as shown by a white diamond symbol on an association. Like UML, SysML defines no specific semantics or constraints for properties with shared aggregation, but particular models or tools may interpret them in specific ways.

**Attributes**

- **isEncapsulated**: Boolean [0..1]

  If true, then the block is treated as a black box; a part typed by this black box can only be connected via its ports or directly to its outer boundary. If false, or if a value is not present, then connections can be established to elements of its internal structure via deep-nested connector ends.

**Constraints**

[1] For an association in which both ends are typed by blocks, the number of ends must be exactly two.

[2] The number of ends of a connector owned by a block must be exactly two. (In SysML, a binding connector is not typed by an association, so this constraint is not implied entirely by the preceding constraint.)

[3] In the UML metamodel on which SysML is built, any instance of the Property metaclass that is typed by a block (a Class with the «block» stereotype applied) and which is owned by an Association may not have a name and may not be defined as a navigable owned end of the association. (While the Property has a “name” property as defined by its NamedElement superclass, the value of the “name” property, which is optional, must be missing.)

[4] In the UML metamodel on which SysML is built, a Property that is typed by a block must be defined as an end of an association. (An inverse end of this association, whether owned by another block or the association itself, must always be present so there is always a metamodel element to record the inverse multiplicity of the reference.)
The following constraint under Section 9.3.6, “Connector” in the UML 2 Superstructure Specification is removed by SysML: “[3] The ConnectableElements attached as roles to each ConnectorEnd owned by a Connector must be roles of the Classifier that owned the Connector, or they must be ports of such roles.”

If the aggregation attribute of a property owned by a SysML block is equal to “composite” or “shared,” then the type of the property must be a block.

Within an instance of a SysML Block, the values of any property with composite aggregation (aggregation = composite) must not contain the block in any of its own properties that also have composite aggregation, or within any unbroken chain of properties that all have composite aggregation. (Within an instance of a SysML Block, the instances of properties with composite aggregation must form an acyclic graph.)

8.3.2.3 ConnectorProperty

Description
Connectors can be typed by association classes that are stereotyped by Block (association blocks, see Section 8.3.2.7, “ParticipantProperty”). These connectors specify instances (links) of the association block that exist due to instantiation of the block owning or inheriting the connector. The value of a connector property on an instance of a block will be exactly those link objects that are instances of the association block typing the connector referred to by the connector property.

A connector property can optionally be shown in an internal block diagram with a dotted line from the connector line to a rectangle notating the connector property. The keyword «connector» before a property name indicates the property is stereotyped by ConnectorProperty.

Attributes
• connector : Connector A connector of the block owning the property on which the stereotype is applied.

Constraints
[1] ConnectorProperty may only be applied to properties of classes stereotyped by Block.
[2] The connector attribute of the applied stereotype must refer to a connector owned or inherited by a block owning the property on which the stereotype is applied.
[3] The aggregation of a property stereotyped by ConnectorProperty must be composite.
[4] The type of the connector referred to by a connector attribute must be an association class stereotyped by Block.
[5] A property stereotyped by ConnectorProperty must have the same name and type as the connector referred to by the connector attribute.

8.3.2.4 DistributedProperty

DistributedProperty is a stereotype of Property used to apply a probability distribution to the values of the property. Specific distributions should be defined as subclasses of the DistributedProperty stereotype with the operands of the distributions represented by properties of those stereotype subclasses.

Constraints
[1] The DistributedProperty stereotype may be applied only to properties of classifiers stereotyped by Block or ValueType.
8.3.2.5 Dimension

A Dimension is a kind of quantity that may be stated by means of defined units. For example, the dimension of length may be measured by units of meters, kilometers, or feet.

Dimension is defined as a stereotype of InstanceSpecification, but it uses this metaclass only to define supporting elements for ValueType definitions. (The reuse of InstanceSpecification to define another metaclass is similar to the EnumerationLiteral metaclass in UML.)

The only valid use of a Dimension instance is to be referenced by the “dimension” property of a ValueType or Unit stereotype.

8.3.2.6 NestedConnectorEnd

Description

The NestedConnectorEnd stereotype of UML ConnectorEnd extends a UML ConnectorEnd so that the connected property may be identified by a multi-level path of accessible properties from the block that owns the connector.

Attributes

- `propertyPath: Property [1..*]` (ordered)

  The `propertyPath` list of the NestedConnectorEnd stereotype must identify a path of containing properties that identify the connected property in the context of the block that owns the connector. The ordering of properties is from a property of the block that owns the connector, through a property of each intermediate block that types the preceding property, until a property is reached that contains a connector end property within its type. The connector end property is not included in the `propertyPath` list, but instead is held by the role property of the UML ConnectorEnd metaclass.

Constraints

1. The property at the first position in the `propertyPath` attribute of the NestedConnectorEnd must be owned by the block that owns the connector.
2. The property at each successive position of the `propertyPath` attribute, following the first position, must be contained in the Block, DataType, or ValueType that types the property at the immediately preceding position.
3. Within a ConnectorEnd metaclass to which the NestedConnectorEnd stereotype has been applied, the role property of the ConnectorEnd metaclass must be contained in the type of the property at the last position of the `propertyPath` list.
4. Within a ConnectorEnd metaclass to which the NestedConnectorEnd stereotype has been applied, the value of the “partWithPort” property of the ConnectorEnd metaclass must be equal to the property at the last position of the `propertyPath` list.

8.3.2.7 ParticipantProperty

Description

The Block stereotype extends Class, so it can be applied to any specialization of Class, including Association Classes. These are informally called “association blocks.” An association block can own properties and connectors, like any other block. Each instance of an association block can link together instances of the end classifiers of the association.
To refer to linked objects and values of an instance of an association block, it is necessary for the modeler to specify which (participant) properties of the association block identify the instances being linked at which end of the association. The value of a participant property on an instance (link) of the association block is the value or object at the end of the link corresponding to this end of the association.

Participant properties can be the ends of connectors owned by an association block. The association block can be the type of multiple other connectors to reuse the same internal structure for all the connectors. The keyword «participant» before a property name indicates the property is stereotyped by ParticipantProperty. The types of participant properties can be elided if desired. They are always the same as the corresponding association end type.

Attributes

- end : Property A member end of the association block owning the property on which the stereotype is applied.

Constraints

[1] ParticipantProperty may only be applied to properties of association classes stereotyped by Block.
[2] ParticipantProperty may not be applied to properties that are member ends of an association.
[3] The aggregation of a property stereotyped by ParticipantProperty must be none.
[4] The end attribute of the applied stereotype must refer to a member end of the association block owning the property on which the stereotype is applied.
[5] A property stereotyped by ParticipantProperty must have the same type as the property referred to by the end attribute.
[6] The property referred to by end must have an upper multiplicity of 1.

8.3.2.8 PropertySpecificType

The PropertySpecificType stereotype is automatically applied to a classifier created by the notation for a property-specific type for a property belonging to a SysML Block or ValueType. It identifies these classifiers so that they may be managed along with the property that they type.

Constraints

[1] A classifier to which the PropertySpecificType stereotype is applied must be referenced as the type of one and only one property.
[2] The name of a classifier to which a PropertySpecificType is applied must be missing. (The “name” attribute of the NamedElement metaclass must be empty.)

8.3.2.9 Unit

A Unit is a quantity in terms of which the magnitudes of other quantities that have the same dimension can be stated. A unit often relies on precise and reproducible ways to measure the unit. For example, a unit of length such as meter may be specified as a multiple of a particular wavelength of light. A unit may also specify less stable or precise ways to express some value, such as a cost expressed in some currency, or a severity rating measured by a numerical scale.

Unit is defined as a stereotype of InstanceSpecification, but it uses this metaclass only to define supporting elements for ValueType definitions. (The reuse of InstanceSpecification to define another metaclass is similar to the EnumerationLiteral metaclass in UML.)

The only valid use of a Unit instance is to be referenced by the “unit” property of a ValueType stereotype.
8.3.2.10 ValueType

Description

A ValueType defines types of values that may be used to express information about a system, but cannot be identified as the target of any reference. Since a value cannot be identified except by means of the value itself, each such value within a model is independent of any other, unless other forms of constraints are imposed.

Value types may be used to type properties, operation parameters, or potentially other elements within SysML. SysML defines ValueType as a stereotype of UML DataType to establish a more neutral term for system values that may never be given a concrete data representation. For example, the SysML “Real” ValueType expresses the mathematical concept of a real number, but does not impose any restrictions on the precision or scale of a fixed or floating-point representation that expresses this concept. More specific value types can define the concrete data representations that a digital computer can process, such as conventional Float, Integer, or String types.

SysML ValueType adds an ability to carry a unit of measure or dimension associated with the value. A dimension is a kind of quantity that may be stated in terms of defined units, but does not restrict the selection of a unit to state the value. A unit is a particular value in terms of which a quantity of the same dimension may be expressed.

If these additional characteristics are not required, then UML DataType may be used.

Attributes

• dimension: ValueType [0..1]
  A kind of quantity that may be stated by means of defined units, as identified by an instance of the Dimension stereotype. A value type may optionally specify a dimension without any unit. Such a value has no concrete representation, but may be used to express a value in an abstract form independent of any specific units.

• unit: ValueType [0..1]
  A quantity in terms of which the magnitudes of other quantities that have the same dimension can be stated, as identified by an instance of the Unit stereotype.

Constraints

[1] If a value is present for the unit attribute, the dimension attribute must be equal to the dimension property of the referenced unit.

8.3.3 Model Libraries

Package Blocks

![Diagram of model library for blocks]

Figure 8.7 - Model Library for Blocks
8.3.3.1 Complex

Description

A Complex value type represents the mathematical concept of a complex number. A complex number consists of a real part defined by a real number, and an imaginary part defined by a real number multiplied by the square root of -1. Complex numbers are used to express solutions to various forms of mathematical equations.

Attributes

- realPart: Real  A real number used to express the real part of a complex number.
- imaginaryPart: Real  A real number used to express the imaginary part of a complex number.

8.3.3.2 Real

A Real value type represents the mathematical concept of a real number. A Real value type may be used to type values that hold continuous quantities, without committing a specific representation such as a floating point data type with restrictions on precision and scale.

8.4 Usage Examples

8.4.1 Wheel Hub Assembly

In Figure 8.8 a block definition diagram shows the blocks that comprise elements of a Wheel. The block property LugBoltJoint.torque has a specialization of DistributedProperty applied to describe the uniform distribution of its values. Examples of such distributions can be found in Section C.5.
Figure 8.8 - Block diagram for the Wheel Package
In Figure 8.9 an internal block diagram (ibd) shows how the blocks defined in the Wheel package are used. This ibd is a partial view that focuses on particular parts of interest and omits others from the diagram, such as the "v" InflationValve and "weight" BalanceWeight, which are also parts of a Wheel.

### 8.4.2 SI Value Types

In Figure 8.10, several value types using SI units and dimensions are defined to be generally available in the SI Value Types package for typing value properties. Because a unit already identifies the type of quantity, or dimension, that the unit measures, a value type only needs to identify the unit to identify the dimension as well. The value types in this example refer to units that are assumed to be defined in an imported package, such as the SI Definitions model library defined in Section C.4.
8.4.3 Design Configuration for SUV EPA Fuel Economy Test

SysML internal block diagrams may be used to specify blocks with unique identification and property values. Figure 8.11 shows an example used to specify a unique vehicle with a vehicle identification number (VIN) and unique properties such as its weight, color, and horsepower. This concept is distinct from the UML concept of instance specifications in that it does not imply or assume any run-time semantic, and can also be applied to specify design configurations.

In SysML, one approach is to capture system configurations by creating a context for a configuration in the form of a context block. The context block may capture a unique identity for the configuration, and utilizes parts and part-specific types to express property design values within the specification of a particular system configuration. Such a context block may contain a set of parts that represent the block instances in this system configuration, each containing specific values for each property. This technique also provides for configurations that reflect hierarchical system structures, where nested parts or other properties are assigned design values using property-specific types. The following example illustrates the approach.
Figure 8.11 - SUV EPA Fuel Economy Test
8.4.4 Water Delivery

Figure 8.12 shows an association block Water Delivery between a bank of spigots and a faucet. Figure 8.13 shows the internal structure of Water Delivery defining connectors between the spigots in the bank and inlets on the faucet. The participant properties identify the spigot bank and faucet being connected. The end property on the stereotype refers to the corresponding association end in Figure 8.12. The type of participant properties is shown for clarity, but is always the same as the association end type and can be elided. They are shown with dashed rectangles because they are reference properties. The internal structure connects hot and cold properties of the participants.

Figure 8.12 Water Delivery association block

Figure 8.13 - Internal structure of Water Delivery association block

Figure 8.14 shows two views of a block House with a connector of type Water Delivery. The connector in the top view “decomposes” into the subconnectors in the lower view according to the internal structure of Water Delivery. The subconnectors relate the nested properties of :WaterSupply to the nested properties of :WaterClient.
The top portion of Figure 8.15 shows specializations of the block WaterClient into Bath, Sink, and Shower. These are used as part types in the internal structure of the block House 2 shown in the lower portion of the figure. The composite connector for Water Delivery is reused three times to establish connections between spigots on the water supply and the inlets of faucets on the bath, sink, and shower.
Figure 8.15 - Specializations of Water Client in house example

Figure 8.16 modifies Figure 8.12 to add a Plumbing association block for the association between Spigot and Faucet Inlet. Figure 8.17 shows the internal structure for the Plumbing association block, which includes a pipe and two fittings (the additional part and connector definitions are omitted for brevity).

Figure 8.16 - Plumbing association block
Figure 8.17 - Internal structure of Plumbing association block

Figure 8.18 modifies Figure 8.13 to use Plumbing as a connector type within the Water Delivery association block. The lower connector shows its connector property explicitly, enabling the pipe it contains to be connected to a mounting bracket (the additional part and connector definitions are omitted for brevity).

Figure 8.18 - Water Delivery association block with internal Plumbing connector
9  Ports and Flows

9.1  Overview

This chapter specifies flow ports that enable flow of items between blocks and parts, as well as standard ports that enable invocation of services on blocks and parts. A port is an interaction point between a block or part and its environment that is connected with other ports via connectors. The main motivation for specifying such ports on system elements is to allow the design of modular reusable blocks, with clearly defined interfaces. (Note: the block owns its ports and therefore the port is part of the blocks definition.) This chapter also specifies item flows across connectors and associations.

9.1.1  Standard Ports

A standard port specifies the services the owning block provides (offers) to its environment as well as the services that the owning block expects (requires) of its environment. The specification of the services is achieved by typing the standard port by the provided and/or required interfaces. In general standard ports are used in the context of service-oriented architectures, which is typical for software component architectures. Standard ports typically contain operations which specify bidirectional flow of data, so they are typically used in the context of peer-to-peer synchronous request/reply communications. A special case of a service is signal reception, which signifies a one-way communication of signal instances, where the handling of the request is asynchronous.

For example, a block representing an automatic transmission in a car could have a standard port that specifies that the Transmission block can accept commands to switch gears. Standard ports are another name for UML 2 ports; they are defined by the same metaclass and use the same notations.

9.1.2  Flow Ports

A flow port specifies the input and output items that may flow between a block and its environment. Flow ports are interaction points through which data, material, or energy can enter or leave the owning block. The specification of what can flow is achieved by typing the flow port with a specification of things that flow. This can include typing an atomic flow port with a single type representing the items that flow in or out, or typing a nonatomic flow port with a flow specification which lists multiple items that flow. A block representing an automatic transmission in a car could have an atomic flow port that specifies “Torque” as an input and another atomic flow port that specifies “Torque” as an output. A more complex flow port could specify a set of signals and/or properties that flow in and out of the flow port. In general, flow ports are intended to be used for asynchronous, broadcast, or send-and-forget interactions. Flow ports extend UML 2 ports.

9.1.3  Item Flows

Item flows represent the things that flow between blocks and/or parts and across associations or connectors. Whereas flow ports specify what “can” flow in or out of a block, item flows specify what “does” flow between blocks and/or parts in a particular usage context. This important distinction enables blocks to be interconnected in different ways depending on its usage context. For example, a tank may include a flow port that can accept fluid as an input. In a particular use of the tank, “gasoline” flows across a connector into its flow port, and in another use of the tank, “water” flows across a connector into its flow port. The item flow would specify what “does” flow on the connector in the particular usage (e.g., gas, water) and the flow port specifies what can flow (e.g., fluid). This enables type matching between the item flows and between flow ports to assist in interface compatibility analysis.
Item flows may be allocated from object nodes in activity diagrams or signals sent from state machines across a connector. Flow allocation is described in Chapter 15, “Allocations,” and can be used to help ensure consistency across the different parts of the model.

### 9.2 Diagram Elements

#### 9.2.1 Extensions to Block Definition Diagram

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>StandardPort</td>
<td><img src="image" alt="StandardPort Diagram" /></td>
<td>UML4SysML::Port</td>
</tr>
<tr>
<td>StandardPort (Compartment Notation)</td>
<td><img src="image" alt="StandardPort Compartment Diagram" /></td>
<td>SysML::Ports&amp;Flows::StandardPort</td>
</tr>
<tr>
<td>FlowPort</td>
<td><img src="image" alt="FlowPort Diagram" /></td>
<td>SysML::Ports&amp;Flows::FlowPort</td>
</tr>
</tbody>
</table>

Note: The diagrams illustrate the syntax for StandardPort, FlowPort, and the conjugated flow port.
<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>FlowPort</strong> (Compartment Notation)</td>
<td><img src="image" alt="Diagram of FlowPort compartment notation" /></td>
<td>SysML::Ports&amp;Flows::FlowPort</td>
</tr>
<tr>
<td><strong>Interface</strong></td>
<td><img src="image" alt="Diagram of Interface" /></td>
<td>UML4SysML::Interfaces::Interface</td>
</tr>
<tr>
<td><strong>FlowSpecification</strong></td>
<td><img src="image" alt="Diagram of FlowSpecification" /></td>
<td>SysML::Ports&amp;Flows::FlowSpecification</td>
</tr>
<tr>
<td><strong>ItemFlow</strong></td>
<td><img src="image" alt="Diagram of ItemFlow" /></td>
<td>SysML::Ports&amp;Flows::ItemFlow</td>
</tr>
</tbody>
</table>
### 9.2.1.1 Extensions to Internal Block Diagram

#### Table 9.2 - Extension to Internal Block Diagram

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>StandardPort</strong></td>
<td><img src="image1" alt="StandardPort Diagram" /></td>
<td>SysML::Ports&amp;Flows::StandardPort</td>
</tr>
<tr>
<td><strong>FlowPort</strong></td>
<td><img src="image2" alt="FlowPort Diagram" /></td>
<td>SysML::Ports&amp;Flows::FlowPort</td>
</tr>
</tbody>
</table>

**Diagram Descriptions**

- **StandardPort**
  - `iTnscmd`: In Transmission Command
  - `p1`: Transmission
  - `iTnscda`: In Transmission Data

- **FlowPort**
  - `p`: In Transmission
  - `t`: Transmission
  - `Flow Port` (Conjugated Flow Port)
  - `networkType`: ElectricNetworkType
  - `ac`: AC Voltage
  - `dc`: DC Voltage
  - `tr`: Transformer
  - `Atomic Flow Ports`
9.3 UML Extensions

9.3.1 Diagram Extensions

9.3.1.1 FlowPort

A FlowPort is an interaction point through which input and/or output of items such as data, material, or energy may flow. The notation of flow port is a square on the boundary of the owning block or its usage. The label of the flow port is in the format `portName: portType`. Atomic flow ports have an arrow inside them indicating the direction of the port with respect to the owning Block. A nonatomic flow port has two open arrow heads facing away from each other (i.e., `< >`). The fill color of the square is white and the line and text colors are black, unless the flow port is conjugated, in which case the fill color of the square is black and the text is in white.

In addition, flow ports can be listed in a special compartment labeled “flow ports.” The format of each line is:

```
in | out | inout portName:portType [{conjugated}]
```

9.3.1.2 FlowProperty

A FlowProperty signifies a single flow element to/from a block. A flow property has the same notation as a Property only with a direction prefix (in | out | inout). Flow properties are listed in a compartment labeled “flowProperties.”

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ItemFlow</td>
<td></td>
<td>SysML::Ports&amp;Flows::ItemFlow</td>
</tr>
</tbody>
</table>
9.3.1.3 FlowSpecification

A FlowSpecification specifies inputs and outputs as a set of flow properties. It has a “flowProperties” compartment that lists the flow properties.

9.3.1.4 ItemFlow

An ItemFlow describes the flow of items across a connector or an association. The notation of an item flow is a black arrowhead on the connector or association. The arrowhead is towards the target element. For an item flow with an item property, the label shows the name and type of the item property (in name: type format). Otherwise the item flow is labeled with the name of the conveyed classifier.

9.3.2 Stereotypes

Package Ports&Flows

Figure 9.1 - Port Stereotypes
9.3.2.1 Block

Description

Blocks may own standard ports and/or flow ports. See Chapter 8, “Blocks” for further details of blocks.

9.3.2.2 FlowDirection

Description

FlowDirection is an enumeration type that defines literals used for specifying input and output directions. FlowDirection is used by flow properties to indicate if a property is an input or an output with respect to its owner.

Literal Values are

- in: Indicates that the flow property is input to the owning block.
- out: Indicates that the flow property is an output of the owning block.
- inout: Indicates that the flow property is both an input and an output of the owning block.

9.3.2.3 FlowPort

Description

A FlowPort is an interaction point through which input and/or output of items such as data, material, or energy may flow. This enables the owning block to declare which items it may exchange with its environment and the interaction points through which the exchange is made.
We distinguish between atomic flow port and a nonatomic flow port. Atomic flow ports relay items that are classified by a single Block, ValueType, DataType, or Signal classifier. A nonatomic flow port relays items of several types as specified by a FlowSpecification.

The distinction between atomic and nonatomic flow ports is made according to the flow port’s type: If a flow port is typed by a flow specification, then it is nonatomic; if a flow port is typed by a Block, ValueType, DataType or Signal classifier, then it is atomic.

Flow ports and associated flow specifications define “what can flow” between the block and its environment, whereas item flows specify “what does flow” in a specific usage context.

Flow ports relay items to/from the associated connector to/from properties of the owning block or parameters of the block behavior if the port is not connected to an internal link that may relay the items to an internal part of its owner. This means that every flow property contained within a flow port is bound to a property owned by the block or a parameter of the block behavior.

In case of flow properties or atomic flow ports of type Signal, inbound properties or atomic flow port are mapped to a Reception of the signal type (or a subtype) of the flow property's type. Outbound flow properties only declare the ability of the flow port to relay the signal over external connectors attached to it and are not mapped to a property of the flow port’s owning block.

The item flows specified as flowing on a connector between flow ports must match the flow properties of the ports at each end of the connector: the source of the item flow should be the port that has an outbound/bidirectional flow property that matches the item flow’s type and the target of the item flow should be the port that has an inbound/bidirectional flow property that matches the type of the item flow.

If a flow port is connected to multiple external and/or internal connectors, then the items are propagated (broadcast) over all connectors that have matching properties at the other end.

**Semantic Variation Points**

The binding of the flow properties on the ports to behavior parameters and/or block properties is a semantic variation point. One approach is to perform name and type matching. Another approach is to explicitly use binding relationships between the ports properties and behavior parameters or block properties.

**Attributes**

- /isAtomic : Boolean (derived)
  
  This is a derived attribute (derived from the flow port’s type). For a flow port typed by a flow specification the value of this attribute is False, otherwise the value is True.

- direction : FlowDirection
  
  Indicates the direction in which an atomic flow port relays its items. If the direction is set to “in,” then the items are relayed from an external connector via the flow port into the flow port’s owner (or one of its parts). If the direction is set to “out,” then the items are relayed from the flow port’s owner, via the flow port, through an external connector attached to the flow port. If the direction is set to “inout,” then items can flow both ways. By default, the value is inout.

- isConjugated : Boolean [0..1]
  
  Indicates if the flow of items of a nonatomic flow port maintain the directions specified in the flow specification or if the direction of every flow property specified in the flow specification is reversed (IN becomes OUT and vice versa).
If set to True, then all the directions of the flow properties specified by the flow specification that types a nonatomic flow port are relayed in the opposite direction (i.e., an “in” flow property is treated as an “out” flow property by the flow port and vice-versa). By default, the value is False. This attribute applies only to nonatomic flow ports since atomic flow ports have a direction attribute signifying the direction of the flow.

**Constraints**

[1] A FlowPort must be typed by a FlowSpecification, Block, Signal, DataType, or ValueType.

[2] If the FlowPort is atomic (by its type), then isAtomic=True, the direction must be specified (has a value), and isConjugated is not specified (has no value).

[3] If the FlowPort is nonatomic, and the FlowSpecification typing the port has flow properties with direction “in,” the FlowPort direction is “in” (or “out” if isConjugated=true). If the flow properties are all out, the FlowPort direction is out (or in if isConjugated=true). If flow properties are both in and out, the direction is inout.

[4] A FlowPort can be connected (via connectors) to one or more flow ports that have matching flow properties. The matching of flow properties is done in the following steps:

1. Type Matching: The type being sent is the same type or a sub-type of the type being received.
2. Direction Matching: If the connector connects two parts that are external to one another, then the direction of the flow properties must be opposite, or at least one of the ends should be inout. If the connector is internal to the owner of one of the flow ports, then the direction should be the same or at least one of the ends should be inout.
3. Name Matching: In case there is type and direction match to several flow properties at the other end, the property that has the same name at the other end is selected. If there is no such property, then the connection is ambiguous (ill-formed)

**9.3.2.4 FlowProperty**

**Description**

A FlowProperty signifies a single flow element that can flow to/from a block. A flow property’s values are either received from or transmitted to an external block. Flow properties are defined directly on blocks or flow specifications that are those specifications which type the flow ports.

Flow properties enable item flows across connectors connecting parts of the corresponding block types, either directly (in case of the property is defined on the block) or via flowPorts. For Block, Data Type and Value Type properties, setting an “out” FlowProperty value of a block usage on one end of a connector will result in assigning the same value of an “in” FlowProperty of a block usage at the other end of the connector, provided the flow properties are matched. Flow properties of type Signal imply sending and/or receiving of a signal usage. An “out” FlowProperty of type Signal means that the owning Block may broadcast the signal via connectors and an “in” FlowProperty means that the owning block is able to receive the Signal.

**Attributes**

- direction : FlowDirection
  
  Specifies if the property value is received from an external block (direction="in"), transmitted to an external Block (direction="out") or both (direction="inout").

**Constraints**

[1] A FlowProperty is typed by a ValueType, DataType, Block, or Signal.
An “in” FlowProperty value cannot be modified by its owning block.

### 9.3.2.5 FlowSpecification

**Description**

A FlowSpecification specifies inputs and outputs as a set of flow properties. A flow specification is used by flow ports to specify what items can flow via the port.

**Constraints**

1. Flow specifications cannot own operations or receptions (they can only own FlowProperties)
2. Every “ownedAttribute” of a FlowSpecification must be a FlowProperty

### 9.3.2.6 ItemFlow

**Description**

An ItemFlow describes the flow of items across a connector or an association. It may constrain the item exchange between blocks, block usages, or flow ports as specified by their flow properties. For example, a pump connected to a tank: the pump has an “out” flow property of type Liquid and the tank has an “in” FlowProperty of type Liquid. To signify that only water flows between the pump and the tank, we can specify an ItemFlow of type Water on the connector.

One can label an ItemFlow with the classifiers that may be conveyed. For example: a label Water would imply that usages of Water might be transmitted over this ItemFlow. In addition, if there is an item property (corresponds to the conveyed classifier), then one can label the item flow with the item property. For example, a label of “liquid: Water” would imply that the item flow relays Water and this relay is associated with an item property “liquid” of the item flow, i.e., the “liquid” item property is set once Water is relayed.

**Attributes**

- itemProperty: Property [0..1]
  
  An optional property that relates the flowing item to the instances of the connector’s enclosing block. This property is applicable only for item flows assigned to connectors. The multiplicity is zero if the item flow is assigned to an Association.

**Constraints**

1. A Connector or an Association, or an inherited Association must exist between the source and the target of the InformationFlow.
2. An ItemFlow itemProperty is typed by a Block or by a ValueType.
3. ItemProperty is a property of the block owning the source and the target.
4. The type of itemProperty should be the same or a sub-type of the conveyedClassifier.
5. Item property cannot have a value if there is only an association between the source and the target of the InformationFlow.
9.3.2.7 Standard Port

Description

A StandardPort is an interaction point through which a block provides and requires a set of services to and from its environment.

The services that a block provides to its environment via a standard port are specified by a set of provided interfaces. The services that a block requires from the environment via a standard port are specified by a set of required interfaces.

An interface may specify operations or signals. If the interface is provided, then external parts may call operations or send signals via the port to its owning block. If the interface is required, then the block may call operations or send signals via the port to its environment.

Standard ports are UML 2 ports. As a guideline, it is recommended that standard ports be used in the context of service-based components and/or architectures, either when specifying software components or applying a service-based approach to system specification.

9.4 Usage Examples

9.4.1 Standard Ports

Figure 9.3 is a fragment of the ibd:PwrSys diagram used in the HybridSUV sample (Annex B). The ecu:PowerControlUnit part has three standard ports, each connected to a standard port of another part. Each of the standard ports in this example has one provided and one required interface that specify the messages that can be sent via the ports. For example, the I_ICEcmds interface specifies the operations setMixture and setThrottle (Figure 9.4). This interface is provided by the ctrl port of InternalCombustionEngine and is required by the ice port of PowerControlUnit. Since the ecu:PowerControlUnit part and ice:InternalCombustionEngine part are connected via these ports, the ecu:PowerControlUnit part may send the messages setThrottle and setMixture to the ice:InternalCombustionEngine part from its ice port, across the connector to the ctrl port of ice:InternalCombustionEngine. By sending these messages, the PowerControlUnit can set the throttle and mixture of the InternalCombustionEngine. Inversely, the InternalCombustionEngine can report (notify) changes in its temperature, RPM and knockSensor by having the I_ICEData (Figure 9.4) as required interface on its ctrl port and connecting this port to the ice port of the PowerControlUnit where this interface is provided.
Figure 9.3 - Usage example of standard ports

Figure 9.4 - Interfaces “ctrl” standard port of InternalCombustionEngine
9.4.1.1 Atomic Flow Ports and Item Flows

Figure 9.5 is taken from the HybridSUV example in Annex B. Here we see how Fuel may flow between the FuelTankAssy and the InternalCombustionEngine. The FuelPump ejects Fuel via p1 port of FuelTankAssy, the Fuel flows across the fuelSupplyLine connector to the fuelFittingPort of InternalCombustionEngine and from there it is distributed via other atomic flow ports of type Fuel to internal parts of the engine. Some of the fuel is returned to the FuelTankAssy from the fuelFitting port across the fuelReturnLine connector. Note that it is possible to connect a single flow port to multiple connectors: in this example the direction of the flow via the fuelFitting port on the external connectors is implied by the direction of the flow ports on the other side of the fuel lines as well as by the directions of the item flows on the fuel lines. The direction of the flow on the internal connectors is implied by the direction of the atomic flow ports of the engine’s internal parts.

Figure 9.5 also shows the usage of ItemFlow, here each of the item flows has an item property (fuelSupply:Fuel and fuelReturn:Fuel) that signify the actual flow of fuel across the fuel lines.

Figure 9.5 - Usage of atomic flow ports in the HybridSUV Sample - ibd:FuelDist diagram
9.4.1.2 Non-Atomic Flow Ports and Flow Specification

Figure 9.6 taken from “Sample Problem” shows a way to connect the PowerControlUnit to other parts over a CAN bus. Since connections over buses are characterized by broadcast asynchronous communications, flow ports are used to connect the parts to the CAN bus. To specify the flow between the flow ports, we need to specify flow specifications as done in Figure 9.7. Here the flow specification has three flow properties: an “out” flow property of type signal (ICEData) and two “in” flow properties of type float. This allows the InternalCombustionEngine to transmit an ICEData signal via its fp flow port which will be transmitted over the CAN bus to the ice port of PowerControlUnit (a conjugated flow port typed by the FS_ICE flow specification). This single signal carries the temperature, rpm, and knockSensor information of the engine. In addition, the PowerControlUnit can set the mixture and throttle of the InternalCombustionEngine via the mixture and throttlePosition flow properties of the FS_ICE flow specification.

Figure 9.6 - Using flow ports to connect the PowerControlUnit to the ElectricalPowerController, Transmission, and InternalCombustionEngine over a CAN bus
Figure 9.7 - Flow specification for the InternalCombustionEngine flow port to allow its connection over the CAN bus
10 Constraint Blocks

10.1 Overview

Constraint blocks provide a mechanism for integrating engineering analysis such as performance and reliability models with other SysML models. Constraint blocks can be used to specify a network of constraints that represent mathematical expressions such as \( F = m \times a \) and \( a = \frac{dv}{dt} \), which constrain the physical properties of a system. Such constraints can also be used to identify critical performance parameters and their relationships to other parameters, which can be tracked throughout the system life cycle.

A constraint block includes the constraint, such as \( F = m \times a \), and the parameters of the constraint such as \( F \), \( m \), and \( a \). Constraint blocks define generic forms of constraints that can be used in multiple contexts. For example, a definition for Newton’s Laws may be used to specify these constraints in many different contexts. Reusable constraint definitions may be specified on block definition diagrams and packaged into general-purpose or domain-specific model libraries. Such constraints can be arbitrarily complex mathematical or logical expressions. The constraints can be nested to enable a constraint to be defined in terms of more basic constraints such as primitive mathematical operators.

Parametric diagrams include usages of constraint blocks to constrain the properties of another block. The usage of a constraint binds the parameters of the constraint, such as \( F \), \( m \), and \( a \), to specific properties of a block, such as a mass, that provide values for the parameters. The constrained properties, such as mass or response time, typically have simple value types that may also carry units, dimensions, and probability distributions. A pathname dot notation can be used to refer to nested properties within a block hierarchy. This allows a value property (such as an engine displacement) that may be deeply nested within a containing hierarchy (such as vehicle, power system, engine) to be referenced at the outer containing level (such as vehicle-level equations). The context for the usages of constraint blocks must also be specified in a parametric diagram to maintain the proper namespace for the nested properties.

Time can be modeled as a property that other properties may be dependent on. A time reference can be established by a local or global clock that produces a continuous or discrete time value property. Other values of time can be derived from this clock, by introducing delays and/or skew into the value of time. Discrete values of time as well as calendar time can be derived from this global time property. SysML includes the time model from UML, but other UML specifications offer more specialized descriptions of time that may also apply to specific needs.

A state of the system can be specified in terms of the values of some of its properties. For example, when water temperature is below 0 degrees Celsius, it may change from liquid to solid state. In this example, the change in state results in a different set of constraint equations. This can be accommodated by specifying constraints that are conditioned on the value of the state property.

Parametric diagrams can be used to support trade-off analysis. A constraint block can define an objective function to compare alternative solutions. The objective function can constrain measures of effectiveness or merit and may include a weighting of utility functions associated with various criteria used to evaluate the alternatives. These criteria, for example, could be associated with system performance, cost, or desired physical characteristics. Properties bound to parameters of the objective function may have probability distributions associated with them that are used to compute expected or probabilistic measures of the system. The use of an objective function and measures of effectiveness in parametric diagrams are included in Annex C: Non-normative Extensions.

SysML identifies and names constraint blocks, but does not specify a computer interpretable language for them. The interpretation of a given constraint block (e.g., a mathematical relation between its parameter values) must be provided. An expression may rely on other mathematical description languages both to capture the detailed specification of
mathematical or logical relations, and to provide a computational engine for these relations. In addition, the block constraints are non-causal and do not specify the dependent or independent variables. The specific dependent and independent variables are often defined by the initial conditions, and left to the computational engine.

A constraint block is defined by a keyword of «constraint» applied to a block definition. The properties of this block define the parameters of the constraint. The usage of a constraint block is distinguished from other parts by a box having rounded corners rather than the square corners of an ordinary part. A parametric diagram is a restricted form of internal block diagram that shows only the use of constraint blocks along with the properties they constrain within a context.

10.2 Diagram Elements

10.2.1 Block Definition Diagram

The diagram elements described in this section are additions to the Block Definition Diagram described in Chapter 8, “Blocks.”

Table 10.1 - Graphical nodes defined in Block Definition diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Metamodel Reference</th>
</tr>
</thead>
</table>
| ConstraintBlock | ✔️constraint
  ConstraintBlock1
  constraints
  {{[L-1]} x > y}
  nested: ConstraintBlock2
  parameters
  x: Real
  y: Real | SysML::ConstraintBlocks::ConstraintBlock |
10.2.2 Parametric Diagram

The diagram elements described in this section are additions to the Internal Block Diagram described in Chapter 8, “Blocks.” The Parametric Diagram includes all of the notations of an Internal Block Diagram, subject only to the restrictions described in Section 10.3.1.2.

Table 10.2 - Graphical nodes defined in Parametric diagrams.

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax Example</th>
<th>Metamodel Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ParametricDiagram</td>
<td>par Block1</td>
<td>SysML::ConstraintBlocks::ConstraintBlock</td>
</tr>
<tr>
<td></td>
<td>length: Real</td>
<td>SysML::Blocks::Block</td>
</tr>
<tr>
<td></td>
<td>width: Real</td>
<td></td>
</tr>
<tr>
<td></td>
<td>x: Real</td>
<td></td>
</tr>
<tr>
<td></td>
<td>y: Real</td>
<td></td>
</tr>
<tr>
<td></td>
<td>C1: Constraint1</td>
<td></td>
</tr>
<tr>
<td>ConstraintProperty</td>
<td>x: Real</td>
<td>SysML::ConstraintBlocks::ConstraintProperty</td>
</tr>
<tr>
<td></td>
<td>C1: Constraint1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>y: Real</td>
<td></td>
</tr>
<tr>
<td></td>
<td>«constraint» C1: Constraint1</td>
<td></td>
</tr>
</tbody>
</table>

10.3 UML Extensions

10.3.1 Diagram Extensions

10.3.1.1 Block Definition Diagram

Constraint block definition

The «constraint» keyword on a block definition states that the block is a constraint block. An expression that specifies the constraint may appear in the constraints compartment of the block definition, using either formal statements in some language, or informal statements using text. This expression can include a formal reference to a language in braces as indicated in Table 10.1. Parameters of the constraint may be shown in a compartment with the predefined compartment label “parameters.”
Parameters compartment

Constraint blocks support a special form of compartment, with the label “parameters,” which may contain declarations for some or all of its constraint parameters. Properties of a constraint block should be shown either in the constraints compartment, for nested constraint properties, or within the parameters compartment.

10.3.1.2 Parametric Diagram

A parametric diagram is defined as a restricted form of internal block diagram. A parametric diagram may contain constraint properties and their parameters, along with other properties from within the internal block context. All properties that appear, other than the constraints themselves, must either be bound directly to a constraint parameter, or contain a property that is bound to one (through any number of levels of containment).

Round-cornered rectangle notation for constraint property

A constraint property may be shown on a parametric diagram using a rectangle with rounded corners. This graphical shape distinguishes a constraint property from all other properties and avoids the need to show an explicit «constraint» keyword. Otherwise, this notation is equivalent to the standard form of an internal property with a «constraint» keyword shown. Compartments and internal properties may be shown within the shape just as for other types of internal properties.

«constraint» keyword notation for constraint property

A constraint property may be shown on a parametric diagram using a standard form of internal property rectangle with the «constraint» keyword preceding its name. Parameters are shown within a constraint property using the standard notations for internal properties. The stereotype ConstraintProperty is applied to a constraint property, but only the shorthand keyword «constraint» is used when shown on an internal property.

Small square box notation for an internal property

A value property may optionally be shown by a small square box, with the name and other specifications appearing in a text string close to the square box. The text string for such a value property may include all the elements that could ordinarily be used to declare the property in a compartment of a block, including an optional default value. The box may optionally be shown with one edge flush with the boundary of a containing property. Placement of property boxes is purely for notational convenience, for example to enable simpler connection from the outside, and has no semantic significance. If a connector is drawn to a region where an internal property box is shown flush with the boundary of a containing property, the connector is always assumed to connect to the innermost property.

10.3.2 Stereotypes

Package ConstraintBlocks

Figure 10.1 Stereotypes defined in SysML ConstraintBlocks package
10.3.2.1 ConstraintBlock

Description

A constraint block is a block that packages the statement of a constraint so it may be applied in a reusable way to constrain properties of other blocks. A constraint block typically defines one or more constraint parameters, which are bound to properties of other blocks in a surrounding context where the constraint is used. Binding connectors, as defined in Chapter 8: Blocks, are used to bind each parameter of the constraint block to a property in the surrounding context. All properties of a constraint block are constraint parameters, with the exception of constraint properties that hold internally nested usages of other constraint blocks.

Constraints

[1] A constraint block may not own any structural or behavioral elements beyond the properties that define its constraint parameters, constraint properties that hold internal usages of constraint blocks, binding connectors between its internally nested constraint parameters, constraint expressions that define an interpretation for the constraint block, and general-purpose model management and crosscutting elements.

10.3.2.2 ConstraintProperty

Description

A constraint property is a property of any block that is typed by a constraint block. It holds a localized usage of the constraint block. Binding connectors may be used to bind the parameters of this constraint block to other properties of the block that contains the usage.

Constraints

[1] A property to which the ConstraintProperty stereotype is applied must be owned by a SysML Block.
[2] The ConstraintProperty stereotype must be applied to any property of a SysML Block that is typed by a ConstraintBlock.

10.4 Usage Examples

10.4.1 Definition of Constraint Blocks on a Block Definition Diagram

Constraint blocks can only be defined on a block definition diagram or a package diagram, where they must have the «constraint» keyword shown. The strings in braces in the compartment labeled “constraints” are ordinary UML constraints, using a special compartment to hold the constraint. This is shown in Figure 10.2. These particular constraints are specified only in an informal language, but a more formal language such as OCL or MathML could also be used. The compartment labeled “parameters” shows the parameters of this constraint, which are bound on the parametric diagram.
10.4.2 Usage of Constraint Blocks on a Parametric Diagram

Figure 10.3 shows the use of constraint properties on a parametric diagram (note that this is a subset of the corresponding diagram in the sample problem). This diagram shows the use of nested property references to the properties of the parts; parametric diagrams can make use of the nested property name notation to refer to multiple levels of nested property containment, as shown in this example. A parametric diagram is similar to an internal block diagram with the exception that the only connectors that may be shown are binding connectors connected to constraint parameters on at least one end. The Sample Problem in Annex B provides definitions of the containing EconomyContext block for which this parametric diagram is shown.
Figure 10.3 - Usage of constraint blocks on a parametric diagram
Part III - Behavioral Constructs

This Part specifies the dynamic, behavioral constructs used in SysML behavioral diagrams, including the activity diagram, sequence diagram, state machine diagram, and use case diagram. This part includes the following chapters:

Chapter 11 - Activities - defines the extensions to UML 2 activities, which represent the basic unit of behavior that is used in activity, sequence, and state machine diagrams. The activity diagram is used to describe the flow of control and flow of inputs and outputs among actions.

Chapter 12 - Interactions - defines the constructs for describing message based behavior used in sequence diagrams.

Chapter 13 - State Machines - describes the constructs used to specify state based behavior in terms of system states and their transitions.

Chapter 14 - Use Cases - describes behavior in terms of the high level functionality and uses of a system, that are further specified in the other behavioral diagrams referred to above.
11 Activities

11.1 Overview

Activity modeling emphasizes the inputs, outputs, sequences, and conditions for coordinating other behaviors. It provides a flexible link to blocks owning those behaviors. The following is a summary of the SysML extensions to UML 2.1 Activity diagrams. For additional information, see extensions for Enhanced Functional Flow Block Diagrams in Annex C, “Non-normative Extensions,” Section C.1, “Activity Diagram Extensions.”

11.1.1 Control as Data

SysML extends control in activity diagrams as follows.

- In UML 2.1 Activities, control can only enable actions to start. SysML extends control to support disabling of actions that are already executing. This is accomplished by providing a model library with a type for control values that are treated like data (see ControlValue in Figure 11.9).
- A control value is an input or output of a control operator, which is how control acts as data. A control operator can represent a complex logical operation that transforms its inputs to produce an output that controls other actions (see ControlOperator in Figure 11.8).

11.1.2 Continuous Systems

SysML provides extensions that might be very loosely grouped under the term “continuous,” but are generally applicable to any sort of distributed flow of information and physical items through a system. These are:

- Restrictions on the rate at which entities flow along edges in an activity, or in and out of parameters of a behavior (see Rate in Figure 11.8). This includes both discrete and continuous flows, either of material, energy, or information. Discrete and continuous flows are unified under rate of flow, as is traditionally done in mathematical models of continuous change, where the discrete increment of time approaches zero.

- Extension of object nodes, including pins, with the option for newly arriving values to replace values that are already in the object nodes (see Overwrite in Figure 11.8). SysML also extends object nodes with the option to discard values if they do not immediately flow downstream (see NoBuffer in Figure 11.8). These two extensions are useful for ensuring that the most recent information is available to actions by indicating when old values should not be kept in object nodes, and for preventing fast or continuously flowing values from collecting in an object node, as well as modeling transient values, such as electrical signals.

11.1.3 Probability

SysML introduces probability into activities as follows (see Probability in Figure 11.8):

- Extension of edges with probabilities for the likelihood that a value leaving the decision node or object node will traverse an edge.
- Extension of output parameter sets with probabilities for the likelihood that values will be output on a parameter set.
11.1.4 Activities as Classes

In UML 2.1, all behaviors including activities are classes, and their instances are executions. Behaviors can appear on block definition and class diagrams, and participate in generalization and associations. SysML clarifies the semantics of composition association between activities, and between activities and the type of object nodes in the activities, and defines consistency rules between these diagrams and activity diagrams. See Section 11.3.1.1, “Activity.”

11.1.5 Timelines

The simple time model in UML can be used to represent timing and duration constraints on actions in an activity model. These constraints can be notated as constraint notes in an activity diagram. Although the UML 2 timing diagram was not included in this version of SysML, it can complement SysML behavior diagrams to notate this information. More sophisticated SysML modeling techniques can incorporate constraint blocks from Chapter 10, “Constraint Blocks” to specify resource and related constraints on the properties of the inputs, outputs, and other system properties. (Note: refer to Section 11.3.1.4, “ObjectNode” for constraining properties of object nodes).

11.2 Diagram Elements

11.2.1 Activity Diagram

Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Action, CallBehaviorAction, AcceptEventAction, SendSignalAction</td>
<td><img src="image" alt="Activity Diagram" /></td>
<td>UML4SysML::Action, UML4SysML::CallBehaviorAction, UML4SysML::AcceptEventAction, UML4SysML::SendSignalAction</td>
</tr>
<tr>
<td>Activity</td>
<td><img src="image" alt="Activity Diagram" /></td>
<td>UML4SysML::Activity</td>
</tr>
</tbody>
</table>
Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ActivityFinal</td>
<td></td>
<td>UML4SysML::ActivityFinalNode</td>
</tr>
<tr>
<td>ActivityNode</td>
<td>See ControlNode and ObjectNode.</td>
<td>UML4SysML::ActivityNode</td>
</tr>
<tr>
<td>ActivityParameterNode</td>
<td></td>
<td>UML4SysML::ActivityParameterNode</td>
</tr>
<tr>
<td>ControlNode</td>
<td>See DecisionNode, FinalNode, ForkNode, InitialNode, JoinNode, and MergeNode.</td>
<td>UML4SysML::ControlNode</td>
</tr>
<tr>
<td>ControlOperator</td>
<td></td>
<td>SysML::Activities::ControlOperator</td>
</tr>
<tr>
<td>DecisionNode</td>
<td></td>
<td>UML4SysML::DecisionNode</td>
</tr>
<tr>
<td>FinalNode</td>
<td>See ActivityFinal and FlowFinal.</td>
<td>UML4SysML::FinalNode</td>
</tr>
<tr>
<td>FlowFinal</td>
<td></td>
<td>UML4SysML::FlowFinalNode</td>
</tr>
</tbody>
</table>
Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ForkNode</td>
<td>![ForkNode Diagram]</td>
<td>UML4SysML::ForkNode</td>
</tr>
<tr>
<td>InitialNode</td>
<td>![InitialNode Diagram]</td>
<td>UML4SysML::InitialNode</td>
</tr>
<tr>
<td>JoinNode</td>
<td>![JoinNode Diagram] (joinspec=...)</td>
<td>UML4SysML::JoinNode</td>
</tr>
<tr>
<td>isControl</td>
<td>![isControl Diagram] (control)</td>
<td>UML4SysML::Pin.isControl</td>
</tr>
<tr>
<td>isStream</td>
<td>![isStream Diagram] (stream)</td>
<td>UML4SysML::Parameter.isStream</td>
</tr>
</tbody>
</table>

Note: The diagrams are not included in this text representation.
Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Local pre- and postconditions</td>
<td><img src="#" alt="Diagram" /></td>
<td>UML4SysML::Action.local Precondition, UML4SysML::Action.local Postcondition</td>
</tr>
<tr>
<td>MergeNode</td>
<td><img src="#" alt="Diagram" /></td>
<td>UML4SysML::MergeNode</td>
</tr>
<tr>
<td>NoBuffer</td>
<td><img src="#" alt="Diagram" /></td>
<td>SysML::Activities::NoBuffer</td>
</tr>
<tr>
<td>ObjectNode</td>
<td><img src="#" alt="Diagram" /></td>
<td>UML4SysML::ObjectNode and its children, SysML::Activities::ObjectNode</td>
</tr>
<tr>
<td>Optional</td>
<td><img src="#" alt="Diagram" /></td>
<td>SysML::Activities::Optional</td>
</tr>
</tbody>
</table>
Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>OverWrite</td>
<td></td>
<td>SysML::Activities::Overwrite</td>
</tr>
<tr>
<td>ParameterSet</td>
<td></td>
<td>UML4SysML::ParameterSet</td>
</tr>
<tr>
<td>Probability</td>
<td></td>
<td>SysML::Activities::Probability</td>
</tr>
</tbody>
</table>

**OverWrite**

```
{ probability = valueSpecification }
```

**ParameterSet**

```
{ probability = valueSpecification }
```

**Probability**

```
{ probability = valueSpecification }
```

```
{ probability = valueSpecification }
```
### Table 11.1 - Graphical nodes included in activity diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rate</td>
<td></td>
<td>SysML::Activities::Rate, SysML::Activities::Continuous, SysML::Activities::Discrete</td>
</tr>
</tbody>
</table>

![Node Diagram](image1)

### Table 11.2 - Graphical paths included in activity diagrams

<table>
<thead>
<tr>
<th>Path Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ActivityEdge</td>
<td>See ControlFlow and ObjectFlow</td>
<td>UML4SysML::ActivityEdge</td>
</tr>
<tr>
<td>ControlFlow</td>
<td></td>
<td>UML4SysML::ControlFlow SysML::Activities::ControlFlow</td>
</tr>
</tbody>
</table>

![Path Diagram](image2)
Table 11.2 - Graphical paths included in activity diagrams

<table>
<thead>
<tr>
<th>Path Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>ObjectFlow</td>
<td><img src="image" alt="ObjectFlow Diagram" /></td>
<td>UML4SysML::ObjectFlow</td>
</tr>
<tr>
<td>Probability</td>
<td><img src="image" alt="Probability Diagram" /></td>
<td>SysML::Activities::Probability</td>
</tr>
<tr>
<td>Rate</td>
<td><img src="image" alt="Rate Diagram" /></td>
<td>SysML::Activities::Rate,</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SysML::Activities::Continuous,</td>
</tr>
<tr>
<td></td>
<td></td>
<td>SysML::Activities::Discrete</td>
</tr>
</tbody>
</table>
### 11.3 UML Extensions

#### 11.3.1 Diagram Extensions

The following specify diagram extensions to the notations defined in Chapter 17: Profiles & Model Libraries.

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>In Block Definition Diagrams, Activity, Association</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Activities, Diagram Usage for Block Definition Diagrams</td>
</tr>
<tr>
<td>ActivityPartition</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::ActivityPartition</td>
</tr>
<tr>
<td>InterruptibleActivity Region</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::InterruptibleActivity-Region</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>In Block Definition Diagrams, Activity, Association</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Activities, Diagram Usage for Block Definition Diagrams</td>
</tr>
<tr>
<td>ActivityPartition</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::ActivityPartition</td>
</tr>
<tr>
<td>InterruptibleActivity Region</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::InterruptibleActivity-Region</td>
</tr>
</tbody>
</table>

#### Table 11.3 - Other graphical elements included in activity diagrams

<table>
<thead>
<tr>
<th>Element Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>In Block Definition Diagrams, Activity, Association</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Activities, Diagram Usage for Block Definition Diagrams</td>
</tr>
<tr>
<td>ActivityPartition</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::ActivityPartition</td>
</tr>
<tr>
<td>InterruptibleActivity Region</td>
<td><img src="image" alt="Diagram" /></td>
<td>UML4SysML::InterruptibleActivity-Region</td>
</tr>
</tbody>
</table>
### 11.3.1.1 Activity

#### Notation

In UML 2.1, all behaviors are classes, including activities, and their instances are executions of the activity. This follows the general practice that classes define the constraints under which the instances must operate. Creating an instance of an activity causes the activity to start executing, and vice versa. Destroying an instance of an activity terminates the corresponding execution, and vice versa. Terminating an execution also terminates the execution of any other activities that it invoked synchronously, that is, expecting a reply.

Activities as classes can have associations between each other, including composition associations. Composition means that destroying an instance at the whole end destroys instances at the part end. When composition is used with activity classes, the termination of execution of an activity on the whole end will terminate executions of activities on the part end of the links.

Combining the two aspects above, when an activity invokes other activities, they can be associated by a composition association, with the invoking activity on the whole end, and the invoked activity on the part end. If an execution of an activity on the whole end is terminated, then the executions of the activities on the part end are also terminated. The upper multiplicity on the part end restricts the number of concurrent synchronous executions of the behavior that can be invoked by the containing activity. The lower multiplicity on the part end is always zero, because there will be some time during the execution of the containing activity that the lower level activity is not executing. See Constraints sections below.

Activities in block definition diagrams appear as regular blocks, except the «activity» keyword may be used to indicate the Block stereotype is applied to an activity, as shown in Figure 11.1. See example in “Usage Examples” on page 102. This provides a means for representing activity decomposition in a way that is similar to classical functional decomposition hierarchies. The names of the CallBehaviorActions that correspond to the association can be used as end names of the association on the part end. Activities in block definition diagrams can also appear with the same notation as CallBehaviorAction, except the rake notation can be omitted, if desired. Also see use of activities in block definition diagrams that include ObjectNodes.

![Image](image.png)

**Figure 11.1 - Block definition diagram with activities as blocks**

#### Constraints

The following constraints apply when composition associations in block definition diagrams are defined between activities:

1. **One-to-One Multiplicity Constraints**: The upper multiplicity on the part end restricts the number of concurrent synchronous executions of the behavior that can be invoked by the containing activity. The lower multiplicity on the part end is always zero, because there will be some time during the execution of the containing activity that the lower level activity is not executing.

---

**OMG SysML™, Version 1.0**
[1] The part end name must be the same as the name of a synchronous CallBehaviorAction in the composing activity. If the action has no name, and the invoked activity is only used once in the calling activity, then the end name is the same as name of the invoked activity.

[2] The part end activity must be the same as the activity invoked by the corresponding CallBehaviorAction.

[3] The lower multiplicity at the part end must be zero.

[4] The upper multiplicity at the part end must be 1 if the corresponding action invokes a nonreentrant behavior.

11.3.1.2 CallBehaviorAction

Stereotypes applied to behaviors may appear on the notation for CallBehaviorAction when invoking those behaviors, as shown in Figure 11.2.

```
«stereotype name»
  behavior name
```

Figure 11.2 - CallBehaviorAction notation with behavior stereotype

CallBehaviorActions in activity diagrams may optionally show the action name with the name of the invoked behavior using the colon notation shown in Figure 11.3.

```
action name : behavior name
```

Figure 11.3 - CallBehaviorAction notation with action name

11.3.1.3 ControlFlow

Presentation Option

Control flow may be notated with a dashed line and stick arrowhead, as shown in Figure 11.4.

```
Action --- Action
```

Figure 11.4 - Control flow notation
### 11.3.1.4 ObjectNode

**Notation**

See Section 11.3.1.1, “Activity” concerning activities appearing in block definition diagrams. Associations can be used between activities and classifiers (blocks or datatypes) that are the type of object nodes in the activity, as shown in Figure 11.5. This supports linking the execution of the activity with items that are flowing through the activity and happen to be contained by the object node at the time the link exists. The names of the object node that correspond to the association can be used as end names of the association on the end towards the object node type. Like any association end or property these can be the subject of parametric constraints, design values, units, and dimensions. The upper multiplicity on the object node end restricts the number of instances of the item type that can reside in the object node at one time, which must be lower than the maximum amount allowed by the object node itself. The lower multiplicity on the object node end is always zero, because there will be some time during the execution of the containing activity that there is no item in the object node. The associations may be composition if the intention is to delete instances of the classifier flowing the activity when the activity is terminated. See example in “Usage Examples” on page 102.

![Diagram showing associations between activities and object nodes](image)

*Figure 11.5 - Class or block definition diagram with activities as classes associated with types of object nodes*

Object nodes in activity diagrams can optionally show the node name with the name of the type of the object node as shown in Figure 11.6.

![Diagram showing object node notation](image)

*Figure 11.6 - ObjectNode notation in activity diagrams*
Stereotypes applying to parameters can appear on object nodes in activity diagrams, as shown in Figure 11.7, when the object node notation is used as a shorthand for pins. The stereotype applies to all parameters corresponding to the pins notated by the object node. Stereotype applying to object nodes can also appear in object nodes, and applies to all the pins notated by the object node.

**Figure 11.7 - ObjectNode notation in activity diagrams**

**Constraints**

The following constraints apply when associations in block definition diagrams are defined between activities and classifiers typing object nodes:

1. The end name towards the object node type is the same as the name of an object node in the activity at the other end.
2. The classifier must be the same as the type of the corresponding object node.
3. The lower multiplicity at the object node type end must be zero.
4. The upper multiplicity at the object node type end must be equal to the upper bound of the corresponding object node.

**11.3.2 Stereotypes**

The following abstract syntax defines the stereotypes in this chapter and which metaclasses they extend. The descriptions, attributes, and constraints for each stereotype are specified below.
11.3.2.1 Continuous

Continuous rate is a special case of rate of flow (see Rate) where the increment of time between items approaches zero. It is intended to represent continuous flows that may correspond to water flowing through a pipe, a time continuous signal, or continuous energy flow. It is independent from UML streaming, see “Rate” on page 101. A streaming parameter may or may not apply to continuous flow, and a continuous flow may or may not apply to streaming parameters.

UML places no restriction on the rate at which tokens flow. In particular, the time between tokens can approach as close to zero as needed, for example to simulate continuous flow. There is also no restriction in UML on the kind of values that flow through an activity. In particular, the value may represent as small a number as needed, for example to simulate continuous material or energy flow. Finally, the exact timing of token flow is not completely prescribed in UML. In particular, token flow on different edges may be coordinated to occur in a clocked fashion, as in time march algorithms for numerical solvers of ordinary differential equations, such as Runge-Kutta.
11.3.2.2 ControlOperator

**Description**

A control operator is a behavior that is intended to represent an arbitrarily complex logical operator that can be used to enable and disable other actions. When the «controlOperator» stereotype is applied to behaviors, the behavior takes control values as inputs or provides them as outputs, that is, it treats control as data (see “ControlValue” on page 101). When the «controlOperator» stereotype is not applied, the behavior may not have a parameter typed by ControlValue. The «controlOperator» stereotype also applies to operations with the same semantics.

The control value inputs do not enable or disable the control operator execution based on their value, they only enable based on their presence as data. Pins for control parameters are regular pins, not UML control pins. This is so the control value can be passed into or out of the action and the invoked behavior, rather than control the starting of the action, or indicating the ending of it.

**Constraints**

[1] When the «controlOperator» stereotype is applied, the behavior or operation must have at least one parameter typed by ControlValue. If the stereotype is not applied, the behavior or operation may not have any parameter typed by ControlValue.

[2] A behavior must have the «controlOperator» stereotype applied if it is a method of an operation that has the «controlOperator» stereotype applied.

11.3.2.3 Discrete

**Description**

Discrete rate is a special case of rate of flow (see Rate) where the increment of time between items is non-zero. Examples include the production of assemblies in a factory and signals set at periodic time intervals.

**Constraints**

[1] The «discrete» and «continuous» stereotypes cannot be applied to the same element at the same time.

11.3.2.4 NoBuffer

**Description**

When the «nobuffer» stereotype is applied to object nodes, tokens arriving at the node are discarded if they are refused by outgoing edges, or refused by actions for object nodes that are input pins. This is typically used with fast or continuously flowing data values, to prevent buffer overrun, or to model transient values, such as electrical signals. For object nodes that are the target of continuous flows, «nobuffer» and «overwrite» have the same effect. The stereotype does not override UML token offering semantics; it just indicates what happens to the token when it is accepted. When the stereotype is not applied, the semantics are as in UML, specifically, tokens arriving at an object node that are refused by outgoing edges, or action for input pins, are held until they can leave the object node.

**Constraints**

[1] The «nobuffer» and «overwrite» stereotypes cannot be applied to the same element at the same time.
11.3.2.5 Overwrite

Description

When the «overwrite» stereotype is applied to object nodes, a token arriving at a full object node replaces the ones already there (a full object node has as many tokens as allowed by its upper bound). This is typically used on an input pin with an upper bound of 1 to ensure that stale data is overridden at an input pin. For upper bounds greater than one, the token replaced is the one that would be the last to be selected according to the ordering kind for the node. For FIFO ordering, this is the most recently added token, for LIFO it is the least recently added token. A null token removes all the tokens already there. The number of tokens replaced is equal to the weight of the incoming edge, which defaults to 1. For object nodes that are the target of continuous flows, «overwrite» and «nobuffer» have the same effect. The stereotype does not override UML token offering semantics, just indicates what happens to the token when it is accepted. When the stereotype is not applied, the semantics is as in UML, specifically, tokens arriving at object nodes do not replace ones that are already there.

Constraints

[1] The «overwrite» and «nobuffer» stereotypes cannot be applied to the same element at the same time.

11.3.2.6 Optional

Description

When the «optional» stereotype is applied to parameters, the lower multiplicity must be equal to zero. This means the parameter is not required to have a value for the activity or any behavior to begin execution. Otherwise, the lower multiplicity must be greater than zero, which is called “required.” The absence of this stereotype indicates a constraint, see below.

Constraints

[1] A parameter with the «optional» stereotypes applied must have multiplicity.lower equal to zero, otherwise multiplicity.lower must be greater than zero.

11.3.2.7 Probability

Description

When the «probability» stereotype is applied to edges coming out of decision nodes and object nodes, it provides an expression for the probability that the edge will be traversed. These must be between zero and one inclusive, and add up to one for edges with same source at the time the probabilities are used.

When the «probability» stereotype is applied to output parameter sets, it gives the probability the parameter set will be given values at runtime. These must be between zero and one inclusive, and add up to one for output parameter sets of the same behavior at the time the probabilities are used.

Constraints

[1] The «probability» stereotype can only be applied to activity edges that have decision nodes or object nodes as sources, or to output parameter sets.

[2] When the «probability» stereotype is applied to an activity edge, then it must be applied to all edges coming out of the same source.
When the «probability» stereotype is applied to an output parameter set, it must also be applied to all the parameter sets of the behavior or operation owning the original parameter set.

When the «probability» stereotype is applied to an output parameter set, all the output parameters must be in some parameter set.

11.3.2.8 Rate

Description

When the «rate» stereotype is applied to an activity edge, it specifies the expected value of the number of objects and values that traverse the edge per time interval, that is, the expected value rate at which they leave the source node and arrive at the target node. It does not refer to the rate at which a value changes over time. When the stereotype is applied to a parameter, the parameter must be streaming, and the stereotype gives the number of objects or values that flow in or out of the parameter per time interval while the behavior or operation is executing. Streaming is a characteristic of UML behavior parameters that supports the input and output of items while a behavior is executing, rather than only when the behavior starts and stops. The flow may be continuous or discrete, see the specialized rates in 11.3.2.1 ("Continuous") and "Discrete" on page 99. The «rate» stereotype has a rate property of type ValueSpecification. The values of this property must be instances of classifiers stereotyped by «valueType» or «distributionDefinition», see Chapter 8, “Blocks.” In particular, the denominator for units used in the rate property must be time units.

Constraints

[1] When the «rate» stereotype is applied to a parameter, the parameter must be streaming.

[2] The rate of a parameter must be less than or equal to rates on edges that come into or go out from pins and parameters nodes corresponding to the parameter.

11.3.3 Model Libraries

The SysML model library for activities is shown in Figure 11.9.

![ControlValue](https://example.com/controlvalues.png)

**Figure 11.9 - Control values.**

11.3.3.1 ControlValue

Description

The ControlValue enumeration is a type for treating control values as data (see Section 11.3.2.2) and for UML control pins. It can be used as the type of behavior and operation parameters, object nodes, and attributes, and so on. The possible runtime values are given as enumeration literals. Modelers can extend the enumeration with additional literals, such as suspend, resume, with their own semantics.

The disable literal means a termination of an executing behavior that can only be started again from the beginning (compare to suspend). The enable literal means to start a new execution of a behavior (compare to resume).
Constraints

[1] UML4SysML::ObjectNode::isControlType is true for object nodes with type ControlValue.

11.4 Usage Examples

The following examples illustrate modeling continuous systems (see Continuous Systems in Section 11.1.1). Figure 11.10 shows a simplified model of driving and braking in a car that has an automatic braking system. Turning the key on starts two behaviors, Driving and Braking. These behaviors execute until the key is turned off, using streaming parameters to communicate with other behaviors. The Driving behavior outputs a brake pressure continuously to the Braking behavior while both are executing, as indicated by the «continuous» rate and streaming properties (streaming is a characteristic of UML behavior parameters that supports the input and output of items while a behavior is executing, rather than only when the behavior starts and stops). Brake pressure information also flows to a control operator that outputs a control value to enable or disable the Monitor Traction behavior. No control pins are used on Monitor Traction, so once it is enabled, the continuously arriving enable control values from the control operator have no effect, per UML semantics. When the brake pressure goes to zero, disable control values are emitted from the control operator. The first one disables the monitor, and the rest have no effect. While the monitor is enabled, it outputs a modulation frequency for applying the brakes as determined by the ABS system. The brake notations on the control operator and Monitor Traction indicate they are further defined by activities, as shown in Figures 11.11 and 11.12. An alternative notation for this activity decomposition is shown in Figure 11.13.
The activity diagram for Monitor Traction is shown in Figure 11.11. When Monitor Traction is enabled, it begins listening for signals coming in from the wheel and accelerometer, as indicated by the signal receipt symbols on the left, which begin listening automatically when the activity is enabled. A traction index is calculated every 10 ms, which is the slower of the two signal rates. The accelerometer signals come in continuously, which means the input to Calculate Traction does not buffer values. The result of Calculate Traction is filtered by a decision node for a threshold value and Calculate Modulation Frequency determines the output of the activity.

Figure 11.10 - Continuous system example 1
The activity diagram for the control operator Enable on Brake Pressure > 0 is shown in Figure 11.12. The decision node and guards determine if the brake pressure is greater than zero, and flow is directed to value specification actions that output an enabling or disabling control value from the activity. The edges coming out of the decision node indicate the probability of each branch being taken.

Figure 11.13 shows a block definition diagram with composition associations between the activities in Figures 11.10, 11.11, and 11.12, as an alternative way to show the activity decomposition of Figures 11.10, 11.11, and 11.12. Each instance of Operating Car is an execution of that behavior. It owns the executions of the behaviors it invokes synchronously, such as Driving. Like all composition, if an instance of Operating Car is destroyed, terminating the execution, the executions it owns are also terminated.
Figure 11.14 shows a block definition diagram with composition associations between the activity in Figure 11.10 and the types the object nodes in that activity. In an instance of Operating Car, which is one execution of it, instances of Brake Pressure and Modulation Frequency are linked to the execution instance when they are in the object nodes of the activity.

Figure 11.13 - Example block definition diagram for activity decomposition

Figure 11.14 - Example block definition diagram for object node types
12 Interactions

12.1 Overview

Interactions are used to describe interactions between entities. UML 2.1 Interactions are supported by four diagram types including the Sequence Diagram, Communications Diagram, Interaction Overview Diagram, and Timing Diagram. The Sequence Diagram is the most common of the Interaction Diagrams. SysML includes the Sequence Diagram only and excludes the Interaction Overview Diagram and Communication Diagram, which were considered to offer significantly overlapping functionality without adding significant capability for system modeling applications. The Timing Diagram is also excluded due to concerns about its maturity and suitability for systems engineering needs.

The sequence diagram describes the flow of control between actors and systems (blocks) or between parts of a system. This diagram represents the sending and receiving of messages between the interacting entities called lifelines, where time is represented along the vertical axis. The sequence diagrams can represent highly complex interactions with special constructs to represent various types of control logic, reference interactions on other sequence diagrams, and decomposition of lifelines into their constituent parts.

12.2 Diagram Elements

12.2.1 Sequence Diagram

Table 12.1 - Graphical nodes included in sequence diagrams\(^1\).

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>SequenceDiagram</td>
<td><img src="image" alt="sd Interaction1" /></td>
<td>UML4SysML::Interaction</td>
</tr>
<tr>
<td>Lifeline</td>
<td><img src="image" alt="b1:Block1" /></td>
<td>UML4SysML::Lifeline</td>
</tr>
<tr>
<td>Node Name</td>
<td>Concrete Syntax</td>
<td>Abstract Syntax Reference</td>
</tr>
<tr>
<td>-------------------</td>
<td>-----------------</td>
<td>----------------------------</td>
</tr>
<tr>
<td><strong>Execution</strong></td>
<td></td>
<td>UML4SysML::ExecutionSpecification</td>
</tr>
<tr>
<td><strong>Specification</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Diagram" /></td>
<td></td>
</tr>
<tr>
<td><strong>InteractionUse</strong></td>
<td></td>
<td>UML4SysML::InteractionUse</td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Diagram" /></td>
<td></td>
</tr>
</tbody>
</table>

1. Table is compliant with UML 2.1 Superstructure document.
A combined fragment is defined by an interaction operator and corresponding interaction operands.

Interaction Operators include:
- `seq` - Weak Sequencing
- `alt` - Alternatives
- `opt` - Option
- `break` - Break
- `par` - Parallel
- `strict` - Strict Sequencing
- `loop` - Loop
- `critical` - Critical Region
- `neg` - Negative
- `assert` - Assertion
- `ignore` - Ignore
- `consider` - Consider

### Table: Syntax and Reference

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>CombinedFragment</td>
<td><img src="image" alt="CombinedFragment Diagram" /></td>
<td>UML4SysML::CombinedFragment</td>
</tr>
<tr>
<td>StateInvariant / Continuations</td>
<td><img src="image" alt="StateInvariant Diagram" /></td>
<td>UML4SysML::Continuation, UML4SysML::StateInvariant</td>
</tr>
<tr>
<td>Coregion</td>
<td><img src="image" alt="Coregion Diagram" /></td>
<td>UML4SysML::CombinedFragment (under parallel)</td>
</tr>
<tr>
<td>Node Name</td>
<td>Concrete Syntax</td>
<td>Abstract Syntax Reference</td>
</tr>
<tr>
<td>--------------------</td>
<td>-----------------</td>
<td>-----------------------------------</td>
</tr>
<tr>
<td>CreationEvent</td>
<td></td>
<td>UML4SysML::CreationEvent</td>
</tr>
<tr>
<td>DestructionEvent</td>
<td></td>
<td>UML4SysML::DestructionEvent</td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Diagram" /></td>
<td></td>
</tr>
<tr>
<td>DurationConstraint</td>
<td></td>
<td>UML4SysML::Interactions</td>
</tr>
<tr>
<td>Duration</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Observation</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Diagram" /></td>
<td></td>
</tr>
<tr>
<td>TimeConstraint</td>
<td></td>
<td>UML4SysML::Interactions</td>
</tr>
<tr>
<td>TimeObservation</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Diagram" /></td>
<td></td>
</tr>
</tbody>
</table>
### Table 12.2 - Graphical paths included in sequence diagram

<table>
<thead>
<tr>
<th>Path Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Message</td>
<td><img src="message_diagram.png" alt="Message Diagram" /></td>
<td>UML4SysML::Message</td>
</tr>
<tr>
<td>Lost Message</td>
<td><img src="lost_message_diagram.png" alt="Lost Message Diagram" /></td>
<td>UML4SysML::Message</td>
</tr>
<tr>
<td>Found Message</td>
<td><img src="found_message_diagram.png" alt="Found Message Diagram" /></td>
<td>UML4SysML::Message</td>
</tr>
<tr>
<td>GeneralOrdering</td>
<td><img src="general_ordering_diagram.png" alt="GeneralOrdering Diagram" /></td>
<td>UML4SysML::GeneralOrdering</td>
</tr>
</tbody>
</table>

### 12.3 UML Extensions

#### 12.3.1 Diagram Extensions

The following specify diagram extensions to the notations defined in Chapter 17, “Profiles & Model Libraries.”

#### 12.3.1.1 Exclusion of Communication Diagram, Interaction Overview Diagram, and Timing Diagram

Communication diagrams and interaction overview diagrams are excluded from SysML. The other behavioral diagram representations were considered to provide sufficient coverage without introducing these diagram kinds. Timing Diagrams are also excluded due to concerns about their maturity and suitability for systems engineering needs.
12.4 Usage Examples

12.4.1 Sequence Diagrams

The diagram in Figure 12.1 illustrates the overall system behavior for operating the vehicle in sequence diagram format. To manage the complexity, a hierarchical sequence diagram is used which refers to other interactions that further elaborate the system behavior. (“ref StartVehicleBlackBox”) Combined Fragments are used to illustrate that steering can take place at the same time as controlling the speed and that controlling speed can be either idling, accelerating/cruising, or braking.

![Hierarchical Sequence Diagram](image.png)

**Figure 12.1 - Hierarchical Sequence Diagram illustrating system behavior for “Operate the vehicle” use case**

The diagram in Figure 12.2 shows an interaction that includes events and messages communicated between the driver and vehicle during the starting of the vehicle. The “hybridSUV” lifeline represents another interaction which further elaborates what happens inside the “hybridSUV” when the vehicle is started.
The diagram in Figure 12.3 shows the sequence of communication that occurs inside the HybridSUV when the vehicle is started successfully.
13 State Machines

13.1 Overview

The StateMachine package defines a set of concepts that can be used for modeling discrete behavior through finite state transition systems. The state machine represents behavior as the state history of an object in terms of its transitions and states. The activities that are invoked during the transition, entry, and exit of the states are specified along with the associated event and guard conditions. Activities that are invoked while in the state are specified as “do Activities,” and can be either continuous or discrete. A composite state has nested states that can be sequential or concurrent.

The UML concept of protocol state machines is excluded from SysML to reduce the complexity of the language. The standard UML state machine concept (called behavior state machines in UML) are thought to be sufficient for expressing protocols.

13.2 Diagram Elements

13.2.1 State Machine Diagram

Table 13.1 - Graphical nodes included in state machine diagrams.

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>StateMachineDiagram</td>
<td></td>
<td>UML4SysML::StateMachines</td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="StateMachineDiagram" /></td>
<td></td>
</tr>
<tr>
<td>Choice pseudo state</td>
<td></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td></td>
<td><img src="image" alt="Choice pseudo state" /></td>
<td></td>
</tr>
<tr>
<td>Node Name</td>
<td>Concrete Syntax</td>
<td>Abstract Syntax Reference</td>
</tr>
<tr>
<td>---------------------------</td>
<td>-----------------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>Composite state</td>
<td><img src="image" alt="Composite State Diagram" /></td>
<td>UML4SysML::State</td>
</tr>
<tr>
<td>Entry point</td>
<td><img src="image" alt="Entry Point Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>Exit point</td>
<td><img src="image" alt="Exit Point Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>Final state</td>
<td><img src="image" alt="Final State Diagram" /></td>
<td>UML4SysML::FinalState</td>
</tr>
<tr>
<td>History, Deep Pseudo state</td>
<td><img src="image" alt="Deep Pseudo State Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>History, Shallow pseudo state</td>
<td><img src="image" alt="Shallow Pseudo State Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>Initial pseudo state</td>
<td><img src="image" alt="Initial Pseudo State Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>Junction pseudo state</td>
<td><img src="image" alt="Junction Pseudo State Diagram" /></td>
<td>UML4SysML::PseudoState</td>
</tr>
<tr>
<td>Receive signal action</td>
<td><img src="image" alt="Receive Signal Action Diagram" /></td>
<td>UML4SysML::Transition</td>
</tr>
<tr>
<td>Node Name</td>
<td>Concrete Syntax</td>
<td>Abstract Syntax Reference</td>
</tr>
<tr>
<td>----------------</td>
<td>-----------------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>Send signal action</td>
<td><img src="image" alt="TurnOn" /></td>
<td>UML4SysML::Transition</td>
</tr>
<tr>
<td>Action</td>
<td><img src="image" alt="MinorReq := Id;" /></td>
<td>UML4SysML::Transition</td>
</tr>
<tr>
<td>Region</td>
<td><img src="image" alt="Region" /></td>
<td>UML4SysML::Region</td>
</tr>
<tr>
<td>Simple state</td>
<td><img src="image" alt="State1" /></td>
<td>UML4SysML::State</td>
</tr>
<tr>
<td>State list</td>
<td><img src="image" alt="State1, State2" /></td>
<td>UML4SysML::State</td>
</tr>
<tr>
<td>State Machine</td>
<td><img src="image" alt="ReadAmountSM" /></td>
<td>UML4SysML::StateMachine</td>
</tr>
</tbody>
</table>
Table 13.2 - Graphical paths included in state machine diagrams

<table>
<thead>
<tr>
<th>Path Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Transition</td>
<td>trigger(guard)(activity)</td>
<td>UML4SysML::Transition</td>
</tr>
</tbody>
</table>

13.3 UML Extensions

None.

13.4 Usage Examples

13.4.1 State Machine Diagram

The high level states or modes of the HybridSUV including the events that trigger changes of state are illustrated in the state machine diagram in Figure 13.1.
Figure 13.1 - High level view of the states of the HybridSUV
14 Use Cases

14.1 Overview

The use case diagram describes the usage of a system (subject) by its actors (environment) to achieve a goal, that is realized by the subject providing a set of services to selected actors. The use case can also be viewed as functionality and/or capabilities that are accomplished through the interaction between the subject and its actors. Use case diagrams include the use case and actors and the associated communications between them. Actors represent classifier roles that are external to the system that may correspond to users, systems, and/or other environmental entities. They may interact either directly or indirectly with the system. The actors are often specialized to represent a taxonomy of user types or external systems.

The use case diagram is a method for describing the usages of the system. The association between the actors and the use case represent the communications that occurs between the actors and the subject to accomplish the functionality associated with the use case. The subject of the use case can be represented via a system boundary. The use cases that are enclosed in the system boundary represent functionality that is realized by behaviors such as activity diagrams, sequence diagrams, and state machine diagrams.

The use case relationships are “communication,” “include,” “extend,” and “generalization.” Actors are connected to use cases via communication paths, that are represented by an association relationship. The “include” relationship provides a mechanism for factoring out common functionality that is shared among multiple use cases, and is always performed as part of the base use case. The “extend” relationship provides optional functionality, which extends the base use case at defined extension points under specified conditions. The “generalization” relationship provides a mechanism to specify variants of the base use case.

The use cases are often organized into packages with the corresponding dependencies between the use cases in the packages.

14.2 Diagram Elements

14.2.1 Use Case Diagram

Table 14.1 - Graphical nodes included in Use Case diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Use Case</td>
<td>UseCaseName</td>
<td>UML4SysML::UseCase</td>
</tr>
</tbody>
</table>
### Table 14.1 - Graphical nodes included in Use Case diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Use Case with ExtensionPoints</td>
<td><img src="image" alt="UseCaseName" /></td>
<td>UML4SysML::UseCase</td>
</tr>
<tr>
<td></td>
<td>extension points p1, p2</td>
<td></td>
</tr>
<tr>
<td>Actor</td>
<td><img src="image" alt="actor" /></td>
<td>UML4SysML::Actor</td>
</tr>
<tr>
<td></td>
<td>ActorName</td>
<td></td>
</tr>
<tr>
<td>Subject</td>
<td><img src="image" alt="SubjectName" /></td>
<td>Role name on</td>
</tr>
<tr>
<td></td>
<td></td>
<td>UML4SysML::Classifier</td>
</tr>
</tbody>
</table>

### Table 14.2 - Graphical paths included in Use Case diagrams

<table>
<thead>
<tr>
<th>Path Type</th>
<th>concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Communication path</td>
<td><img src="image" alt="Communication" /></td>
<td>UML4SysML::Association</td>
</tr>
<tr>
<td>Include</td>
<td><img src="image" alt="Include" /></td>
<td>UML4SysML::Include</td>
</tr>
<tr>
<td>Extend</td>
<td><img src="image" alt="Extend" /></td>
<td>UML4SysML::Extend</td>
</tr>
</tbody>
</table>
14.3 UML Extensions

None.

Table 14.2 - Graphical paths included in Use Case diagrams

<table>
<thead>
<tr>
<th>Path Type</th>
<th>concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extend with Condition</td>
<td></td>
<td>UML4SysML::Extend</td>
</tr>
<tr>
<td></td>
<td>![Diagram]</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Condition: (boolean expression)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>extension point: p1, p2</td>
<td></td>
</tr>
<tr>
<td>Generalization</td>
<td></td>
<td>UML4SysML::Kernel</td>
</tr>
</tbody>
</table>
14.4 Usage Examples

Figure 14.1 - Top level use case diagram for the Hybrid SUV subject

Figure 14.1 is a top-level set of use cases for the Hybrid SUV System. Figure 14.2 shows the decomposition of the Operate the Vehicle use case. In this diagram, the frame represents the package that contains the lower level use cases. The convention of naming the package with the same name as the top level use case has been employed. This practice offers an implicit tracing mechanism that complements the explicit trace relationships in SysML.
In the figure 14.2 the Extend relationship specifies that the behavior of a use case may be extended by the behavior of another (usually supplementary) use case. The extension takes place at one or more specific extension points defined in the extended use case. Note, however, that the extended use case is defined independently of the extending use case and is meaningful independently of the extending use case. On the other hand, the extending use case typically defines behavior that may not necessarily be meaningful by itself. Instead, the extending use case defines a set of modular behavior increments that augment an execution of the extended use case under specific conditions. In Figure 14.2, the “Start the Vehicle” use case is modeled as an extension of “Drive the Vehicle.” This means that there are conditions that may exist that require the execution of an instance of “Start the Vehicle” before an instance of “Drive the Vehicle” is executed.

The use cases “Accelerate,” “Steer,” and “Brake” are modeled using the include relationship. Include is a DirectedRelationship between two use cases, implying that the behavior of the included use case is inserted into the behavior of the including use case. It is also a kind of NamedElement so that it can have a name in the context of its
owning use case. The including use case may only depend on the result (value) of the included use case. This value is obtained as a result of the execution of the included use case. This means that “Accelerate,” “Steer,” and “Brake” are all part of the normal process of executing an instance of “Drive the Car.”

In many situations, the use of the Include and Extend relationships is subjective and may be reversed, based on the approach of an individual modeler.
Part IV - Crosscutting Constructs

This Part specifies crosscutting constructs that apply to both structure and behavior. These constructs are defined in the following chapters:

Chapter 15 - Allocations - defines a basic allocation relationship that can be used to allocate a set of model elements to another, such as allocating behavior to structure or allocating logical to physical components.

Chapter 16 - Requirements - specifies constructs for system requirements and their relationships.

Chapter 17 - Profiles and Model Libraries - specifies the approach to further customize and extend SysML for specific applications.
15 Allocations

15.1 Overview

Allocation is the term used by systems engineers to denote the organized cross-association (mapping) of elements within the various structures or hierarchies of a user model. The concept of “allocation” requires flexibility suitable for abstract system specification, rather than a particular constrained method of system or software design. System modelers often associate various elements in a user model in abstract, preliminary, and sometimes tentative ways. Allocations can be used early in the design as a precursor to more detailed rigorous specifications and implementations. The allocation relationship can provide an effective means for navigating the model by establishing cross relationships, and ensuring the various parts of the model are properly integrated.

This chapter does not try to limit the use of the term “allocation,” but provides a basic capability to support allocation in the broadest sense. It does include some specific subclasses of allocation for allocating behavior, structure, and flows. A typical example is the allocation of activities to blocks (e.g., functions to components). This chapter specifies an extension for an allocation relationship and selected subclasses of allocation, along with the notation to represent allocations in a SysML model.

15.2 Diagram Elements

The diagram elements defined in this chapter may be shown on some or all SysML diagram types, in addition to the diagram elements that are specific for each diagram type.
### 15.2.1 Representing Allocation on Diagrams

Table 15.1 - Extension to graphical nodes included in diagrams

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Allocated stereotype</td>
<td><img src="image1" alt="Concrete Syntax Diagram" /></td>
<td>SysML::Allocation:Allocated</td>
</tr>
<tr>
<td>Allocation derived properties displayed in compartment of a Block.</td>
<td><img src="image2" alt="Concrete Syntax Diagram" /></td>
<td>SysML::Allocation:Allocated</td>
</tr>
<tr>
<td>Allocation derived properties displayed in Comment.</td>
<td><img src="image3" alt="Concrete Syntax Diagram" /></td>
<td>SysML::Allocation:Allocated</td>
</tr>
<tr>
<td>Allocation derived properties displayed in compartment of Part on Internal Block Diagram.</td>
<td><img src="image4" alt="Concrete Syntax Diagram" /></td>
<td>SysML::Allocation:Allocated</td>
</tr>
<tr>
<td>Allocation derived properties displayed in compartment of Action on Activity Diagram.</td>
<td><img src="image5" alt="Concrete Syntax Diagram" /></td>
<td>SysML::Allocation:Allocated</td>
</tr>
</tbody>
</table>
15.3 UML Extensions

15.3.1 Diagram Extensions

15.3.1.1 Tables

Allocation relationships may be depicted in tables. A separate row is provided for each «allocate» dependency. “from” is the client of the «allocate» dependency, and “to” is the supplier. Both ElementType and ElementName for client and supplier appear in this table.

15.3.1.2 Allocate Relationship Rendering

The “allocate” relationship is a dashed line with an open arrow head. The arrow points in the direction of the allocation. In other words, the directed line points “from” the element being allocated “to” the element that is the target of the allocation.

15.3.1.3 Allocated Property Compartment Format

When properties of an «allocated» model element are displayed in a property compartment, a shorthand notation is used as shown in Table 15.1. This shorthand groups and displays the AllocatedFrom properties together, then the AllocatedTo properties. These properties are shown without the use of brackets {}.

15.3.1.4 Allocated Property Callout Format

When an «allocate» property component is not used, a property callout may be used. An «allocate» property callout uses the same shorthand notation as the «allocate» property compartment. This notation is also shown in Table 15.1. For brevity, the «elementType» portion of the AllocatedFrom or AllocatedTo property may be elided from the diagram.

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Allocation Activity Partition</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Allocation:Allocate ActivityPartition</td>
</tr>
<tr>
<td>Allocation (general)</td>
<td><img src="image" alt="Diagram" /></td>
<td>SysML::Allocation:Allocate</td>
</tr>
</tbody>
</table>

Table 15.1 - Extension to graphical nodes included in diagrams
15.3.1.5 AllocatedActivityPartition Label

For brevity, the keyword used on an AllocatedActivityPartition is «allocate», rather than the stereotype name («allocateActivityPartition»). For brevity, the «elementType» portion of the AllocatedFrom or AllocatedTo property may be elided from the diagram.

15.3.2 Stereotypes

Package Allocations

![Diagram of UML and SysML stereotypes for allocations]

Figure 15.1 - Abstract syntax extensions for SysML Allocation

![Diagram of UML and SysML stereotypes for allocated activity partition]

Figure 15.2 - Abstract syntax expression for AllocatedActivityPartition

15.3.2.1 Allocate(from Allocations)

Description

Allocate is a dependency based on UML::abstraction. It is a mechanism for associating elements of different types, or in different hierarchies, at an abstract level. Allocate is used for assessing user model consistency and directing future design activity. It is expected that an «allocate» relationship between model elements is a precursor to a more concrete relationship between the elements, their properties, operations, attributes, or sub-classes.

Allocate is a stereotype of a UML4SysML::Abstraction which is permissible between any two NamedElements. It is depicted as a dependency with the “allocate” keyword attached to it.

Allocate is directional in that one NamedElement is the “from” end (no arrow), and at least one NamedElement is the “to” end (the end with the arrow).
The following paragraphs describe types of allocation that are typical in systems engineering.

Behavior allocation relates to the systems engineering concept segregating form from function. This concept requires independent models of “function” (behavior) and “form” (structure), and a separate, deliberate mapping between elements in each of these models. It is acknowledged that this concept does not support a standard object-oriented paradigm, nor is this always even desirable. Experience on large scale, complex systems engineering problems have proven, however, that segregation of form and function is a valuable approach. In addition, behavior allocation may also include the allocation of Behaviors to BehavioralFeatures of Blocks, e.g., Operations.

Flow allocation specifically maps flows in functional system representations to flows in structural system representations.

Flow between activities can either be control or object flow. The figures in the Usage Examples show concrete syntax for how object flow is mapped to connectors on Activity Diagrams. Allocation of control flow is not specifically addressed in SysML, but may be represented by relating an ItemFlow to the Control Flow using the UML relationship InformationFlow.realizingActivityEdge.

Note that allocation of ObjectFlow to Connector is an Allocation of Usage, and does NOT imply any relation between any defining Blocks of ObjectFlows and any defining associations of connectors.

The figures in the Usage Examples illustrate an available mechanism for relating the objectNode from an activity diagram to the itemFlow on an internal block diagram. ItemFlow is discussed in Chapter 9, Ports and Flows.

Pin to Port allocation is not addressed in this release of SysML.

Structure allocation is associated with the concept of separate “logical” and “physical” representations of a system. It is often necessary to construct separate depictions of a system and define mappings between them. For example, a complete system hierarchy may be built and maintained at an abstract level. In turn, it must then be mapped to another complete assembly hierarchy at a more concrete level. The set of models supporting complex systems development may include many of these levels of abstraction. This specification will not define “logical” or “physical” in this context, except to acknowledge the stated need to capture allocation relationships between separate system representations.

Constraints

A single «allocate» dependency shall have only one supplier (from), but may have one or many clients (to).

If subtypes of the «allocate» dependency are introduced to represent more specialized forms of allocation, then they should have constraints applied to supplier and client as appropriate.

15.3.2.2 Allocated(from Allocations)

Description

«allocated» is a stereotype that applies to any NamedElement that has at least one allocation relationship with another NamedElement. «allocated» elements may be designated by either the /from or /to end of an «allocate» dependency.

The «allocated» stereotype provides a mechanism for a particular model element to conveniently retain and display the element at the opposite end of any «allocate» dependency. This stereotype provides for the properties “allocatedFrom” and “allocatedTo,” which are derived from the «allocate» dependency.

Attributes

The following properties are derived from any «allocate» dependency:

• /allocatedTo:NamedElement[*]
The element types and names of the set of elements that are clients (“to” end of the concrete syntax) of an «allocate» whose client is extended by this stereotype (instance). This property is the union of all clients to which this instance is the supplier, i.e., there may be more than one /allocatedTo property per allocated model element. Each allocatedTo property will be expressed as «elementType» ElementName.

- /allocatedFrom:NamedElement[*]

Reverse of allocatedTo: the element types and names of the set of elements that are suppliers (from) of an «allocate» whose supplier is extended by this stereotype (instance). The same characteristics apply as to /allocatedTo. Each allocatedFrom property will be expressed as «elementType» ElementName.

For uniformity, the «elementType» displayed for the /allocatedTo or /allocatedFrom properties should be from the following list, as applicable. Other «elementType» designations may be used, if none of the below apply.


Note that the supplier or client may be an Element (e.g., Activity, Block), Property (e.g., Action, Part), Connector, or BehavioralFeature (e.g., Operation). For this reason, it is important to use fully qualified names when displaying /allocatedFrom and /allocatedTo properties. An example of a fully qualified name is the form (PackageName::ElementName.PropertyName). Use of such fully qualified names makes it clear that the «allocate» is referring to the definition of the element, or to its specific usage as a property of another element.

15.3.2.3 AllocateActivityPartition(from Allocations)

Description

AllocateActivityPartition is used to depict an «allocate» relationship on an Activity diagram. The AllocateActivityPartition is a standard UML2::ActivityPartition, with modified constraints as stated in the paragraph below.

Constraints

An Action appearing in an «AllocateActivityPartition» will be the /supplier (from) end of an «allocate» dependency. The element that represents the «AllocateActivityPartition» will be the /client (to) end of the same «allocate» dependency. In the «AllocateActivityPartition» name field, Properties are designated by the use of a fully qualified name (including colon, e.g., “part_name:Block_Name”), and Classifiers are designated by a simple name (no colons, e.g., “Block_Name”).

The «AllocateActivityPartition» maintains the constraints, but not the semantics, of the UML2::ActivityPartition. Classifiers or Properties represented by an «AllocateActivityPartition» do not have any direct responsibility for invoking behavior depicted within the partition boundaries. To depict this kind of direct responsibility, the modeler is directed to the UML 2 Superstructure specification, Section 12.3.10, ActivityPartition, Semantics topic.

15.4 Usage Examples

The following examples depict allocation relationships as property callout boxes (basic), property compartment of a Block (basic), and property compartments of Activities and Parts (advanced). Figure 15.3 shows generic allocation for Blocks.
15.4.1 Behavior Allocation of Actions to Parts and Activities to Blocks

Specific behavior allocation of Actions to Parts are depicted in Figure 15.4. Note that the AllocateActivityPartition, if used in this manner, is unambiguously associated with behavior allocation.

Figure 15.4 - Behavior allocation
15.4.2 Allocate Flow

Figure 15.5 shows flow allocation of ObjectFlow to a Connector, or alternatively to an ItemFlow. Allocation of ControlFlow is not shown as an example, but it is not prohibited in SysML.

Figure 15.5 - Example of flow allocation from ObjectFlow to Connector

Figure 15.6 - Example of flow allocation from ObjectFlow to ItemFlow
15.4.2.1 Allocating Structure

Systems engineers have a frequent need to allocate structural model elements (e.g., blocks, parts, or connectors) to other structural elements. For example, if a particular user model includes an abstract logical structure, it may be important to show how these model elements are allocated to a more concrete physical structure. The need also arises, when adding detail to a structural model, to allocate a connector (at a more abstract level) to a part (at a more concrete level).
15.4.2.2 Automotive Example

Example: consider the functions required to portion and deliver power for a hybrid SUV. The activities for providing power are allocated to blocks within the Hybrid SUV, as shown in Figure 15.9. This example is consistent with Sample Problem.

Figure 15.9 - AllocateActivityPartitions (Swimlanes) for HybridSUV Accelerate Example
### 15.4.3 Tabular Representation

The table shown in Figure 15.11 is provided as a specific example of how the «allocate» dependency may be depicted in tabular form, consistent with the automotive example above.

<table>
<thead>
<tr>
<th>type</th>
<th>name</th>
<th>end</th>
<th>relation</th>
<th>end</th>
<th>type</th>
<th>name</th>
</tr>
</thead>
<tbody>
<tr>
<td>activity</td>
<td>a1:ProportionPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>PowerControlUnit</td>
</tr>
<tr>
<td>activity</td>
<td>a2:ProvideGasPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>InternalCombustionEngine</td>
</tr>
<tr>
<td>activity</td>
<td>a3:ControlElectricPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>ElectricalPowerController</td>
</tr>
<tr>
<td>activity</td>
<td>a4:ProvideElectricPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>ElectricalMotorGenerator</td>
</tr>
<tr>
<td>objectNode</td>
<td>driveCurrent</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>itemFlow</td>
<td>i1:ElectricCurrent</td>
</tr>
</tbody>
</table>
The allocation table can also be shown using a sparse matrix style as in the following example shown in Figure 15.12:

<table>
<thead>
<tr>
<th>Source</th>
<th>Target</th>
<th>InternalCombustionEngine</th>
<th>ElectricalPowerController</th>
<th>ElectricalMotorGenerator</th>
<th>II:ElectricCurrent</th>
</tr>
</thead>
<tbody>
<tr>
<td>A1:ProportionPower</td>
<td>allocate</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A2:ProvideGasPower</td>
<td>allocate</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A3:ControlElectricPower</td>
<td>allocate</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A4:ProvideElectricPower</td>
<td>allocate</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>driveCurrent</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>allocate</td>
</tr>
</tbody>
</table>

Figure 15.12 - Allocation Matrix Showing Allocation for Hybrid SUV Accelerate Example
16 Requirements

16.1 Overview

A requirement specifies a capability or condition that must (or should) be satisfied. A requirement may specify a function that a system must perform or a performance condition a system must achieve. SysML provides modeling constructs to represent text-based requirements and relate them to other modeling elements. The requirements diagram described in this chapter can depict the requirements in graphical, tabular, or tree structure format. A requirement can also appear on other diagrams to show its relationship to other modeling elements. The requirements modeling constructs are intended to provide a bridge between traditional requirements management tools and the other SysML models.

A requirement is defined as a stereotype of UML Class subject to a set of constraints. A standard requirement includes properties to specify its unique identifier and text requirement. Additional properties such as verification status, can be specified by the user.

Several requirements relationships are specified that enable the modeler to relate requirements to other requirements as well as to other model elements. These include relationships for defining a requirements hierarchy, deriving requirements, satisfying requirements, verifying requirements, and refining requirements.

A composite requirement can contain subrequirements in terms of a requirements hierarchy, specified using the UML namespace containment mechanism. This relationship enables a complex requirement to be decomposed into its containing child requirements. A composite requirement may state that the system shall do A and B and C, which can be decomposed into the child requirements that the system shall do A, the system shall do B, and the system shall do C. An entire specification can be decomposed into children requirements, which can be further decomposed into their children to define the requirements hierarchy.

There is a real need for requirement reuse across product families and projects. Typical scenarios are regulatory, statutory, or contractual requirements that are applicable across products and/or projects and requirements that are reused across product families (versions/variants). In these cases, one would like to be able to reference a requirement, or requirement set in multiple contexts with updates to the original requirements propagated to the reused requirement(s).

The use of namespace containment to specify requirements hierarchies precludes reusing requirements in different contexts since a given model element can only exist in one namespace. Since the concept of requirements reuse is very important in many applications, SysML introduces the concept of a slave requirement. A slave requirement is a requirement whose text property is a read-only copy of the text property of a master requirement. The text property of the slave requirement is constrained to be the same as the text property of the related master requirement. The master/slave relationship is indicated by the use of the copy relationship.

The “derive requirement” relationship relates a derived requirement to its source requirement. This typically involves analysis to determine the multiple derived requirements that support a source requirement. The derived requirements generally correspond to requirements at the next level of the system hierarchy. A simple example may be a vehicle acceleration requirement that is analyzed to derive requirements for engine power, vehicle weight, and body drag.

The satisfy relationship describes how a design or implementation model satisfies one or more requirements. A system modeler specifies the system design elements that are intended to satisfy the requirement. In the example above, the engine design satisfies the engine power requirement.
The verify relationship defines how a test case verifies a requirement. In SysML, a test case is intended to be used as a general mechanism to represent any of the standard verification methods for inspection, analysis, demonstration, or test. Additional subclasses can be defined by the user if required to represent the different verification methods. A verdict property of a test case can be used to represent the verification result. The SysML test case is defined consistent with the UML testing profile to facilitate integration between the two profiles.

The refine requirement relationship can be used to describe how a model element or set of elements can be used to further refine a requirement. For example, a use case or activity diagram may be used to refine a text-based functional requirement. Alternatively, it may be used to show how a text-based requirement refines a model element. In this case, some elaborated text could be used to refine a less fine-grained model element.

A generic trace requirement relationship provides a general-purpose relationship between a requirement and any other model element. The semantics of trace include no real constraints and therefore are quite weak. As a result, it is recommended that the trace relationship not be used in conjunction with the other requirements relationships described above.

The rationale construct that is defined in Chapter 7, “Model Elements” is quite useful in support of requirements. It enables the modeler to attach a rationale to any requirements relationship or to the requirement itself. For example, a rationale can be attached to a satisfy relationship that refers to an analysis report or trade study that provides the supporting rationale for why the particular design satisfies the requirement. Similarly, this can be used with the other relationships such as the derive relationship. It also provides an alternative mechanism to capture the verify relationship by attaching a rationale to a satisfy relationship that references a test case.

Modelers can customize requirements taxonomies by defining additional subclasses of the Requirement stereotype. For example, a modeler may want to define requirements categories to represent operational, functional, interface, performance, physical, storage, activation/deactivation, design constraints, and other specialized requirements such as reliability and maintainability, or to represent a high level stakeholder need. The stereotype enables the modeler to add constraints that restrict the types of model elements that may be assigned to satisfy the requirement. For example, a functional requirement may be constrained so that it can only be satisfied by a SysML behavior such as an activity, state machine, or interaction. Some potential Requirement subclasses are defined in Annex C: Non-normative Extensions.
### 16.2 Diagram Elements

#### 16.2.1 Requirement Diagram

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Requirement Diagram</td>
<td><img src="image" alt="requirement" /></td>
<td>SysML::Requirements::Requirement, SysML::ModelElements::Package</td>
</tr>
<tr>
<td>Requirement</td>
<td><img src="image" alt="requirement" /></td>
<td>SysML::Requirements::Requirement</td>
</tr>
<tr>
<td>TestCase</td>
<td><img src="image" alt="testCase" /></td>
<td>SysML::Requirements::TestCase</td>
</tr>
</tbody>
</table>

Table 16.1 - Graphical nodes included in Requirement diagrams
Table 16.2 - Graphical paths included in Requirement diagrams

<table>
<thead>
<tr>
<th>Path Type</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Requirement containment relationship</td>
<td><img src="image" alt="Diagram of Parent and Children" /></td>
<td>UML4SysML::NestedClassifier</td>
</tr>
<tr>
<td>CopyDependency</td>
<td><img src="image" alt="Diagram of Slave Copying Master" /></td>
<td>SysML::Requirements::Copy</td>
</tr>
<tr>
<td>MasterCallout</td>
<td><img src="image" alt="Diagram of Master and Slave" /></td>
<td>SysML::Requirements::Copy</td>
</tr>
<tr>
<td>Derive Dependency</td>
<td><img src="image" alt="Diagram of Client and Supplier" /></td>
<td>SysML::Requirements::DeriveReqt</td>
</tr>
<tr>
<td>DeriveCallout</td>
<td><img src="image" alt="Diagram of ReqA and ReqB" /></td>
<td>SysML::Requirements::DeriveReqt</td>
</tr>
<tr>
<td>Satisfy Dependency</td>
<td><img src="image" alt="Diagram of Supplier Satisfying Req" /></td>
<td>SysML::Requirements::Satisfy</td>
</tr>
</tbody>
</table>
Table 16.2 - Graphical paths included in Requirement diagrams

<table>
<thead>
<tr>
<th>Path Type</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>SatisfyCallout</td>
<td><img src="https://example.com/diagram.png" alt="Diagram" /></td>
<td>SysML::Requirements::Satisfy</td>
</tr>
<tr>
<td>Verify Dependency</td>
<td><img src="https://example.com/diagram.png" alt="Diagram" /></td>
<td>SysML::Requirements::Verify</td>
</tr>
<tr>
<td>VerifyCallout</td>
<td><img src="https://example.com/diagram.png" alt="Diagram" /></td>
<td>SysML::Requirements::Verify</td>
</tr>
<tr>
<td>Refine Dependency</td>
<td><img src="https://example.com/diagram.png" alt="Diagram" /></td>
<td>UML4SysML::Refine</td>
</tr>
<tr>
<td>RefineCallout</td>
<td><img src="https://example.com/diagram.png" alt="Diagram" /></td>
<td>UML4SysML::Refine</td>
</tr>
</tbody>
</table>
16.3 UML Extensions

16.3.1 Diagram Extensions

16.3.1.1 Requirement Diagram

The Requirement Diagram can only display requirements, packages, other classifiers, test cases, and rationale. The relationships for containment, deriveReqt, satisfy, verify, refine, copy, and trace can be shown on a requirement diagram. The callout notation can also be used to reflect the relationship of other model elements to a requirement.

16.3.1.2 Requirement Notation

The requirement is represented as shown in Table 16-1. The «requirement» compartment label for the stereotype properties compartment (e.g., id and text) can be elided.

16.3.1.3 Requirement Property Callout Format

A callout notation can be used to represent derive, satisfy, verify, refine, copy, and trace relationships as indicated in Table 16.2. For brevity, the «elementType» may be elided.

16.3.1.4 Requirements on Other Diagrams

Requirements can also be represented on other diagrams to show their relationship to other model elements. The compartment and callout notation described in 16.3.1.2 and 16.3.1.3 can be used. The callouts represent the requirement that is attached to another model element such as a design element.
16.3.1.5 Requirements Table

The tabular format is used to represent the requirements, their properties and relationships, and may include:

- Requirements with their properties in columns.
- A column that includes the supplier for any of the dependency relationships (Derive, Verify, Refine, Trace).
- A column that includes the model elements that satisfy the requirement.
- A column that represents the rationale for any of the above relationships, including reference to analysis reports for trace rationale, trade studies for design rationale, or test procedures for verification rationale.

The relationships between requirements and other objects can also be shown using a sparse matrix style that is similar to the table used for allocations (Section 15.4.3, “Tabular Representation”). The table should include the source and target elements names (and optionally kinds) and the requirement dependency kind.

<table>
<thead>
<tr>
<th>id</th>
<th>name</th>
<th>relation</th>
<th>id</th>
<th>name</th>
<th>relation</th>
<th>id</th>
<th>name</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>Performance</td>
<td>deriveReqt</td>
<td>d.1</td>
<td>RegenerativeBraking</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.1</td>
<td>Braking</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td>FuelEconomy</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.3</td>
<td>OffRoadCapability</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.4</td>
<td>Acceleration</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.1</td>
<td>Braking</td>
<td>deriveReqt</td>
<td>d.1</td>
<td>RegenerativeBraking</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td>FuelEconomy</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.2</td>
<td>FuelEconomy</td>
<td>deriveReqt</td>
<td>d.2</td>
<td>Range</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.2</td>
<td>FuelCapacity</td>
<td>deriveReqt</td>
<td>d.2</td>
<td>Range</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.3</td>
<td>OffRoadCapability</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.4</td>
<td>Acceleration</td>
<td>deriveReqt</td>
<td>d.4</td>
<td>Power</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.1</td>
<td>CargoCapacity</td>
<td>deriveReqt</td>
<td>d.4</td>
<td>Power</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The Hybrid SUV shall have the braking, acceleration, and off-road capability of a typical SUV, but have dramatically better fuel economy.

The Hybrid SUV shall have the braking capability of a typical SUV.

The Hybrid SUV shall have dramatically better fuel economy than a typical SUV.

The Hybrid SUV shall have the off-road capability of a typical SUV.

The Hybrid SUV shall have the acceleration of a typical SUV.
16.3.2 Stereotypes

Package Requirements

Figure 16.1 - Abstract Syntax for Requirements Stereotypes
16.3.2.1 Copy

Description

A Copy relationship is a dependency between a supplier requirement and a client requirement that specifies that the text of the client requirement is a read-only copy of the text of the supplier requirement.

A Copy dependency created between two requirements maintains a master/slave relationship between the two elements for the purpose of requirements re-use in different contexts. When a Copy dependency exists between two requirements, the requirement text of the client requirement is a read-only copy of the requirement text of the requirement at the supplier end of the dependency.

Constraints

[1] A Copy dependency may only be created between two classes that have the “requirement” stereotype, or a subtype of the “requirement” stereotype applied.
[2] If the supplier requirement has subrequirements, copies of the subrequirements are made recursively in the context of the client requirement and Copy dependencies are created between each subrequirement and the associated copy.
[3] The text property of the client requirement is constrained to be a read-only copy of the text property of the supplier requirement.

16.3.2.2 DeriveReqt

Description

A DeriveReqt relationship is a dependency between two requirements in which a client requirement can be derived from the supplier requirement. For example, a system requirement may be derived from a business need, or lower-level requirements may be derived from a system requirement. As with other dependencies, the arrow direction points from the derived (client) requirement to the (supplier) requirement from which it is derived.

Constraints

[1] The supplier must be an element stereotyped by «requirement» or one of «requirement» subtypes.
The client must be an element stereotyped by «requirement» or one of «requirement» subtypes.

### 16.3.2.3 Requirement

**Description**

A requirement specifies a capability or condition that must (or should) be satisfied. A requirement may specify a function that a system must perform or a performance condition that a system must satisfy. Requirements are used to establish a contract between the customer (or other stakeholder) and those responsible for designing and implementing the system.

A requirement is a stereotype of Class. Compound requirements can be created by using the nesting capability of the class definition mechanism. The default interpretation of a compound requirement, unless stated differently by the compound requirement itself, is that all its subrequirements must be satisfied for the compound requirement to be satisfied. Subrequirements can be accessed through the “nestedClassifier” property of a class. When a requirement has nested requirements, all the nested requirements apply as part of the container requirement. Deleting the container requirement deleted the nested requirements, a functionality inherited from UML.

**Attributes**

- **text**: String  
  The textual representation or a reference to the textual representation of the requirement.
- **id**: String  
  The unique id of the requirement.
- **/satisfiedBy**: NamedElement [*]  
  Derived from all elements that are the client of a «satisfy» relationship for which this requirement is a supplier.
- **/verifiedBy**: NamedElement [*]  
  Derived from all elements that are the client of a «verify» relationship for which this requirement is a supplier.
- **/tracedTo**: NamedElement [*]  
  Derived from all elements that are the client of a «trace» relationship for which this requirement is a supplier.
- **/derived**: Requirement [0..1]  
  Derived from all requirements that are the client of a «deriveReqt» relationship for which this requirement is a supplier.
- **/derivedFrom**: Requirement [*]  
  Derived from all requirements that are the supplier of a «deriveReqt» relationship for which this requirement is a client.
- **/refinedBy**: NamedElement [*]  
  Derived from all elements that are the client of a «refine» relationship for which this requirement is a supplier.
- **/master**: Requirement [0..1]  
  This is a derived property that lists the master requirement for this slave requirement. The master attribute is derived from the supplier of the Copy dependency that has this requirement as the slave.

**Constraints**

1. The property “isAbstract” must be set to true.
2. The property “ownedOperation” must be empty.
3. The property “ownedAttribute” must be empty.
16.3.2.4 RequirementRelated

Description

This stereotype is used to add properties to those elements that are related to requirements via the various dependencies described in Figure 16.1. The property values are shown using callout notation (i.e., notes) as shown in the diagram element table.

Attributes

- /satisfies: Requirement [*]
  Derived from all requirements that are the supplier of a «satisfy» relationship for which this element is a client.
- /refines: Requirement [*]
  Derived from all requirements that are the supplier of a «refine» relationship for which this element is a client.
- /tracedFrom: Requirement [*]
  Derived from all requirements that are the supplier of a «trace» relationship for which this element is a client.

16.3.2.5 TestCase

Description

A test case is a method for verifying a requirement is satisfied.

Attributes

- /verifies: Requirement [*]
  Derived from all requirements that are the supplier of a «verify» relationship for which this element is a client.

Constraints

[1] The type of return parameter of the stereotyped model element must be VerdictKind. (note this is consistent with the UML Testing Profile).

16.3.2.6 Satisfy

Description

A Satisfy relationship is a dependency between a requirement and a model element that fulfills the requirement. As with other dependencies, the arrow direction points from the satisfying (client) model element to the (supplier) requirement that is satisfied.

Constraints

[1] The supplier must be an element stereotyped by «requirement» or one of «requirement» subtypes.
16.3.2.7 Verify

Description

A Verify relationship is a dependency between a requirement and a test case that can determine whether a system fulfills the requirement. As with other dependencies, the arrow direction points from the (client) test case to the (supplier) requirement.

Constraints

[1] The supplier must be an element stereotyped by «requirement» or one of «requirement» subtypes.
[2] The client must be an element stereotyped by «testCase» or one of the «testCase» subtypes.

16.4 Usage Examples

All the examples in this chapter are based on a set of publicly available (on-line) requirement specifications from the National Highway Traffic Safety Administration (NHTSA.) Excerpts of the original requirement text used to create the models are shown in Figure 16.3. The name and ID of these requirements are referred to in the SysML usage examples that follow. See NHTSA specification 49CFR571.135 for the complete text from which these examples are taken.

16.4.1 Requirement Decomposition and Traceability

The diagram in Figure 16.3 shows an example of a compound requirement decomposed into multiple subrequirements.

![Figure 16.3 - Requirements Derivation](image)

16.4.1.1 Requirements and Design Elements

The diagram in Figure 16.4 shows derived requirements and refers to the design elements that satisfy them. The rationale is also shown as a basis for the design solution.

Text = "This test method covers the measurement of peak braking coefficient of paved surfaces using a standard reference test tire (SRTT) as described in Specification E1136 that represents current technology passenger car radial tires."
ID = "A. 24241"

Text = "The road test surface produces a peak friction coefficient (PFC) of 0.9 when measured using an American Society for Testing and Materials (ASTM) E1136 standard reference test tire, in accordance with ASTM Method E 1337–90,"
ID = "S6.2.1"

Text = "(a) IBT: 65 °C (149 °F), 100 °C (212 °F).
(b) Test surface: PFC of at least 0.9."
ID="S7.4.3"
Figure 16.4 - Links between requirements and design
16.4.1.2 Requirements Reuse

Figure 16.6 illustrates the use of the Copy dependency to allow a single requirement to be reused in several requirements hierarchies. The master tag provides a textual reference to the reused requirement.
16.4.1.3 Verification Procedure (Test Case)

The example diagram in Figure 16.7 shows how a complex test case, in this example a performance test for a passenger-car brake system, given as a set of steps in text form (see part of the procedure text at the upper right-handside corner of the figure), can be described using another type of diagram representation. The performance test, modeled as a Test Case is linked to a requirement using the «verify» relationship. Note that the modeling of test case can also be addressed using the UML Testing Profile, available from the Object Management Group.

![Diagram](Figure 16.7 - Linkage of a Test Case to a requirement: This figure shows the Requirement Diagram)
Figure 16.8 - Linkage of a Test Case to a requirement: This figure shows the Test Case as a State Diagram
17 Profiles & Model Libraries

17.1 Overview

The Profiles package contains mechanisms that allow metaclasses from existing metamodels to be extended to adapt them for different purposes. This includes the ability to tailor the UML metamodel for different domains. The profiles mechanism is consistent with the OMG Meta Object Facility (MOF). SysML has added some notational extensions to represent stereotype properties in compartments as well as notes.

The stereotype is the primary mechanism used to create profiles to extend the metamodel. Stereotypes are defined by extending a metaclass, and then have them applied to the applicable model elements in the user model. A stereotype of a requirement could be extended to create a «functionalRequirement» as described in Annex C: Non-normative Extensions. This would allow specific properties and constraints to be created for a functional requirement. For example, a functional requirement may be constrained such that it must be satisfied by an operation or behavior. When the stereotype is applied to a requirement, then the requirement would include the notation «functionalRequirement» in addition to the name of the particular functional requirement. Extending the metaclass requirement is different from creating a subclass of requirement called functionalRequirement.

The Usage Examples section provides guidance both on how to use existing profiles and how to create new profiles. In addition, the examples provide guidance on the use of model libraries. A model library is a library of model elements including class and other type definitions that are considered reusable for a given domain. This guidelines can be applied to further customize SysML for domain specific applications such as automotive, military, or space systems.
## 17.2 Diagram Elements

### 17.2.1 Profile Definition in Class Diagram

Table 17.1 - Graphical nodes used in profile definition

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Stereotype</td>
<td>«stereotype» StereotypeName</td>
<td>UML4SysML::Stereotype</td>
</tr>
<tr>
<td>Metaclass</td>
<td>«metaclass» MetaClassName</td>
<td>UML4SysML::Class</td>
</tr>
<tr>
<td>Profile</td>
<td>«profile» ProfileName</td>
<td>UML4SysML::Profile</td>
</tr>
<tr>
<td>Model Library</td>
<td>«modelLibrary» LibraryName</td>
<td>UML::StandardProfileL1</td>
</tr>
</tbody>
</table>
### Table 17.2 - Graphical paths used in profile definition

<table>
<thead>
<tr>
<th>Path Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Extension</td>
<td></td>
<td>UML4SysML::Extension</td>
</tr>
<tr>
<td>Generalization</td>
<td></td>
<td>UML4SysML::Generalization</td>
</tr>
<tr>
<td>ProfileApplication</td>
<td></td>
<td>UML4SysML::ProfileApplication</td>
</tr>
<tr>
<td>MetamodelReference</td>
<td></td>
<td>UML4SysML::PackageImport; UML4SysML::ElementImport</td>
</tr>
<tr>
<td>Unidirectional Association</td>
<td></td>
<td>UML4SysML::Association</td>
</tr>
</tbody>
</table>

**NOTE:** In the above table, boolean properties can alternatively be displayed as BooleanPropertyName=[True|False].
17.2.1.1 Extension

In Figure 17.1, a simple stereotype *Clock* is defined to be applicable at will (dynamically) to instances of the metaclass *Class* and describes a clock software component for an embedded software system. It has description of the operating system version supported, an indication of whether it is compliant to the POSIX operating system standard and a reference to the operation that starts the clock.

![Figure 17.1 - Defining a stereotype](image-url)

*Figure 17.1 - Defining a stereotype*
<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>StereotypeNote</td>
<td>«stereotypeName» PropertyName=ValueString, ValueString BooleanPropertyName</td>
<td>UML4SysML::Element</td>
</tr>
<tr>
<td>StereotypeNote</td>
<td>«stereotypeName» PropertyName=ValueString, ValueString BooleanPropertyName</td>
<td>UML4SysML::Element</td>
</tr>
<tr>
<td>StereotypeInNode</td>
<td>«stereotypeName» PropertyName=ValueString, ValueString BooleanPropertyName</td>
<td>UML4SysML::Element</td>
</tr>
<tr>
<td>StereotypeInCompartment</td>
<td>«stereotypeName» (PropertyName=ValueString; BooleanPropertyName) ElementName</td>
<td>UML4SysML::Element</td>
</tr>
<tr>
<td>StereotypeOnEdge</td>
<td>«stereotypeName» ((PropertyName=ValueString; BooleanPropertyName) ElementName</td>
<td>UML4SysML::Element</td>
</tr>
</tbody>
</table>
Table 17.3 - Notations for Stereotype Use

<table>
<thead>
<tr>
<th>Node Name</th>
<th>Concrete Syntax</th>
<th>Abstract Syntax Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Stereotype</td>
<td>«stereotypeName»</td>
<td>UML4SysML::Element</td>
</tr>
<tr>
<td>Compartment</td>
<td>NodeName&lt;br&gt;PropertyName=ValueString&lt;br&gt;MultiPropertyName=ValueString,&lt;br&gt;ValueString&lt;br&gt;BooleanPropertyName</td>
<td></td>
</tr>
</tbody>
</table>

17.2.2.1 StereotypeInNode

Figure 17.2 shows how the stereotype *Clock*, as defined in Figure 17.1, is applied to a class called *AlarmClock*.

17.2.2.2 StereotypeInComment

When, two stereotypes, *Clock* and *Creator*, are applied to the same model element, as is shown in Figure 17.3, the attribute values of each of the applied stereotypes can be shown in a comment symbol attached to the model element.

Figure 17.2 - Using a stereotype

Figure 17.3 - Using stereotypes and showing values
17.2.2.3 StereotypeInCompartment

Finally, the compartment form is shown.

<table>
<thead>
<tr>
<th>AlarmClock</th>
</tr>
</thead>
<tbody>
<tr>
<td>Start()</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>«clock»</td>
</tr>
<tr>
<td>OSVersion=&quot;3.4&quot;</td>
</tr>
<tr>
<td>startOperation=Start</td>
</tr>
<tr>
<td>POSIXCompliant=True</td>
</tr>
</tbody>
</table>

Figure 17.4 - Other notational forms for showing values

In this case, AlarmClock is valid for OS version 3.4, is POSIX-compliant and has a starting operation called Start. Note that multiple stereotypes can be shown using multiple compartments.

17.3 UML Extensions

None.

17.4 Usage Examples

17.4.1 Defining a Profile

Figure 17.5 - Definition of a profile
In this example, the modeler has created a new profile called SE Toolkit, which imports the SysML profile, so that it can build upon the stereotypes it contains. The set of metaclasses available to users of the SysML profile is identified by a reference to a metamodel, in this case a subset of UML specific to SysML. The SE Toolkit can extend those metaclasses from UML that the SysML profile references.

### 17.4.2 Adding Stereotypes to a Profile

In SE Toolkit, both the mechanisms for adding new stereotypes are used. The first, exemplified by configurationItem, is called an extension, shown by a line with a filled triangle; this relates a stereotype to a reference (called base) class or classes, in this case NamedElement and DirectedRelationship from UML and adds new properties that every NamedElement or DirectedRelationship stereotyped by configurationItem must have. NamedElement and DirectedRelationship are abstract classes in UML so it is their subclasses that can have the stereotype applied. The second mechanism is demonstrated by the system and context stereotypes which are sub-stereotypes of an existing SysML stereotype, Block; sub-stereotypes inherit any properties of their super-stereotype (in this case none) and also extend the same base class or classes. Note that TypedElements whose type is extended by «system» do not display the «system» stereotype; this also applies to InstanceSpecifications. Any notational conventions of this have to be explicitly specified in a diagram extension.

There is also an example of how stereotypes (in this case FunctionalRequirement) can have unidirectional associations to metaclasses in the reference metamodel (in this case Behavior).
17.4.3 Defining a Model Library that Uses a Profile

The model library SI Value Types imports a model library called SI Definitions, so it can use model elements from them in its own definition. It defines value types having specific units which can be used when property values are measured in SI units. SI Definitions is a separately published model library, containing definitions of standard SI units and dimensions such as shown in Annex C, Section C.4. A further model library, Physical, imports SI Value Types so it can define properties that have those types. One model element, PhysicalObject, is shown, a block that can be used as a supertype for an physical object.

Figure 17.7 - Two model libraries

17.4.4 Guidance on Whether to Use a Stereotype or Class

This section provides guidance on when to use stereotypes. Stereotypes can be applied to any model element. Stereotyping a model element allows the model element to be identified with the «guillemet» notation. In addition, the stereotyped model element can have stereotype properties, and the stereotype can specify constraints on the model element.

The modeler must decide when to create a stereotype of a class versus when to specialize (subclass) the class. One reason is to be able to identify the class with the «guillemet» notation. In addition, the stereotype properties are different from properties of classes. Stereotype properties represent properties of the class that are not instantiated and therefore do not have a unique value for each instance of the class, although a class thus stereotyped can have a separate value for the property.

SE Toolkit::functionalRequirement, which extends Class through its superstereotype, Requirement, is an example where a stereotype is appropriate because every modeling element stereotyped by SE Toolkit::functionalRequirement has a reference to another modeling element. In another example, SE Toolkit::configurationItem defined above, which applies to classes amongst other concepts, is a stereotype because its properties characterize the author, version, and last changed.
date of the modeling element themselves. One test of this is whether the new properties are inheritable; in this case author, version, and last-changed date are not, because it is only those classes under configuration control that need the properties. To summarize, in the following circumstances a stereotype is appropriate:

- Where the model concept to be extended is not a class or class-based.
- Where the extensions include properties that reference other model elements.
- Where the extensions include properties that describe modeling data, not system data.

An example where a class is more appropriate is PhysicalObject from Figure 17.7. In this case, the properties density and volume, and the component numbers, have distinct values for each system element described by the class, and are inherited by every subclass of PhysicalObject.

### 17.4.5 Using a Profile

![Figure 17.8 - A model with applied profile and imported model library](image)

The HSUVModel is a systems engineering model that needs to use stereotypes from SysML. It therefore needs to have the SysML profile applied to it. In order to use the predefined SI units, it also needs to import the SI Definitions model library. Having done this, elements in HSUVModel can be extended by SysML stereotypes and types like SIVolume can be used to type properties. Both the SI Definitions model library and HSUVModel have applied the profile strictly which means that only those metaclasses directly referenced by SysML can be used in those models.
17.4.6 Using a Stereotype

Figure 17.9 - Using two stereotypes on a model element

StoppingDistance has two stereotypes applied: functionalRequirement, which identifies it as a requirement that is satisfied by a function, and configurationItem, which allows it to have configuration management properties. The modeler has provided values for all the newly available properties; those for criticalRequirement are shown in a compartment in the node symbol for StoppingDistance; those for configurationItem are shown in a separate note.

17.4.7 Using a Model Library Element

Figure 17.10 - Using model library elements

Model library elements can be used just like any other model element of the same type. In this case, Shot is a specialization of PhysicalObject from the Physical model library. It adds a new property, circumference, of type SILength to measure the circumference of the (spherical) shot.
Part V - Annexes

This section contains the following non-normative annexes for this specification:

- A - Diagrams
- B - Sample Problem
- C - Non-normative Extensions
- D - Model Interchange
- E - Requirements Traceability
- F - Terms and Definitions
Annex A: Diagrams

(informative)

A.1 Overview

SysML diagrams contain diagram elements (mostly nodes connected by paths) that represent model elements in the SysML model, such as activities, blocks, and associations. The diagram elements are referred to as the concrete syntax.

The SysML diagram taxonomy is shown in Figure A.1. SysML reuses many of the major diagram types of UML. In some cases, the UML diagrams are strictly reused, such as use case, sequence, state machine, and package diagrams, whereas in other cases they are modified so that they are consistent with SysML extensions. For example, the block definition diagram and internal block diagram are similar to the UML class diagram and composite structure diagram respectively, but include extensions as described in Chapter 8, “Blocks.” Activity diagrams have also been modified via the activity extensions. Tabular representations, such as the allocation table, are used in SysML but are not considered part of the diagram taxonomy.

SysML does not use all of the UML diagram types such as the object diagram, communication diagram, interaction overview diagram, timing diagram, and deployment diagram. This is consistent with the approach that SysML represents a subset of UML. In the case of deployment diagrams, the deployment of software to hardware can be represented in the SysML internal block diagram. In the case of interaction overview and communication diagrams, it was felt that the SysML behavior diagrams provided adequate coverage for representing behavior without the need to include these diagram types. Two new diagram types have been added to SysML including the requirement diagram and the parametric diagram.

Figure A.1 - SysML Diagram Taxonomy
The requirement diagram is a new SysML diagram type. A requirement diagram provides a modeling construct for text-based requirements, and the relationship between requirements and other model elements that satisfy or verify them.

The parametric diagram is a new SysML diagram type that describes the constraints among the properties associated with blocks. This diagram is used to integrate behavior and structure models with engineering analysis models such as performance, reliability, and mass property models.

Although the taxonomy provides a logical organization for the various major kinds of diagrams, it does not preclude the careful mixing of different kinds of diagram types, as one might do when one combines structural and behavioral elements (e.g., showing a state machine nested inside a compartment of a block). However, it is critical that the types of diagram elements that can appear on a particular diagram kind be constrained and well-specified. The diagram elements tables in each chapter describe what symbols can appear in the diagram, but do not specify the different combinations of symbols that can be used.

The package diagram and the callout notation are two mechanisms that SysML provides for adding flexibility to represent a broad range of diagram elements on diagrams. The package diagram can be used quite flexibly to organize the model in packages and views. As such, a package diagram can include a wide array of packageable elements. The callout notation provides a mechanism for representing relationships between model elements that appear on different diagram kinds. In particular, they are used to represent allocations and requirements, such as the allocation of an activity to a block on a block definition diagram, or showing a part that satisfies a particular requirement on an internal block diagram. There are other mechanisms for representing this including the compartment notation that is generally described in Chapter 17, “Profiles & Model Libraries,” Chapter 16, “Requirements,” and Chapter 15, “Allocations” provide specific guidance on how these notations are used.

The model elements and corresponding concrete syntax that are represented in each of the ten SysML diagram kinds are described in the SysML chapters as indicated below.

- activity diagram - Activities chapter
- block definition diagram - Blocks chapter, Ports and Flows chapter
- internal block diagram - Blocks chapter, Ports and Flows chapter
- package diagram - Model Elements chapter
- parametric diagram - Constraint Blocks chapter
- requirement diagram - Requirements chapter
- state machine diagram - State Machines chapter
- sequence diagram - Interactions chapter
- use case diagram - Use Cases chapter
- Other (allocation tables) - Allocation Chapter
Each SysML diagram has a **frame**, with a **contents area**, a **heading**, and a **Diagram Description** see Figure A.2

**Figure A.2 - Diagram Frame**

The frame is a rectangle that is required for SysML diagrams (Note: the frame is optional in UML). The frame must designate a model element that is the default namespace for the model elements enclosed in the frame. A qualified name for the model element within the frame must be provided if it is not contained within default namespace associated with the frame. The following are some of the designated model elements associated with the different diagram kinds.

- activity diagram - activity
- block definition diagram - block, package, or constraint block
- internal block diagram - block or constraint block
- package diagram - package or model
- parametric diagram - block or constraint block
- requirement diagram - package or requirement
- sequence diagram - interaction
- state machine diagram - state machine
- use case diagram - package

The frame may include border elements associated with the designated model element, like ports for blocks, entry/exit points on statemachines, gates on interactions, parameters for activities, and constraint parameters for constraint blocks. The frame may sometimes be defined by the border of the diagram area provided by a tool.

The diagram contents area contains the graphical symbols. The diagram type and usage defines the type of primary graphical symbols that are supported, e.g., a block definition diagram is a diagram where the primary symbols in the contents area are blocks and association symbols along with their adornments.

The heading name is a string contained in a name tag (rectangle with cutoff corner) in the upper leftmost corner of the rectangle, with the following syntax:

```
<DiagramKind> [modelElementType] <modelElementName> [diagramName]
```
A space separates each of these entries. The diagramKind is bolded. The modelElementType and diagramName are in brackets. The heading name should always contain the diagram kind and model element name, and include the model element type and additional information to remove ambiguity. Ambiguity can occur if there is more than one model element type for a given diagram kind, or where there is more than one diagram for the same model element.

SysML diagrams kinds should have the following names or (abbreviations) as part of the heading:

- activity diagram (act)
- block definition diagram (bdd)
- internal block diagram (ibd)
- package diagram (pkg)
- parametric diagram (par)
- requirement diagram (req)
- sequence diagram (sd)
- state machine diagram (stm)
- use case diagram (uc)

The diagram description can be defined by a comment attached to a diagram frame as indicated in Figure A-2 that includes version, description, references to related information, a completeness field that describes the extent to which the modeler asserts the diagram is complete, and other-user defined fields. In addition, the diagram description may identify the view associated with the diagram, and the corresponding viewpoint that identifies the stakeholders and their concerns (refer to Model Elements chapter). The diagram description can be made more explicit by the tool implementation.

SysML also introduces the concept of a diagram usage. This represents a unique usage of a particular diagram type, such as a context diagram as a usage of a block definition diagram, internal block diagram, or use case diagram. The diagram usage can be identified in the header above the diagramKind as «diagramUsage». An example of a diagram usage extension is shown in Figure A.3. For this example, the header in Figure A.2 would replace diagram kind with “uc” and «diagramUsage» with «ContextDiagram». Applying a stereotype approach to specify a diagram usage can allow a tool implementation to check that the diagram constraints defined by the stereotype are satisfied.

NOTE: A diagram is not a metaclass in UML or SysML and therefore cannot be extended by a stereotype. However, the concept of extending a diagram for a particular diagram usage was considered to be of value. The stereotype notation is used to designate this concept without the formal semantics.
Figure A.3 - Diagram Usages

Some typical diagram usages may include:

- Activity diagram usage with swim lanes - SwimLane Diagram
- Block definition diagram usage for a block hierarchy - Block Hierarchy where block can be replaced by system, item, activity, etc.
- Use case diagram or internal block diagram to represent a Context Diagram

A.2 Guidelines

The following provides some general guidelines that apply to all diagram types.

- Decomposition of a model element can be represented by the rake symbol. This does not always mean decomposition in a formal sense, but rather a reference to a more elaborated diagram of the model element that includes the rake symbol. The rake on a model element may include the following:
  - activity diagram - call behavior actions that can refer to another activity diagram.
  - internal block diagram - parts that can refer to another internal block diagram.
  - package diagram - package that can refer to another package diagrams.
  - parametric diagram - constraint property that can refer to another parametric diagram
  - requirement diagram - requirement that can refer to another requirement diagram
  - sequence diagram - interaction fragments that can refer to another sequence diagram.
  - state machine diagram - state that can refer to another state machine diagram.
  - use case diagram - use case can that may be realized by other behavior diagrams (activity, state, interactions).

- The primary mechanism for linking a text label outside of a symbol to the symbol is through proximity of the label to its symbol. This applies to ports, item flows, pins, etc.
• Page connectors (on-page connectors and off-page connectors) can be used to reduce the clutter on diagrams, but should be used sparingly since they are equivalent to go-to’s in programming languages, and can lead to “spaghetti diagrams.” Whenever practical, elaborate the model element designated by the frame instead of using a page connector. A page connector is depicted as a circle with a label inside (often a letter). The circle is shown at both ends of a line break and means that the two line end connect at the circle.

• Diagram overlays are diagram elements that may be used on any diagram kind. An example of an overlay may be a geographic map to provide a spatial context for the symbols.

• SysML provides the capability to represent a document using the UML 2 standard stereotype «document» applied to the artifact model element. Properties of the artifact can capture information about the document. Use a «trace» abstraction to relate the document to model elements. The document can represent text that is contained in the related model elements.

• SysML diagrams including the enhancements described in this section is intended to conform to the Diagram Interchange Standard to facilitate exchange of diagram and layout information. A more formal BNF has been introduced in selected chapters to facilitate diagram interchange, which is referred to in the Language Formalism chapter.

• Tabular representation is an optional alternative notation that can be used in conjunction with the graphical symbols as long as the information is consistent with the underlying metamodel. Tabular representations are often used in systems engineering to represent detailed information such as interface definitions, requirements traceability, and allocation relationships between various types of model elements. They also can be convenient mechanisms to represent property values for selected properties, and basic relationships such as function and inputs/outputs in N2 charts. The UML superstructure contains a tabular representation of a sequence diagram in an interaction matrix (refer to Superstructure Annex with interaction matrix). The implementations of tabular representations are defined by the tool implementations and are not standardized in SysML at this time. However, tabular representations may be included in a frame with the heading designator «table» in bold.

• Graph and tree representations are also optional, alternative notations that can be used in conjunction with graphical symbols as long as the information is consistent with the underlying metamodel. These representations can be used for describing complex series of relationships. One example is the browser window in many tools that depicts a hierarchical view of the model. The implementations of graphs and trees are defined by the tool implementations and are not standardized in SysML at this time.
Annex B: Sample Problem

(informative)

B.1 Purpose

The purpose of this annex is to illustrate how SysML can support the specification, analysis, and design of a system using some of the basic features of the language.

B.2 Scope

The scope of this example is to provide at least one diagram for each SysML diagram type. The intent is to select simplified fragments of the problem to illustrate how the diagrams can be applied, and also demonstrate some of the possible inter-relationships among the model elements in the different diagrams. The sample problem does not highlight all of the features of the language. The reader should refer to the individual chapters for more detailed features of the language. The diagrams selected for representing a particular aspect of the model, and the ordering of the diagrams are intended to be representative of applying a typical systems engineering process, but this will vary depending on the specific process and methodology that is used.

B.3 Problem Summary

The sample problem describes the use of SysML as it applies to the development of an automobile, in particular a Hybrid gas/electric powered Sport Utility Vehicle (SUV). This problem is interesting in that it has inherently conflicting requirements, viz. desire for fuel efficiency, but also desire for large cargo carrying capacity and off-road capability. Technical accuracy and the feasibility of the actual solution proposed were not high priorities. This sample problem focuses on design decisions surrounding the power subsystem of the hybrid SUV; the requirements, performance analyses, structure, and behavior.

This annex is structured to show each diagram in the context of how it might be used on such a example problem. The first section shows SysML diagrams as they might be used to establish the system context; establishing system boundaries, and top level use cases. The next section is provided to show how SysML diagrams can be used to analyze top level system behavior, using sequence diagrams and state machine diagrams. The following section focuses on use of SysML diagrams for capturing and deriving requirements, using diagrams and tables. A section is provided to illustrate how SysML is used to depict system structure, including block hierarchy and part relationships. The relationship of various system parameters, performance constraints, analyses, and timing diagrams are illustrated in the next section. A section is then dedicated to illustrating definition and depiction of interfaces and flows in a structural context. The final section focuses on detailed behavior modeling, functional and flow allocation.
B.4 Diagrams

B.4.1 Package Overview (Structure of the Sample Model)

B.4.1.1 Package Diagram - Applying the SysML Profile

As shown in Figure B.1, the HSUVModel is a package that represents the user model. The SysML Profile must be applied to this package in order to include stereotypes from the profile. The HSUVModel may also require model libraries, such as the SI Units Types model library. The model libraries must be imported into the user model as indicated.

Figure B.1 Establishing the User Model by Importing and Applying SysML Profile & Model Library (Package Diagram)

Figure B.2 details the specification of units and valueTypes employed in this sample problem.
Figure B.2 - Defining valueTypes and units to be Used in the Sample Problem

B.4.1.2 Package Diagram - Showing Package Structure of the Model

The package diagram (Figure B.3) shows the structure of the model used to evaluate the sample problem. Model elements are contained in packages, and relationships between packages (or specific model elements) are shown on this diagram. The relationship between the views (OperationalView and PerformanceView) and the rest of the user model are explicitly expressed using the «access» relationship. Note that the «view» models contain no model elements of their own, and that changes to the model in other packages are automatically updated in the Operational and Performance Views.
B.4.2 Setting the Context (Boundaries and Use Cases)

B.4.2.1 Internal Block Diagram - Setting Context

The term “context diagram,” in Figure B.4, refers to a user defined usage of an internal block diagram, which depicts some of the top level entities in the overall enterprise and their relationships. The diagram usage enables the modeler or methodologist to specify a unique usage of a SysML diagram type using the extension mechanism described in Annex A: Diagrams. The entities are conceptual in nature during the initial phase of development, but will be refined as part of the development process. The «system» and «external» stereotypes are user defined, not specified in SysML, but help the modeler to identify the system of interest relative to its environment. Each model element depicted may include a graphical icon to help convey its intended meaning. The spatial relationship of the entities on the diagram sometimes conveys understanding as well, although this is not specifically captured in the semantics. Also, a background such as a map can be included to provide additional context. The associations among the classes may represent abstract conceptual relationships among the entities, which would be refined in subsequent diagrams. Note how the relationships in this diagram are also reflected in the Automotive Domain Model Block Definition Diagram, Figure B.15.
Figure B.4 - Establishing the Context of the Hybrid SUV System using a User-Defined Context Diagram. (Internal Block Diagram) Completeness of Diagram Noted in Diagram Description

B.4.2.2 Use Case Diagram - Top Level Use Cases

The use case diagram for “Drive Vehicle” in Figure B.5 depicts the drive vehicle usage of the vehicle system. The subject (HybridSUV) and the actors (Driver, Registered Owner, Maintainer, Insurance Company, DMV) interact to realize the use case.
Figure B.5 - Establishing Top Level Use Cases for the Hybrid SUV (Use Case Diagram)

B.4.2.3 Use Case Diagram - Operational Use Cases

Goal-level Use Cases associated with “Operate the Vehicle” are depicted in the following diagram. These use cases help flesh out the specific kind of goals associated with driving and parking the vehicle. Maintenance, registration, and insurance of the vehicle would be covered under a separate set of goal-oriented use cases.
B.4.3 Elaborating Behavior (Sequence and State Machine Diagrams)

B.4.3.1 Sequence Diagram - Drive Black Box

Figure B.7 shows the interactions between driver and vehicle that are necessary for the “Drive the Vehicle” Use Case. This diagram represents the “DriveBlackBox” interaction, which is owned by the AutomotiveDomain block. “BlackBox” for the purpose of this example, refers to how the subject system (HybridSUV block) interacts only with outside elements, without revealing any interior detail.

The conditions for each alternative in the alt controlSpeed section are expressed in OCL, and relate to the states of the HybridSUV block, as shown in Figure B.8.
B.4.3.2 State Machine Diagram - HSUV Operational States

Figure B.8 depicts the operational states of the HSUV block, via a State Machine named “HSUVOperationalStates”. Note that this state machine was developed in conjunction with the DriveBlackBox interaction in Figure B.7. Also note that this state machine refines the requirement “PowerSourceManagement,” which will be elaborated in the requirements section of this sample problem. This diagram expresses only the nominal states. Exception states, like “acceleratorFailure,” are not expressed on this diagram.
B.4.3.3 Sequence Diagram - Start Vehicle Black Box & White Box

The Figure B.9 shows a “black box” interaction, but references “StartVehicleWhiteBox” (Figure B.10), which will decompose the lifelines within the context of the HybridSUV block.
Figure B.9 - Black Box Interaction for “StartVehicle,” referencing White Box Interaction (Sequence Diagram)

The lifelines on Figure B.10 (“whitebox” sequence diagram) need to come from the Power System decomposition. This now begins to consider parts contained in the HybridSUV block.

Figure B.10 - White Box Interaction for “StartVehicle” (Sequence Diagram)
B.4.4 Establishing Requirements (Requirements Diagrams and Tables)

B.4.4.1 Requirement Diagram - HSUV Requirement Hierarchy

The vehicle system specification contains many text-based requirements. A few requirements are highlighted in Figure B.11, including the requirement for the vehicle to pass emissions standards, which is expanded for illustration purposes. The containment (cross hair) relationship, for purposes of this example, refers to the practice of decomposing a complex requirement into simpler, single requirements.

Figure B.11 - Establishing HSUV Requirements Hierarchy (containment) - (Requirements Diagram)

B.4.4.2 Requirement Diagram - Derived Requirements

Figure B.12 shows a set of requirements derived from the lowest tier requirements in the HSUV specification. Derived requirements, for the purpose of this example, express the concepts of requirements in the HSUV specification in a manner that specifically relates them to the HSUV system. Various other model elements may be necessary to help develop a derived requirement, and these model element may be related by a «refinedBy» relationship. Note how PowerSourceManagement is “RefinedBy” the HSUVOperationalStates model (Figure B.8). Note also that rationale can be attached to the «deriveReqt» relationship. In this case, rationale is provided by a referenced document “Hybrid Design Guidance.”
B.4.4.3 Requirement Diagram - Acceleration Requirement Relationships

Figure B.13 focuses on the Acceleration requirement, and relates it to other requirements and model elements. The “refineReqt” relation, introduced in Figure B.12, shows how the Acceleration requirement is refined by a similarly named use case. The Power requirement is satisfied by the PowerSubsystem, and a Max Acceleration test case verifies the Acceleration requirement.
Figure B.13 - Acceleration Requirement Relationships (Requirements Diagram)

B.4.4.4 Table - Requirements Table

Figure B.14 contains two diagrams that show requirement containment (decomposition), and requirements derivation in tabular form. This is a more compact representation than the requirements diagrams shown previously.
### B.4.5 Breaking Down the Pieces (Block Definition Diagrams, Internal Block Diagrams)

#### B.4.5.1 Block Definition Diagram - Automotive Domain

Figure B.15 provides definition for the concepts previously shown in the context diagram. Note that the interactions DriveBlackBox and StartVehicleBlackBox (described in Section B.4.3, “Elaborating Behavior (Sequence and State Machine Diagrams),” on page 183) are depicted as owned by the AutomotiveDomain block.
B.4.5.2 Block Definition Diagram - Hybrid SUV

Figure B.16 defines components of the HybridSUV block. Note that the BrakePedal and WheelHubAssembly are used by, but not contained in, the PowerSubsystem block.

2 wheel drive is the only way to get acceptable fuel economy, even though it limits off-road capability.
**B.4.5.3 Internal Block Diagram - Hybrid SUV**

Figure B.17 shows how the top level model elements in the above diagram are connected together in the HybridSUV block.

![Internal Block Diagram of Hybrid SUV](image)

**Figure B.17 - Internal Structure of Hybrid SUV (Internal Block Diagram)**
B.4.5.4 Block Definition Diagram - Power Subsystem

Figure B.18 defines the next level of decomposition, namely the components of the PowerSubsystem block. Note how the of white diamond (composition) on FrontWheel and BrakePedal denotes the same “use-not-composition” kind of relationship previously shown in Figure B.16.

![Block Definition Diagram](image)

Figure B.18 - Defining Structure of Power Subsystem (Block Definition Diagram)

B.4.5.5 Internal Block Diagram for the “Power Subsystem”

Figure B.19 shows how the parts of the PowerSubsystem block, as defined in the diagram above, are used. It shows «connectors» between parts, «clientServerPorts», «flowPorts», «atomicFlowPorts», and «itemFlows». The dashed borders on FrontWheel and BrakePedal denote the “use-not-composition” relationship depicted elsewhere in Figure B.16 and Figure B.18. The dashed borders on Fuel denote a store, which keeps track of the amount and mass of fuel in the FuelTankAssy. This is also depicted in Figure B.18.
Figure B.19 - Internal Structure of the Power Subsystem (Internal Block Diagram)

Figure B.20 - Interfaces Typing StandardPorts Internal to the Power Subsystem (Block Definition Diagram)
Figure B.20 provides definition of the interfaces applied to Standard Ports associated with connector c1 in Figure B.19.

B.4.6 Defining Ports and Flows

B.4.6.1 Block Definition Diagram - ICE Interface

For purposes of example, the StandardPorts and related point-to-point connectors in Figure B.19 are being refined into a common bus architecture. For this example, FlowPorts have been used to model the bus architecture. Figure B.21 is an incomplete first step in the refinement of this bus architecture, as it begins to identify the flow specification for the InternalCombustionEngine, the Transmission, and the ElectricalPowerController.

Figure B.21 - Initially Defining Flow Specifications for the CAN Bus (Block Definition Diagram)

B.4.6.2 Internal Block Diagram - CANbus

Figure B.22 continues the refinement of this Controller Area Network (CAN) bus architecture using FlowPorts. The explicit structural allocation between the original connectors of Figure B.19 and this new bus architecture is shown in Figure B.36.
B.4.6.3 Block Definition Diagram - Fuel Flow Properties

The FlowPorts on the FuelTankAssembly and InternalCombustionEngine (as shown in Figure B.19) are defined in Figure B.23.
B.4.6.4 Parametric Diagram - Fuel Flow

Figure B.24 is a parametric diagram showing how fuel flowrate is related to FuelDemand and FuelPressure value properties.

![Parametric Diagram - Fuel Flow]

**Figure B.24 - Defining Fuel Flow Constraints (Parametric Diagram)**

B.4.6.5 Internal Block Diagram - Fuel Distribution

Figure B.25 shows how the connectors fuelDelivery and fdist on Figure B.19 have been expanded to include design detail. The fuelDelivery connector is actually two connectors, one carrying fuelSupply and the other carrying fuelReturn. The fdist connector inside the InternalCombustionEngine block has been expanded into the fuel regulator and fuel rail parts. These more detailed design elements are related to the original connectors using the allocation relationship. The Fuel store represents a quantity of fuel in the FuelTankAssy, which is drawn by the FuelPump for use in the engine, and is refreshed, to some degree, by fuel returning to the FuelTankAssy via the FuelReturnLine.
B.4.7 Analyze Performance (Constraint Diagrams, Timing Diagrams, Views)

B.4.7.1 Block Definition Diagram - Analysis Context

Figure B.26 defines the various model elements that will be used to conduct analysis in this example. It depicts each of the constraint blocks/equations that will be used for the analysis, and key relationships between them.
Figure B.26 - Defining Analyses for Hybrid SUV Engineering Development (Block Definition Diagram)

B.4.7.2 Package Diagram - Performance View Definition

Figure B.27 shows the user-defined Performance Viewpoint, and the elements that populate the HSUV specific PerformanceView. The PerformanceView itself may contain a number of diagrams depicting the elements it contains.
B.4.7.3 Parametric Diagram - Measures of Effectiveness

Measure of Effectiveness is a user defined stereotype. Figure B.28 shows how the overall cost effectiveness of the HSUV will be evaluated. It shows the particular measures of effectiveness for one particular alternative for the HSUV design, and can be reused to evaluate other alternatives.
B.4.7.4 Parametric Diagram - Economy

Since overall fuel economy is a key requirement on the HSUV design, this example applies significant detail in assessing it. Figure B.29 shows the constraint blocks and properties necessary to evaluate fuel economy.
Figure B.29 - Establishing Mathematical Relationships for Fuel Economy Calculations (Parametric Diagram)

B.4.7.5 Parametric Diagram - Dynamics

The StraightLineVehicleDynamics constraint block from Figure B.29 has been expanded in Figure B.30. ConstraintNotes are used, which identify each constraint using curly brackets {}. In addition, Rationale has been used to explain the meaning of each constraint maintained.
Figure B.30 - Straight Line Vehicle Dynamics Mathematical Model (Parametric Diagram)

The constraints and parameters in Figure B.30 are detailed in Figure B.31 in Block Definition Diagram format.
Figure B.31 - Defining Straight-Line Vehicle Dynamics Mathematical Constraints (Block Definition Diagram)

Note the use of valueTypes originally defined in Figure B.2.

B.4.7.6 (Non-Normative) Timing Diagram - 100hp Acceleration

Timing diagrams, while included in UML 2, are not directly supported by SysML. For illustration purposes, however, the interaction shown in Figure B.32 was generated based on the constraints and parameters of the StraightLineVehicleDynamics constraintBlock, as described in the Figure B.30. It assumes a constant 100hp at the drive wheels, 4000lb gross vehicle weight, and constant values for Cd and Cf.
Figure B.32 - Results of Maximum Acceleration Analysis (Timing Diagram)
B.4.8 Defining, Decomposing, and Allocating Activities

B.4.8.1 Activity Diagram - Acceleration (top level)

Figure B.33 shows the top level behavior of an activity representing acceleration of the HSUV. It is the intent of the systems engineer in this example to allocate this behavior to parts of the PowerSubsystem. It is quickly found, however, that the behavior as depicted cannot be allocated, and must be further decomposed.

Figure B.33 - Behavior Model for “Accelerate” Function (Activity Diagram)
B.4.8.2 Block Definition Diagram - Acceleration

Figure B.34 defines a decomposition of the activities and objectFlows from the activity diagram in Figure B.33.

![Block Definition Diagram - Acceleration](image)

Figure B.34 - Decomposition of “Accelerate” Function (Block Definition diagram)

B.4.8.3 Activity Diagram (EFFBD) - Acceleration (detail)

Figure B.35 shows the ProvidePower activity, using the decomposed activities and objectFlows from Figure B.34. It also uses AllocateActivityPartitions and an allocation callout to explicitly allocate activities and an object flow to parts in the PowerSubsystem block.

Note that the incoming and outgoing object flows for the ProvidePower activity have been decomposed. This was done to distinguish the flow of electrically generated mechanical power and gas generated mechanical power, and to provide further insight into the specific vehicle conditions being monitored.
B.4.8.4 Internal Block Diagram - Power Subsystem Behavioral and Flow Allocation

Figure B.36 depicts a subset of the PowerSubsystem, specifically showing the allocation relationships generated in Figure B.35.
B.4.8.5 Table - Acceleration Allocation

Figure B.37 shows the same allocation relationships shown in Figure B.36, but in a more compact tabular representation.

<table>
<thead>
<tr>
<th>type</th>
<th>name</th>
<th>end</th>
<th>relation</th>
<th>end</th>
<th>type</th>
<th>name</th>
</tr>
</thead>
<tbody>
<tr>
<td>activity</td>
<td>a1:ProportionPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>PowerControlUnit</td>
</tr>
<tr>
<td>activity</td>
<td>a2:ProvideGasPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>InternalCombustionEngine</td>
</tr>
<tr>
<td>activity</td>
<td>a3:ControlElectricPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>ElectricalPowerController</td>
</tr>
<tr>
<td>activity</td>
<td>a4:ProvideElectricPower</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>block</td>
<td>ElectricalMotorGenerator</td>
</tr>
<tr>
<td>objectNode</td>
<td>driveCurrent</td>
<td>from</td>
<td>allocate</td>
<td>to</td>
<td>itemFlow</td>
<td>i1:ElectricCurrent</td>
</tr>
</tbody>
</table>

Figure B.37 - Tabular Representation of Allocation from “Accelerate” Behavior Model to Power Subsystem (Table)
B.4.8.6 Internal Block Diagram: Property Specific Values - EPA Fuel Economy Test

Figure B.38 shows a particular Hybrid SUV (VIN number) satisfying the EPA fuel economy test. Serial numbers of specific relevant parts are indicated.

Figure B.38 - Special Case of Internal Block Diagram Showing Reference to Specific Properties (serial numbers)
Annex C: Non-normative Extensions

(informative)

This annex describes useful non-normative extensions to SysML that may be considered for standardization in future versions of the language.

Non-normative extensions consist of stereotypes and model libraries and are organized by major diagram type, consistent with how the main body of this specification is organized. Stereotypes in this section are specified using a tabular format, consistent with how non-normative stereotypes are specified in the UML 2 Superstructure specification. Model libraries are specified using the guidelines provided in the Profiles & Model Libraries chapter of this specification.

C.1 Activity Diagram Extensions

C.1.1 Overview

Two non-normative extensions to activities are described for:

- Streaming activities that accept inputs and/or provide outputs while they are active.


C.1.2 Stereotypes

Enhanced Functional Flow Block Diagrams (EFFBD) are a widely-used systems engineering diagram, also called a behavior diagram. Most of its functionality is a constrained use of UML activities, as described below. This extension does not address replication, resources, or kill branches. Kill branches can be translated to activities using interruptible regions and join specifications.

Table C.1 - Addition stereotypes for EFFBDs

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«effbd»</td>
<td>UML4SysML::Activity (or subtype of «nonStreaming» below)</td>
<td>N/A</td>
<td>See below.</td>
<td>Specifies that the activity conforms to the constraints necessary for EFFBD.</td>
</tr>
</tbody>
</table>

When the «effbd» stereotype is applied to an activity, its contents must conform to the following constraints:

1. (On Activity) Activities do not have partitions.
2. (On Activity) All decisions, merges, joins and forks are well-nested. In particular, each decision and merge are matched one-to-one, as are forks and joins, accounting for the output parameter sets acting as decisions, and input parameters and control acting as a join.
(On Action) All actions require exactly one control edge coming into them, and exactly one control edge coming out, except when using parameter sets.

(Execution constraint) All control is enabling.

(On ControlFlow) All control flows into an action target a pin on the action that has isControl = true.

(On ObjectNode) Ordering is first-in first out, ordering = FIFO.

(On ObjectNode) Object flow is never used for control, isControlType = false, except for pins of parameters in parameter sets.

(On Parameter) Parameters take and produce no more than one item, multiplicity.upper = 1.

(On Parameter) Output parameters produce exactly one value, multiplicity.lower = 1. The «optional» stereotype cannot be applied to parameters.

(On Parameter) Parameters cannot be streaming or exception.

(On ParameterSet) Parameter sets only apply to output parameters.

(On ParameterSet) Parameter sets only apply to control. Parameters in parameter sets must have pins with isControlType = true.

(On ParameterSet) Parameter sets have exactly one parameter, and it must not be shared with other parameter sets.

(On ParameterSet) If one output parameter is in a parameter set, then all output parameters of the behavior or operation must be in parameter sets.

(On ActivityEdge) Edges cannot have time constraints.

The following SysML stereotypes cannot be applied: «rate», «controlOperator», «noBuffer», «overwrite».

A second extension distinguishes activities based on whether they can accept inputs or provide outputs after they start and before they finish (streaming), or only accept inputs when they start and provide outputs when they are finished (nonstreaming). EFFBD activities are nonstreaming. Streaming activities are often terminated by other activities, while nonstreaming activities usually terminate themselves.

### Table C.2 - Streaming options for activities

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base Class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«streaming»</td>
<td>UML4SysML::Activity</td>
<td>N/A</td>
<td>The activity has at least one streaming parameter.</td>
<td>Used for activities that can accept inputs or provide outputs after they start and before they finish.</td>
</tr>
<tr>
<td>«nonStreaming»</td>
<td>UML4SysML::Activity</td>
<td>N/A</td>
<td>The activity has no streaming parameters.</td>
<td>Used for activities that accept inputs only when they start, and provide outputs only when they finish.</td>
</tr>
</tbody>
</table>

**C.1.3 Stereotype Examples**

Figure C.1 shows an example activity diagram with the «effbd» stereotype applied, translated from [Long. J., “Relationships between common graphical representations in system engineering,” 2002]. The stereotype applies the constraints specified in Section C.1.2, for example, that the data outputs on all functions are required and that queuing is FIF.
Figure C.1 - Example activity with «effbd» stereotype applied

Figure C.2 shows an example activity diagram with the «streaming» and «nonStreaming» stereotypes applied, adapted from [MathWorks, “Using Simulink,” 2004]. It is a numerical solution for the differential equation \( x'(t) = -2x(t) + u(t) \). Item types are omitted for brevity. The «streaming» and «nonStreaming» stereotypes indicate which subactivities take inputs and produce outputs while they are executing. They are simpler to use than the {stream} notation on streaming inputs and outputs.

The example assumes a default of zero for the lower input to Add, and that the entire activity is executed with clocked token flow, to ensure that actions with multiple inputs receive as many of them as possible before proceeding. See the article referenced in Section C.1.1.

Figure C.2 - Example activity with «streaming» and «nonStreaming» stereotypes applied to subactivities
C.2 Requirements Diagram Extensions

C.2.1 Overview

This section describe an example of a non-normative extension for a requirements profile.

C.2.2 Stereotypes

This section includes stereotypes for a simplified requirements taxonomy that is intended to be further adapted as required to support the particular needs of the application or organization. The requirements categories in this example include functional, interface, performance, physical requirements, and design constraints as shown in Table C.3. As shown in the table, each category is represented as a stereotype of the generic SysML «requirement». The table also includes a brief description of the category. The table does not include any stereotype properties or constraints, although they can be added as deemed appropriate for the application. For example, a constraint that could be applied to a functional requirement is that only SysML activities and operations can satisfy this category of requirement. Other examples of requirements categories may include operational, specialized requirements for reliability and maintainability, store requirements, activation, deactivation, and a high level category for stakeholder needs.

Some general guidance for applying a requirements profile is as follows:

• The categories should be adapted for the specific application or organization and reflected in the table. This includes agreement on the categories and their associated descriptions, stereotype properties and constraints. Additional categories can be added by further subclassing the categories in the table below, or adding additional categories at the pier level of these categories.

• The default requirement category should be the generic «requirement».

• Apply the more specialized requirement stereotype (functional, interface, performance, physical, design constraint) as applicable and ensure consistency with the description, stereotype properties, and constraints.

• A specific text requirement can include the application of more than one requirement category, in which case, each stereotype should be shown in guillemets.

Table C.3 - Additional Requirement Stereotypes

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base Class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«extendedRequirement»</td>
<td>«requirement»</td>
<td>source: String</td>
<td>N/A</td>
<td>A mix-in stereotype that contains generally useful attributes for requirements</td>
</tr>
<tr>
<td></td>
<td></td>
<td>risk: RiskKind</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>verifyMethod: VerifyMethodKind</td>
<td></td>
<td></td>
</tr>
<tr>
<td>«functionalRequirement»</td>
<td>«extendedrequirement»</td>
<td>N/A</td>
<td>satisfied by an operation or behavior</td>
<td>Requirement that specifies an operation or behavior that a system, or part of a system, must perform.</td>
</tr>
<tr>
<td>«interfaceRequirement»</td>
<td>«extendedrequirement»</td>
<td>N/A</td>
<td>satisfied by a port, connector, item flow, and/or constraint property</td>
<td>Requirement that specifies the ports for connecting systems and system parts and the optionally may include the item flows across the connector and/or Interface constraints.</td>
</tr>
</tbody>
</table>
Table C.3 - Additional Requirement Stereotypes

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base Class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«performanceRequirement»</td>
<td>«extendedrequirement»</td>
<td>N/A</td>
<td>satisfied by a value property</td>
<td>Requirement that quantitatively measures the extent to which a system, or a system part, satisfies a required capability or condition.</td>
</tr>
<tr>
<td>«physicalRequirement»</td>
<td>«extendedrequirement»</td>
<td>N/A</td>
<td>satisfied by a structural element.</td>
<td>Requirement that specifies physical characteristics and/or physical constraints of the system, or a system part.</td>
</tr>
<tr>
<td>«designConstraint»</td>
<td>«extendedrequirement»</td>
<td>N/A</td>
<td>satisfied by a block or part</td>
<td>Requirement that specifies a constraint on the implementation of the system or system part, such as the system must use a commercial off the shelf component.</td>
</tr>
</tbody>
</table>

Table C.4 provides the definition of the non-normative enumerations that are used to type properties of “extendedRequirement” stereotype of Figure C.3.

Table C.4 - Requirement property enumeration types

<table>
<thead>
<tr>
<th>Enumeration</th>
<th>Enumeration Literals</th>
<th>Example Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>RiskKind</td>
<td>High</td>
<td>High indicates an unacceptable level of risk</td>
</tr>
<tr>
<td></td>
<td>Medium</td>
<td>Medium indicates an acceptable level of risk</td>
</tr>
<tr>
<td></td>
<td>Low</td>
<td>Low indicates a minimal level of risk or no risk</td>
</tr>
<tr>
<td>VerificationMethodKind</td>
<td>Analysis</td>
<td>Analysis indicates that verification will be performed by technical evaluation using mathematical representations, charts, graphs, circuit diagrams, data reduction, or representative data. Analysis also includes the verification of requirements under conditions, which are simulated or modeled; where the results are derived from the analysis of the results produced by the model.</td>
</tr>
<tr>
<td></td>
<td>Demonstration</td>
<td>Demonstration indicates that verification will be performed by operation, movement or adjustment of the item under specific conditions to perform the design functions without recording of quantitative data. Demonstration is typically considered the least restrictive of the verification types.</td>
</tr>
<tr>
<td></td>
<td>Inspection</td>
<td>Inspection indicates that verification will be performed by examination of the item, reviewing descriptive documentation, and comparing the appropriate characteristics with a predetermined standard to determine conformance to requirements without the use of special laboratory equipment or procedures.</td>
</tr>
<tr>
<td></td>
<td>Test</td>
<td>Test indicates that verification will be performed through systematic exercising of the applicable item under appropriate conditions with instrumentation to measure required parameters and the collection, analysis, and evaluation of quantitative data to show that measured parameters equal or exceed specified requirements.</td>
</tr>
</tbody>
</table>

C.2.3 Stereotype Examples

Figure C.3 shows the use of several sub-types of requirements extended to include the properties risk:RiskKind, verifyMethod:VerificationMethodKind, and a text attribute source:String, used to capture the source of the requirement.
C.3 Parametric Diagram Extensions for Trade Studies

C.3.1 Overview

This section describes a non-normative extension of a parametric diagram (refer to the Constraint Blocks chapter) to support trade studies and analysis, which are an essential aspect of any systems engineering effort. In particular, a trade study is used to evaluate a set of alternatives based on a defined set of criteria. The criteria may have a weighting to reflect their relative importance. An objective function (aka optimization or cost function) can be used to represent the weighted criteria and determine the overall value of each alternative. The objective function can be more complex than a simple linear weighting of the criteria and can include probability distribution functions and utility functions associated with each criteria. However, for this example, we will assume the simpler case.
A measure of effectiveness (moe) represents a parameter whose value is critical for achieving the desired mission cost effectiveness. It will also be assumed that the overall mission cost effectiveness can be determined by applying an objective function to a set of criteria, each of which is represented by a measures of effectiveness.

This section includes stereotypes for an objective function and a measure of effectiveness. The objective function is a stereotype of a ConstraintBlock and the measure of effectiveness is a stereotype of a block property.

### C.3.2 Stereotypes.

#### Table C.5 - Stereotypes for Measures of Effectiveness

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base Class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«objectiveFunction»</td>
<td>«ConstraintBlock» or «ConstraintProperty»</td>
<td>N/A</td>
<td>N/A</td>
<td>An objective function (aka optimization or cost function) is used to determine the overall value of an alternative in terms of weighted criteria and/or moe's.</td>
</tr>
<tr>
<td>«moe»</td>
<td>UML4SysML::Property</td>
<td>N/A</td>
<td>N/A</td>
<td>A measure of effectiveness (moe) represents a parameter whose value is critical for achieving the desired mission cost effectiveness.</td>
</tr>
</tbody>
</table>

#### C.3.3 Stereotype Examples

In this example, operational availability, mission response time, and security effectiveness each represent moe’s along with life cycle cost. The overall cost effectiveness for each alternative may be defined by an objective function that represents a weighted sum of their moe values. For each moe, there is a separate parametric model to estimate the value of operational availability, mission response time, security effectiveness and life cycle cost to determine an overall cost effectiveness for each alternative. It is assumed that the moe’s refer to the values for system alternative j (sj).
C.4 Model Library for Dimensions and Units

The dimensions and units in this section are a subset of units defined by the International System of Units (SI) as defined in NIST Special Publication 330 (available from the NIST Reference on Constants, Units and Uncertainty at http://physics.nist.gov/cuu/Units/units.html).

Figure C.4 - SI Definitions model library

Figure C.5 - SI Base Units
Figure C.6 - SI Derived Units Expressed In Base Units
Figure C.7 - SI Derived Units With Special Names

C.5 Distribution Extensions

C.5.1 Overview

This section describes a non-normative extension to provide a candidate set of distributions (see “DistributedProperty” on page 46). It consists of a profile containing stereotypes that can be used to specify distributions for properties of blocks.
C.5.2 Stereotypes

Package Distributions

Figure C.8 - Basic distribution stereotypes

Table C.6 - Distribution Stereotypes

<table>
<thead>
<tr>
<th>Stereotype</th>
<th>Base Class</th>
<th>Properties</th>
<th>Constraints</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>«BasicInterval»</td>
<td>«DistributedProperty»</td>
<td>min: Real, max: Real</td>
<td>N/A</td>
<td>Basic Interval distribution - value between min and max inclusive</td>
</tr>
<tr>
<td>«Interval»</td>
<td>«BasicInterval»</td>
<td>N/A</td>
<td>N/A</td>
<td>Interval distribution - unknown probability between min and max</td>
</tr>
<tr>
<td>«Uniform»</td>
<td>«BasicInterval»</td>
<td>N/A</td>
<td>N/A</td>
<td>Uniform distribution - constant probability between min and max</td>
</tr>
<tr>
<td>«Normal»</td>
<td>«DistributedProperty»</td>
<td>mean: Real, standardDeviation: Real</td>
<td>N/A</td>
<td>Normal distribution - constant probability between min and max</td>
</tr>
</tbody>
</table>

C.5.3 Usage Example

Figure C.9 shows a simple example of using distributions; the force of the Cannon is specified using a Normal distribution with parameters mean and standard Deviation. Whereas the use of a Normal distribution can be inferred from the names of its parameters, an Interval distribution shares parameters with a Uniform distribution, hence the stereotype keyword «interval» is used to distinguish it.
Figure C.9 - Distribution Example

```plaintext
bdd [block] FiringRange

«block»
Cannon
{mean=100.0, standardDeviation=1.0} force: Newton

«block»
Shot
«interval»{min=101.0, max=105.0} volume: CubicMeter
density: KilogramPerCubicMeter
acceleration: MeterPerSquareSecond
```
Annex D: Model Interchange

(informative)

D.1 Overview

This annex describes several methods for exchanging SysML models between tools. The first method discussed is XML Metadata Interchange (XMI), which is the preferred method for exchanging models between UML-based tools. The second approach describes the use of ISO 10303-233 Application Protocol: Systems engineering and design (AP233), which is one of the series of STEP (Standard for the Exchange of Product Model Data) neutral data schemas for representing engineering data. Other model interchange approaches are possible, but the ones described in this annex are expected to be the primary ones supported by SysML.

D.2 Context for Model Interchange

Developing today’s complex systems typically requires engineering teams that are distributed in time and space and that are often composed of many companies, each with their own culture, methods and tools. Effective collaboration requires agreement on, and a thorough understanding of, the various work assignments and resulting artifacts.

Many of these artifacts pertain to shared engineering data (e.g., requirements, system structural and behavioral models, verification & validation) that transcend the entire life cycle of the system of interest and are the basis for important systems engineering considerations and decisions. So it is critical that the system information contained in these artifacts and information models be accurately captured and readable by all appropriate team members in a timely manner.

Today, this information resides in an array of tools where each is only concerned with a portion of systems engineering data and can’t share its data with other tools because they only understand their own native schema. To mitigate this situation, collaborating organizations are usually forced to either adopt a common set of tools or develop a unique, bidirectional interface between many of the tools that each organization uses. This can be an expensive and untimely approach to data exchange between team members. So there is a need to define standardized approaches for model interchange between the different data schemas in use.

D.3 XMI Serialization of SysML

UML 2.0 is formally defined using the OMG Meta Object Facility (MOF). MOF can be considered a language for specifying modeling languages. The OMG XML Metadata Interchange (XMI) 2.1 standard specifies an XML-based interchange format for any language modeled using MOF. This results in a standard, convenient format for serializing UML user models as XMI files for interchange between UML tools. The XMI specification also includes rules for generating an XML Schema that can be used for basic validation of the structure of those UML user model XMI files.

The UML language includes an extension mechanism called UML Profiles. UML Profiles are themselves defined as UML models (MOF is not used). However, their intent is to specify extensions to the UML language semantics in much the same way one could extend the UML language by adding to the MOF definition of UML. As UML Profiles are valid UML models, XMI does provide a mechanism for exchanging the UML Profiles between UML tools. However, as they are extensions to concepts defined in the UML language itself, the definition of a UML Profile refers to the UML language definitions. An XMI 2.1 representation of the SysML profile (i.e., the UML Profile for SysML), as well as XMI 2.1 representations of Model Libraries defined by SysML, are provided as support documents to this specification. As with UML, XMI provides a
convenient serialized format for model interchange between SysML tools and basic validation of those files using an XML Schema as well.

D.4 Overview of ISO 10303-233 STEP AP233

AP233 is not finalized at this time, so this section reflects the background and current status of the AP233 work.

AP233 is a neutral data schema for representing systems engineering data. AP233 is being standardized under the ISO TC-184 (Technical Committee on Industrial Automation Systems and Integration), SC4 (Subcommittee on Industrial Data Standards), and is part of the larger STEP effort, which provides standardized models and infrastructure for the exchange of product model data.

D.4.1 Scope of ISO 10303-233 STEP AP233

AP233 will include support for describing:

- requirement
- functional
- structure
  - physical structure & allocation
  - configuration & traceability
  - project & data management


D.4.2 ISO 10303-233 STEP AP233 Development Approach & Status

AP233 and several other STEP application protocols are being built using a modular architecture. This enables the same information model to be reused across disciplines and life cycle stages. In the STEP Modular Architecture these reusable information models are called application modules, or more informally simply modules. AP233 will consist of a number of modules that together will satisfy the scope of the requirements stated above. Support for several of systems engineering viewpoints within the scope of AP233 already exist as the result of the development of other application protocols and will simply be reused in AP233. When existing STEP modules do not provide needed capabilities, new modules are being defined as part of AP233 development. Since AP233 is part of STEP, it is easy to relate systems engineering data to that of other engineering disciplines over the lifecycle of a system and to related product models.

Figure D-1 provides an overview of the modules planned to satisfy the scope of AP233 requirements and also shows the current status of each.
D.4.3  STEP Architecture, Modeling & Model Interchange Mechanisms

A good understanding of the STEP architecture and its components are required to understand how SysML models will be interchanged using AP233. This section provides an overview of the key elements of STEP that pertain to model interchange.

D.4.3.1 Modular Architecture

The scope of STEP is very large. While a number of STEP modules and application protocols have been developed (e.g., product data management, geometry, structural, electrical, and other engineering analysis support) and in use for several years, other area such as AP233 are still being defined and developed.

AP233 and several other STEP application protocols are being built using a modular architecture. This enables the same information model to be reused across disciplines and life cycle stages. In the STEP Modular Architecture these reusable information models are called application modules, or more informally simply modules.


D.4.3.2 The Modeling Language for ISO 10303-233 STEP AP233

AP233, like all STEP application protocols, is defined using the EXPRESS modeling language (see ISO 10303-11 Description method: The EXPRESS language reference manual). EXPRESS is a precise text-based information modeling language with a related graphical representation called EXPRESS-G.

An example of the text-based format follows:
SCHEMA people;
    TYPE year = integer;
    END_TYPE;
    TYPE person_or_organization = SELECT ( person, organization );
    END_TYPE;
    ENTITY organization;
        name : STRING;
    ENDENTITY;
    ENTITY building;
        address : STRING;
        owner : person_or_organization;
    ENDENTITY;
    ENTITY person
        ABSTRACT SUPERTYPE;
        spouse : OPTIONAL person;
        name : STRING;
        birthyear : year;
        biological_parents : SET[2:2] of person;
        parents : SET[2:?] of person;
    ENDENTITY;
    ENTITY man
        SUBTYPE OF ( person );
        sister : SET[0:?] of woman;
    ENDENTITY;
    ENTITY woman
        SUBTYPE OF ( person );
        brother : SET[0:?] of man;
    ENDENTITY;
END_SCHEMA;

An overview of an XML Document Type Definition for the EXPRESS language is available at http://stepmod.sourceforge.net/express_model_spec/. Note however, that the powerful expression language for constraint writing is not addressed by that DTD. EXPRESS expressions are similar in nature to OCL expressions and the two languages have similar expressiveness.

Work is underway to produce and standardize a MOF-based EXPRESS metamodel and EXPRESS/UML mappings. Documentation related to those efforts is available at the exff (Engineering eXchange For Free) web site (http://www.exff.org/express_uml/index.html). Eventually these efforts should allow a formal SysML/AP233 relationship to be standardized within the OMG.

An early draft of one mapping of ISO EXPRESS to UML/XMI is available as an OMG document at http://www.omg.org/cgi-bin/doc?liaison/2003-07-01. Please note that this specification is based on EXPRESS Edition 1, UML 1.4, MOF 1.4 and XMI 1.2.

D.4.3.3 Model Interchange Mechanisms

As part of the STEP series of EXPRESS-based information model, a series of implementation methods are also standardized:

- ISO 10303-21 (Part 21), clear text encoding of the exchange structure
- ISO 10303-22 (Part 22), standard data access interface (SDAI) specification
• ISO 10303-25 (Part 25), EXPRESS to OMG XMI binding
• ISO 10303-28 (Part 28), XML representation of EXPRESS schemas and data

A conforming STEP implementation is the combination of a STEP application protocol and one or more of the implementation methods.

SDAI specifies a standard programming interface for access to EXPRESS-based data. SDAI allows the implementors to refer to product data in terms of its conceptual EXPRESS definitions, regardless of the underlying data structure or storage technology. Bindings of the SDAI to C++ (ISO 10303-23), C (ISO 10303-24), Java (ISO 10303-27) provide standardized APIs for accessing EXPRESS-based data.

**D.4.4 ISO 10303-233 STEP AP233 - SysML Alignment & Mapping Model**

The requirements for AP233 and SysML have been largely aligned by the OMG and the ISO teams working together and in close cooperation with the INCOSE Model Driven System Design working group. However, there might be differences in breadth and scope of AP233 and SysML resulting from the different development life cycles of both activities and the different nature of the modeling frameworks used to define SysML and AP233. To avoid semantic issues in exchanging data between SysML and AP233, a neutral mapping model of systems engineering concepts will be defined. Thus the mappings between the mapping model and SysML metamodel and the mapping model and AP233 metamodel can be maintained independently. The neutral mapping model will also help to clarify the semantics of the data elements. This approach is illustrated in Figure D-2.

As AP233 and SysML are defined in different modeling frameworks, the AP233 metamodel will be converted to UML to ease the mapping. OMG has started a standardization activity to capture EXPRESS semantics in UML, but a custom mapping will be used until the UML profile for EXPRESS has been adopted. The mapping model will be expressed as a plain MOF model. The mapping model will be defined based on the concepts used and implemented for AP233 and SysML. Another important input is the conceptual systems engineering model maintained by the INCOSE Model Driven System Design Working group. Since development of the mapping model and SysML and AP233 mappings to it is an ongoing maintenance activity, these specifications will be maintained separately and updates will be posted on the SE DSIG web site.

The mapping model can be used as the basis for the model-exchange methods discussed in the next section and also for the development of conceptual level APIs, which should ease the usage of AP233 and generation of common test cases for SysML and AP233.
D.4.5 Generic Procedures for SysML and ISO 10303-233 STEP AP233 Model Interchange

D.4.5.1 File-based Exchange

Industrial-strength STEP implementations are typically file exchange-based systems integration processes. As OMG has standardized XMI as its model serialization format, one obvious approach is to use the STEP XML-based file exchange capability (Part 28) by simply translating the model contained in an XMI file into a model based on the AP233 XML Schema. This approach encourages systems integrators and SysML tool vendors to develop interoperable SysML-AP233 exchange capabilities. It also provides SysML tool vendors with a means to directly export AP233 XML files.

D.4.5.2 API-Driven Model Interchange

Model interchange can be simplified by the use of high-level application program interfaces (APIs). At the moment, standardized APIs for SysML- or AP233-specific models are not available, but work is underway in the industry to provide implementations of such APIs. Ideally, application level developers can use the same APIs to access backend XML models serialized in either SysML XMI or AP233 XML format, depending on customer needs. When combined, standardized XML serialization formats and high-level APIs will provide a very convenient and interoperable way for SysML tool vendors and systems integrators to exchange SysML and AP233 models. These standardized capabilities will also provide the foundation needed for building a set of Systems Engineering Web Services.
Annex E: Requirements Traceability

The OMG SysML requirements traceability matrix traces this specification to the original source requirements in the UML for Systems Engineering RFP (ad/2003-03-41). The traceability matrix is included by reference in a separate document (ptc/2007-03-09).
Annex F: Terms and Definitions

(informative)

The SysML glossary is included as a support document ad/2006-03-04 to this specification. The terms and definitions are referred to in the SysML specification and are derived from multiple sources including the UML 2.1 Superstructure specification and the UML for Systems Engineering RFP (ad/2003-03-41).
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